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**RCSL No:** 44-RT1171

**Edition:** 06.11.75

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**Title:**

BTU701  
Bus Termination Unit  
Technical Manual

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**Keywords:**

RC3600, Nova Bus, Terminator.

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**Abstract:**

This paper contains information about the bus termination in RC3600 systems.

(14 printed pages)

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## INTRODUCTION

The RC 3600 Bus System interconnects the CPU and the controllers. To prevent reflections on the bus lines the bus must be terminated at both ends. One end of the bus is connected to the CPU, and this end is terminated on the CPU board. The other end of the bus must be terminated with the Bus Termination Unit BTU 701.

## RC 3600 BUS SYSTEM

The RC 3600 Bus System is shown in Figure 1. The bus starts in the CPU and goes to the first CHS 701 controller chassis and then to the next, if any.

Each chassis has a Bus In socket and a Bus Out socket. The bus goes from the CPU to Bus In Plug on the first chassis on the bus. Inside the cabinet the Bus In Plug is connected to the Bus Out Plug. From the Bus Out Plug on the first chassis the bus goes to the Bus In Plug on the second chassis, and so on. The Bus Termination Unit is connected to the Bus Out Plug on the last chassis.

# RC 3600 Bus System

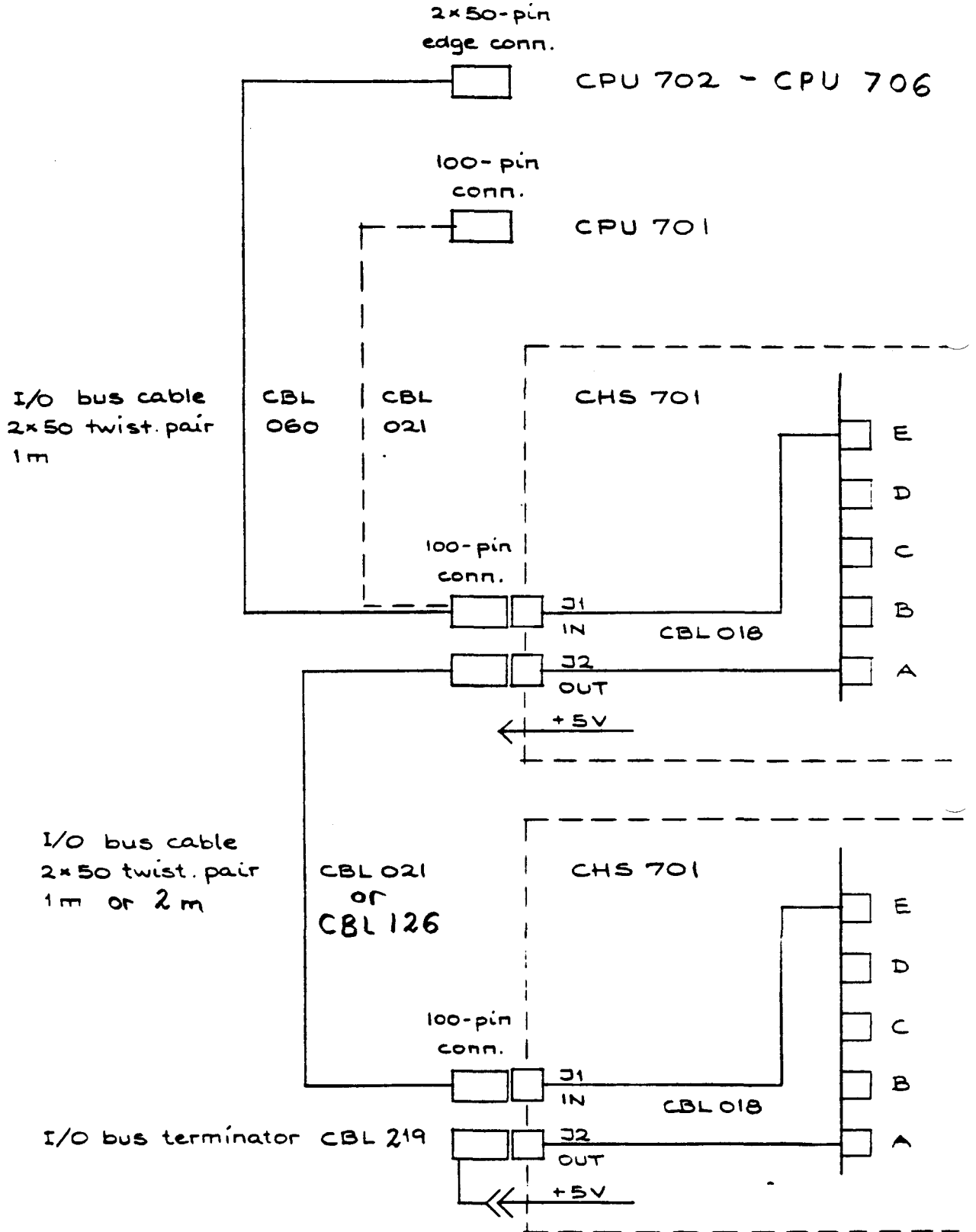


figure 1

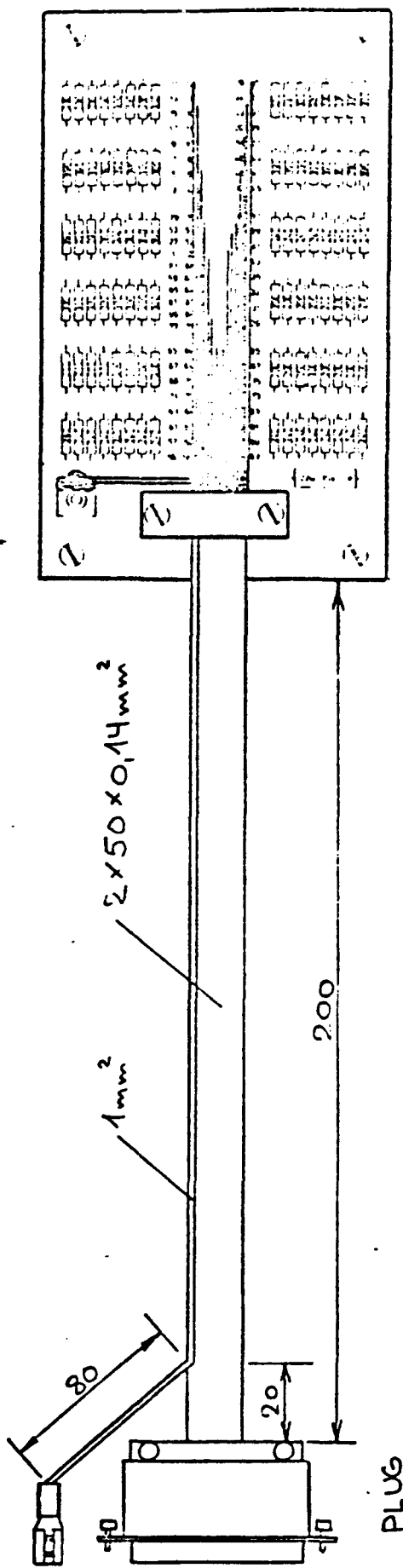
RC 3600 BUS TERMINATION

The RC 3600 Bus is terminated with Bus Termination Unit BTU 701.

The BTU 701 is composed of a CBL 219 cable and two CHS chassis parts.

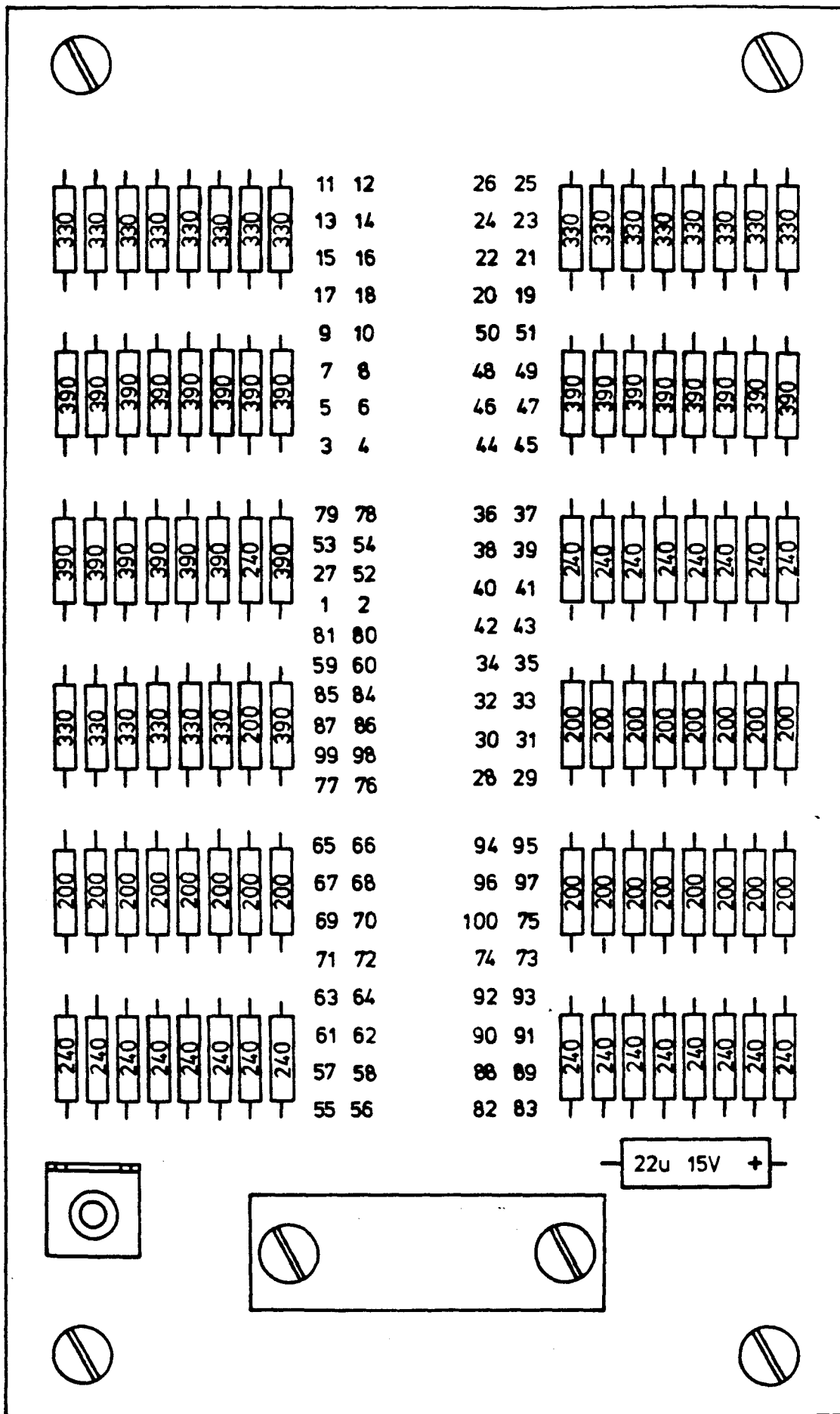
The CBL 219 is shown in Figures 2 to 5.

Installation of the BTU 701 is shown in Figure 6.



CBL 219





mounting

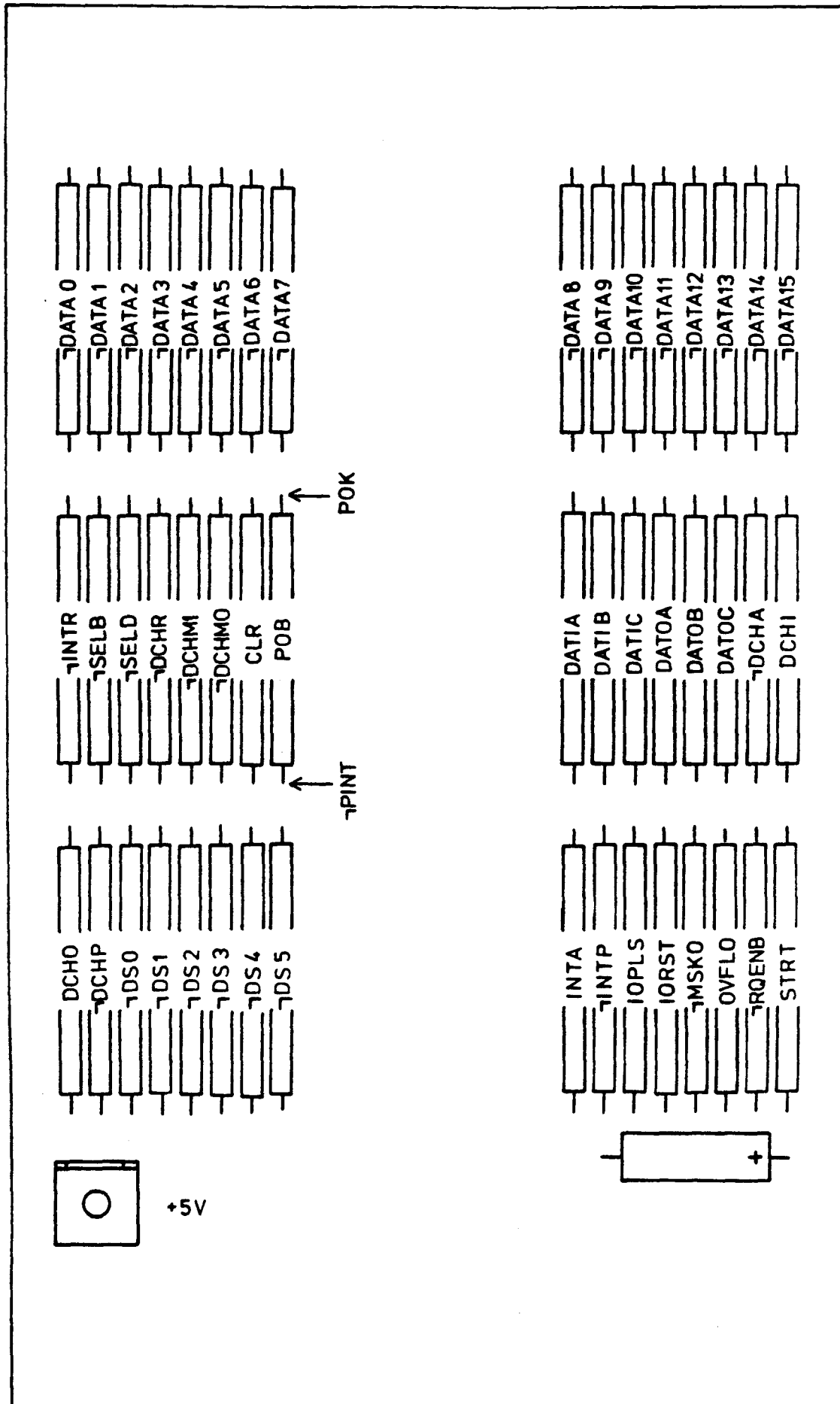


Figure 4

CBL 219

1	CLR	27	7 DCHMO	52	OV	76	OV
2	OV	28	OV	53	OV	77	7 PINT
3	7 DATA 0	29	DCHI	54	OV	78	OV
4	OV	30	OV	55	DCHO	79	POK
5	7 DATA 1	31	7 DCHA	56	OV	80	OV
6	OV	32	OV	57	IN 7 DCHP OUT	81	POB
7	7 DATA 2	33	DATOC	58	OV	82	OV
8	OV	34	OV	59	7 DCHR	83	STRT
9	7 DATA 3	35	DATOB	60	OV	84	OV
10	OV	36	OV	61	7 DSO	85	7 SELD
11	7 DATA 4	37	DATOA	62	OV	86	OV
12	OV	38	OV	63	7 DS1	87	7 SELB
13	7 DATA 5	39	DATIC	64	OV	88	OV
14	OV	40	OV	65	7 DS2	89	7 RQENB
15	7 DATA 6	41	DATIB	66	OV	90	OV
16	OV	42	OV	67	7 DS3	91	OVFLO
17	7 DATA 7	43	DATIA	68	OV	92	OV
18	OV	44	OV	69	7 DS4	93	7 MSKO
19	7 DATA 8	45	7 DATA 15	70	OV	94	OV
20	OV	46	OV	71	7 DS5	95	IORST
21	7 DATA 9	47	7 DATA 14	72	OV	96	OV
22	OV	48	OV	73	INTA	97	IOPLS
23	7 DATA 10	49	7 DATA 13	74	OV	98	OV
24	OV	50	OV	75	IN 7 INTP OUT	99	7 INTR
25	7 DATA 11	51	7 DATA 12			100	OV
26	OV						

Figure 5

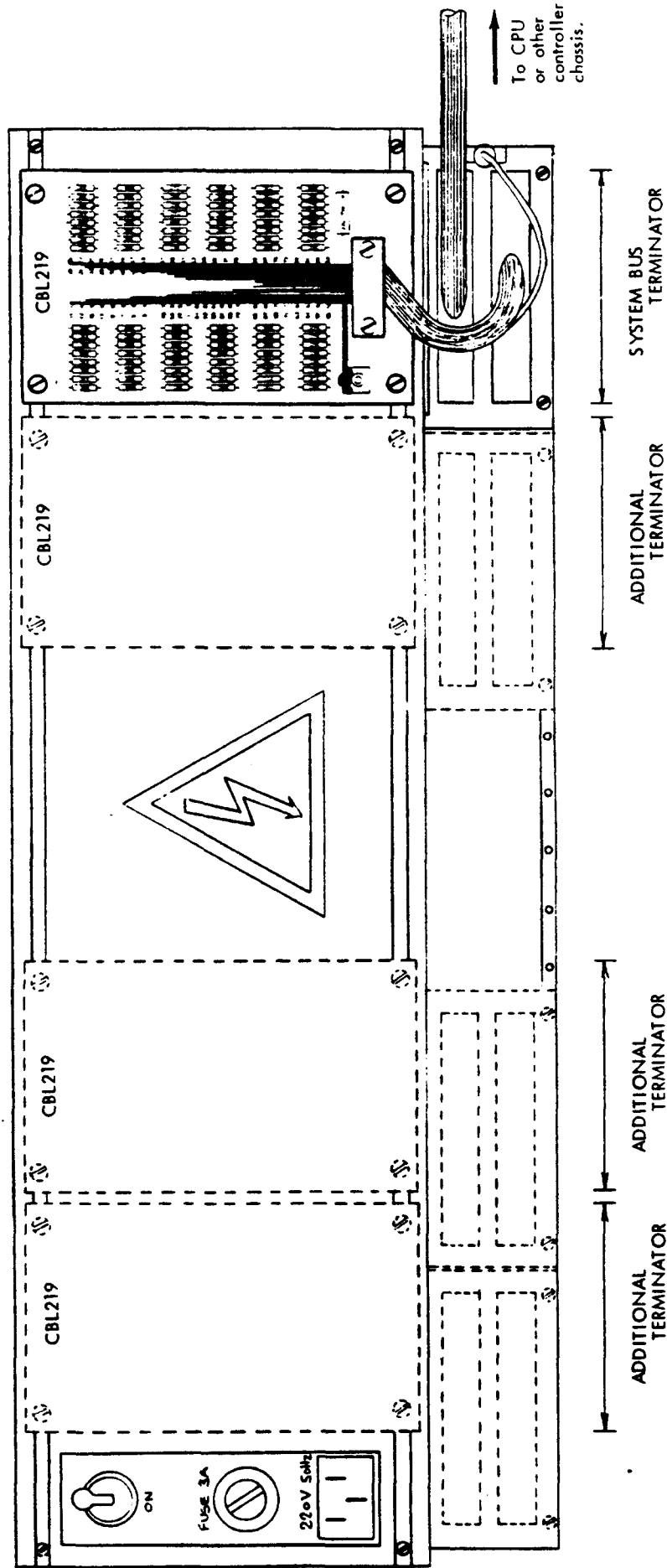


figure 6

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