Title:

Drawings for

PUC 703

A/S REGNECENTRALEN

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Abstract: This paper contain the drawings of PUC 703, Paper Tape Punch Controller.

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CONTENTS PAGE

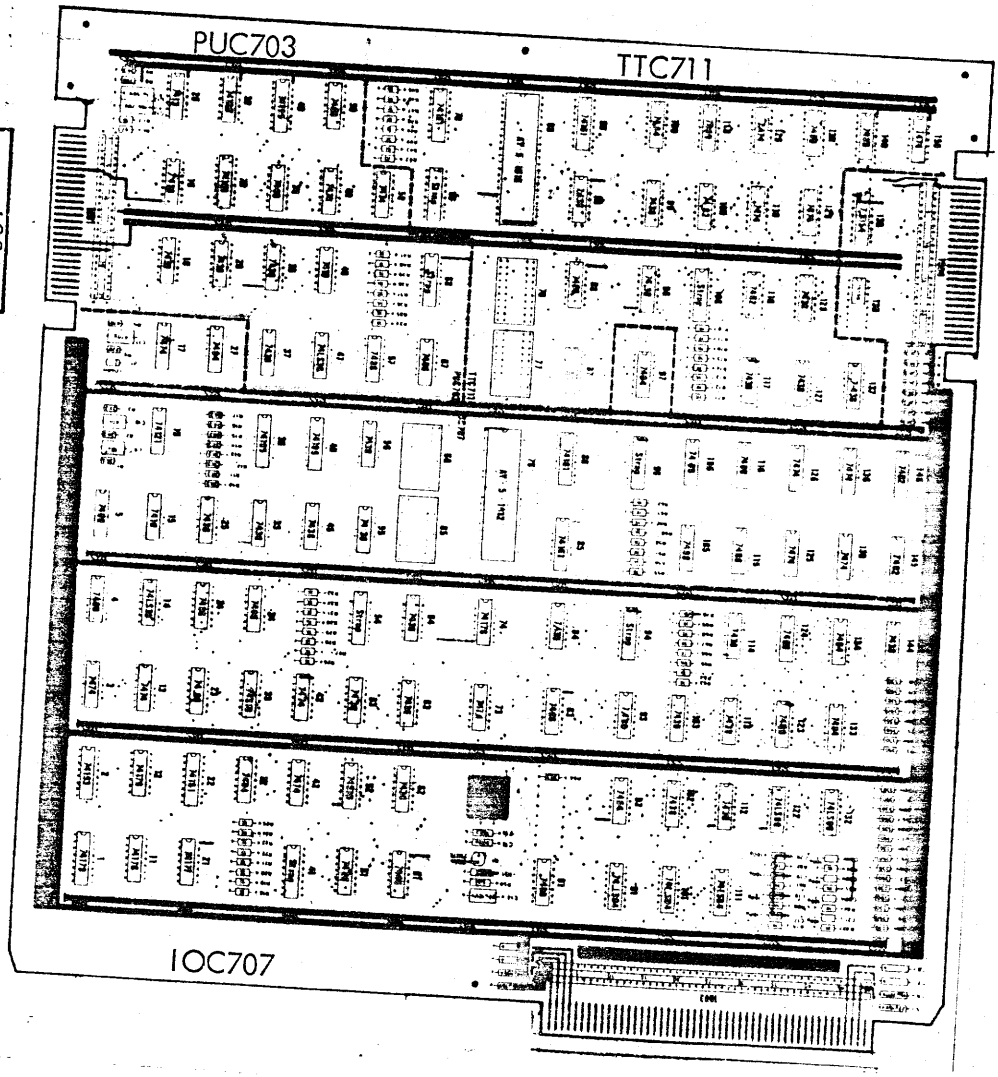
INTRODUCTION:

The PUC 703 is absolute identical to the PUC 702, which it replaces in the future.

PUC 703 always shall be mounted together with the IOC 707, which contains the BUS interface for the PUC 703.

Please observe that if the PUC 703 is the second paper tape punch controller in A system, the strap platform in position 41 (the real time clock) shall be removed. Please put also attention to the paper tape reader (position 54) and the teletype controller (position 94). Are these not used, remove their strap platforms for device code selection.

All references to logic diagrams marked IOC are to IOC - diagrams in the IOC 707 controller.



IOC 707
1001

IOC 707
1004

CBL 215

CBL 194

PUC 703
J1

IOC 707
J2

TTC 711
J1

IOC 707
J1

IOC 707 J1 : Teletype

IOC 707 J2 : Paper Tape Reader

IOC 707 PUC 703 TTC 711	CABLE ASSEMBLY DRAWING	Page

CONNECTOR:

IOC 707 - 1001 : Edgeconnector 2 x 25 contacts
IOC 707 - J2 : Cannon 2 DE 19S
PUC 703 - J1 : Cannon 2 DE 19S

Paper Tape Reader

IOC 707 1001	SIGNAL	IOC 707 J2
A 20	- RDRDY	11
A 21	SPKT	9
A 22	C5	5
A 23	C6	6
A 24	C7	7
A 25	C8	8
B 17	+5V	16
		17
B 18	OV	14
		18
B 19	GO	10
B 20	STOP	12
B 21	FWSTP	13
B 22	C1	1
B 23	C2	2
B 24	C3	3
B 25	C4	4

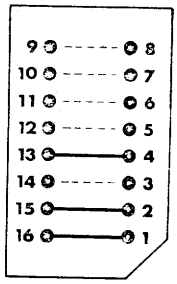
Paper Tape Punch

IOC 707 1001	SIGNAL	PUC 703 J1
A1	TPOUT	15
A2	PUCN8	7
A3	PUCN7	6
A4	PUCN6	5
A5	PULH	9
A6	PUSG	18
A7	OV	12
B1	PUCN5	4
B2	PUCN4	3
B3	PUCN3	2
B4	PUCN2	1
B5	PUCN1	8
B6	OV	13

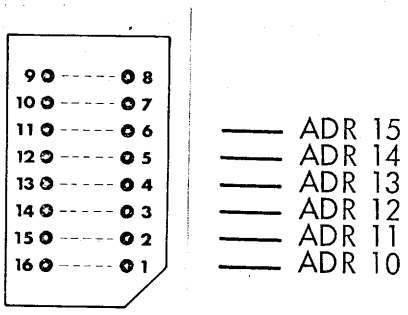
Selection of device code (position 68)

Standard device code

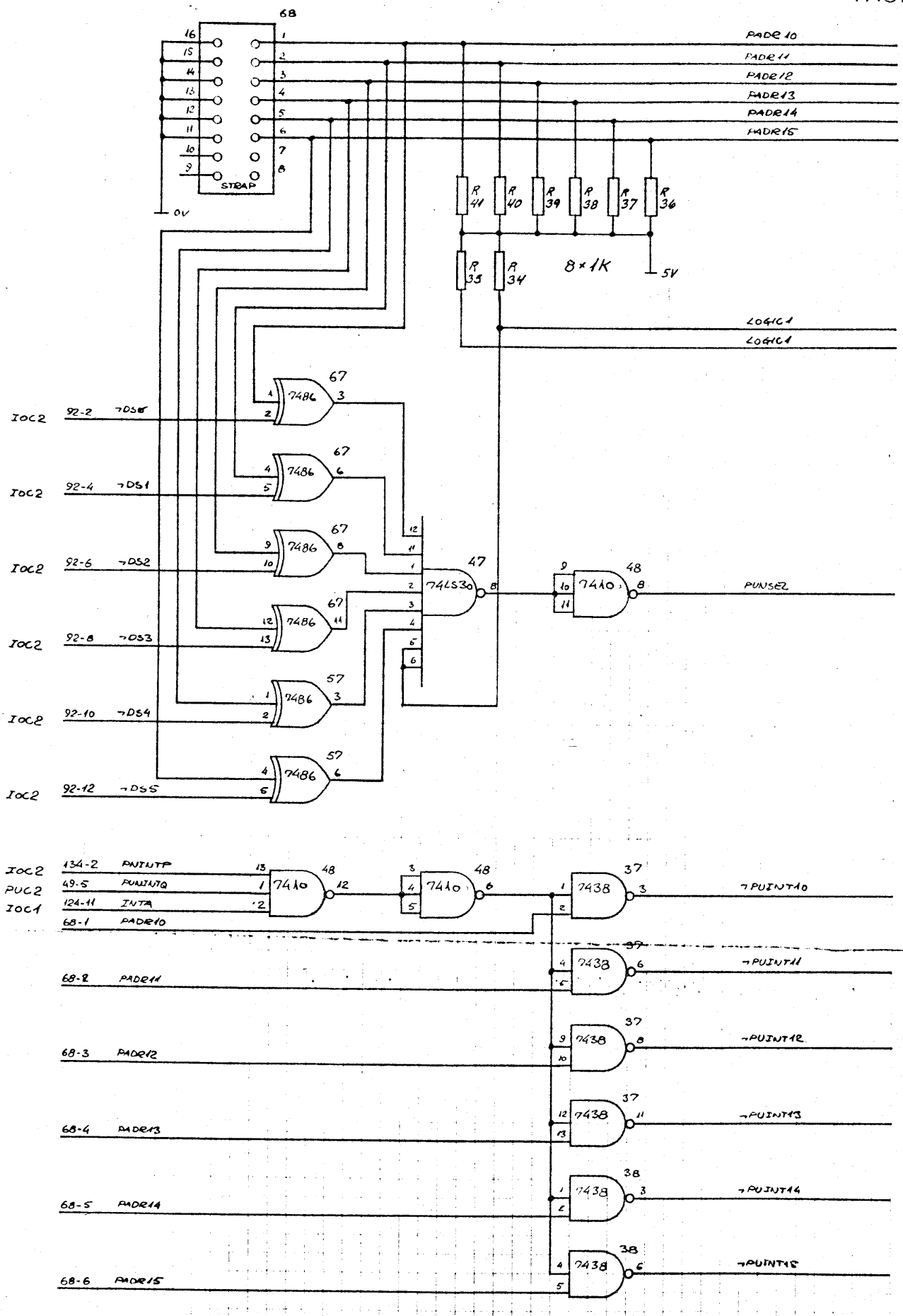
PTP ~ 13_8



Changing of device code



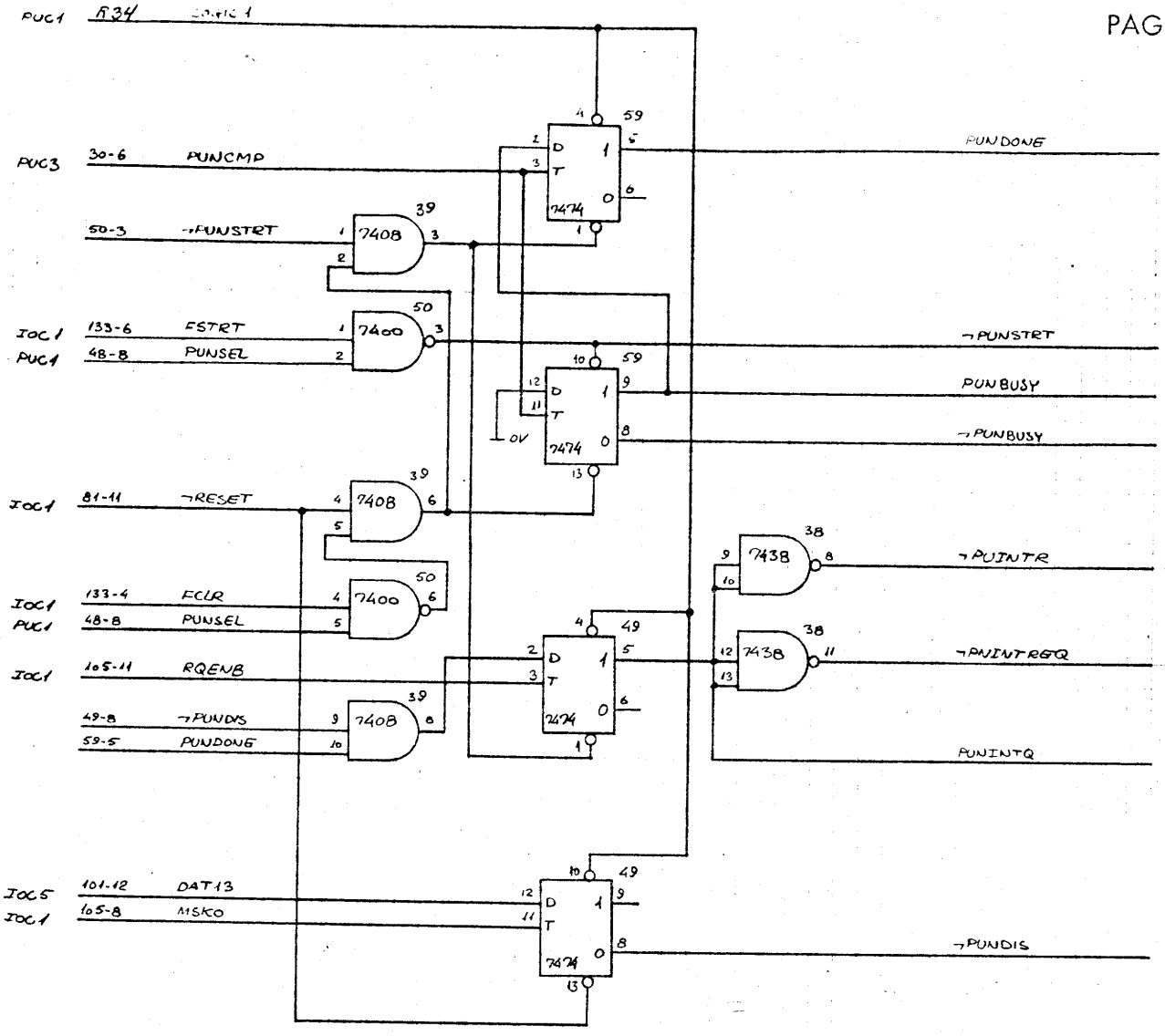
Strap in a position is giving logic 0, and no strap logic 1 in the device code



Unit: PUC 703	SELECT AND INTA LOGIC	PUC 1
Dwg. No. R12069	LOGIC DIAGRAM	

A/S REGNECENTRALEN | Designed by JJO 75.03.05 | Drawn by | Dwg. Office Check | Design Check | Replaces Dwg. No. R11102 | due to ECN | Replaced by Dwg. No.

RC doc. VB



A/S REGNECENTRALEN	Designed by JJO 75.03.06	Drawn by	Dwg. Office Check	Design Check	Replaces Dwg. No. R 11103	due to ECN	Replaced by Dwg. No.
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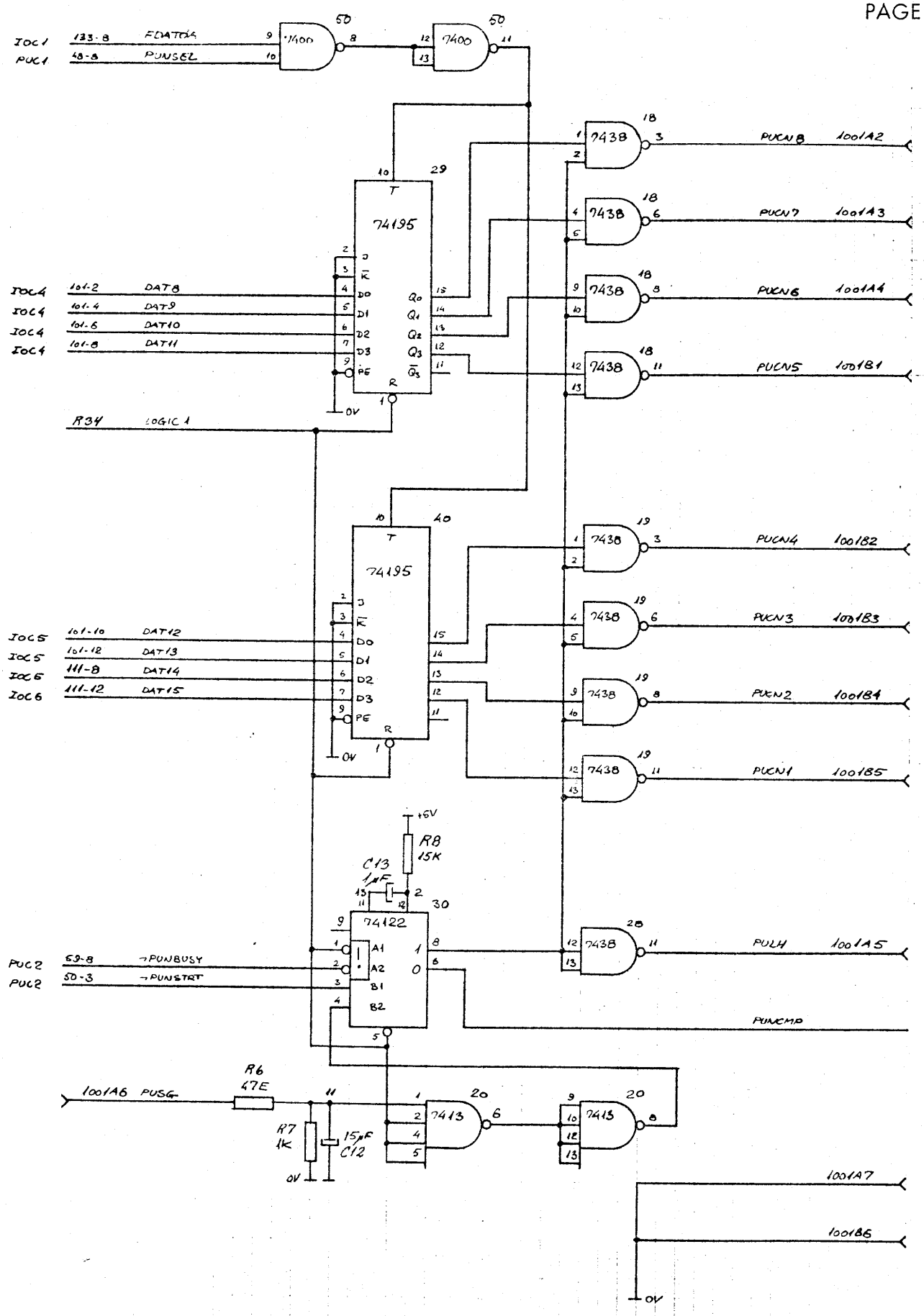
Unit PUC 703	PUC BUSY, DONE AND INTERRUPT LOGIC	PUC 2
Dwg. No. R12070	LOGIC DIAGRAM	



Unit PUC703	OUTPUTBUFFER	PUC 3
Dwg. No. R12071	LOGIC DIAGRAM	

A/S REGNECENTRALEN
Designed by JJO 75.03.06
Drawn by
Dwg. Office Check
Design Check
Replaces Dwg. No. R 11192
due to ECN
Replaced by Dwg. No.

IOC707:

RESISTORS:

R 87 to R 110
2% 0,4W Metalfilm

All other resistors
5% 1/8W Carbonresistors

Pos 41 STRAP:

Real Time Clock Device Code

Pos 54 STRAP:

Paper Tape Reader Contr.
Device Code

Pos 94 STRAP:

First Teletype Contr. Device Code

Pos 96 STRAP:

First Teletype Contr.
Selection of Speed

PUC703:

Pos 68 STRAP:

Paper Tape Punch Contr.
Device Code

TTC711:

Pos 108 STRAP:

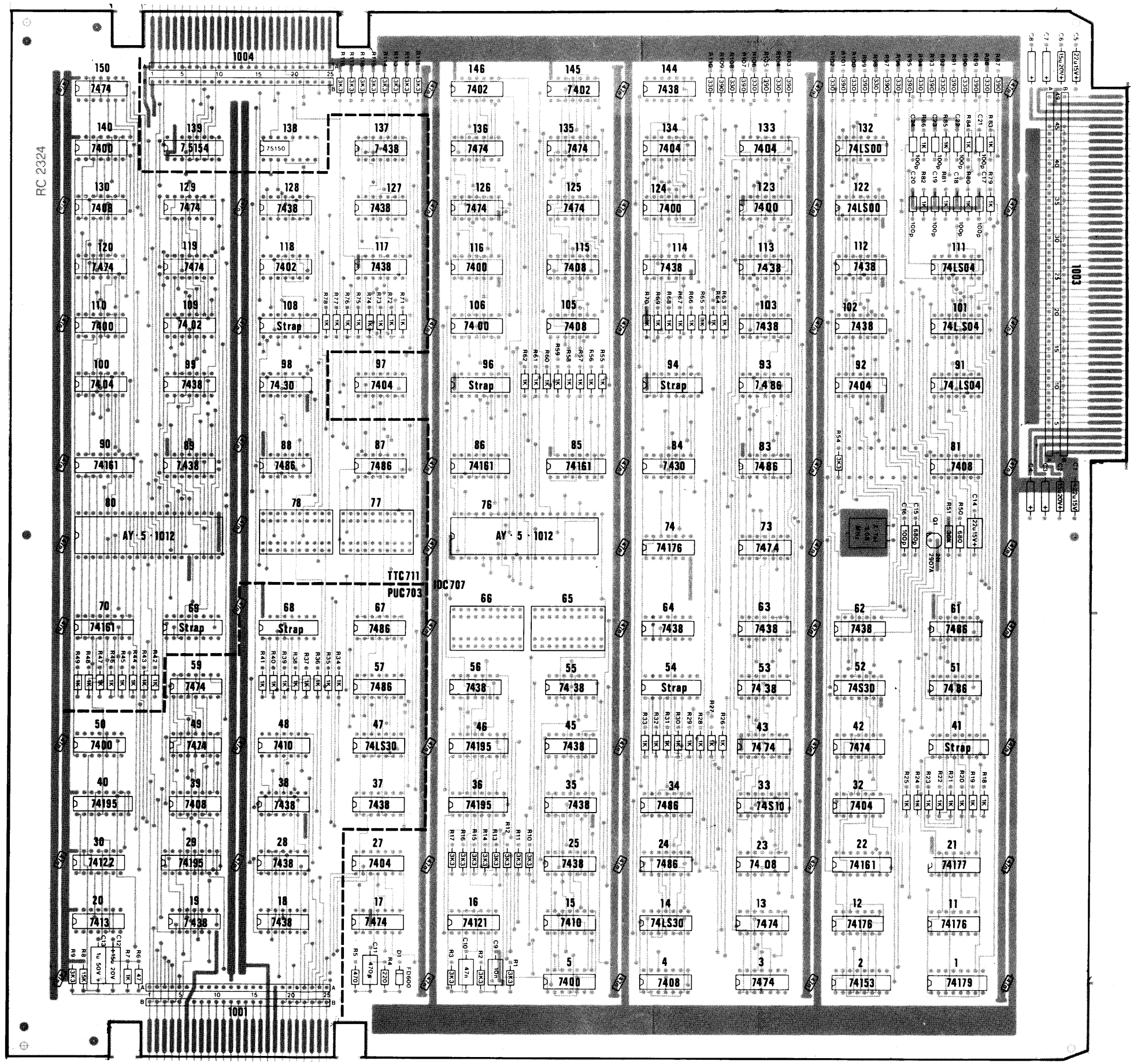
Second Teletype Contr.
Device Code

Pos 69 STRAP:

Second Teletype Contr.
Selection of Speed

All Strappings:

Strap = logic 0
No Strap = logic 1



PCB ASSEMBLY DRAWING RC 2324