RCSL: 44- RT196
Author: Jørgen Green
Edited: January 1971

RC 2000 OPTION TR 22

RC 4000 Interface

KEY: Technical Manual, RC 4000, Paper Tape Reader.

A/S REGNECENTRALEN
Falkoneralle 1
2000 Copenhagen F

CONTENTS

1. MAIN CHARACTERISTICS

The RC 2000 Paper Tape Reader can be adapted to The RC 4000 Computer by implying the option TR 22. This option provides balanced transmission lines for data and control signals between the Reader Cabinet and the Tape Reader Controller in RC 4000.
It also provides an Interrupt signal when the number of characters in the RC 2000 buffer store exeeds 64, and a Connected signal indicating that main power is present in the RC 2000. A Master Clear signal is generated in RC 2000 by the appearance of the DC-power to obtain proper initial conditions before sending the Connected signal to RC 4000.
When modified by this option, the RC 2000 will no longer work with alternative input sources.

2. REALIZATION

The option implies 1 printed circuit board to be removed (position B0), and 2 printed circuit boards to be added (positions A14 and B14). The wiring of the jack J1 is totally changed, and minor alterations in the back plane wiring of the circuit board cage are done. A logic inverter circuit is added within an existing printed circuit board (position B2), and the main power jack is replaced in order to fit the RC 4000 main power system.

3. DOCUMENTATION

The modifications stated in paragraphs 1. and 2. imply no changes of the Users Manual. The Technical Manual is affected as described below.

3.1. Manual Paragraphs 1 through 4.
Paragraph 2.11, Control Circuits, should be regarded only as a scheme of principle, as all circuits have been removed from position B0.
Paragraph 4.7, Specs. for Input to the RC 2000 Buffer, is deleted, and so are 4.8 through 4.15, which are all concerning external input sources.

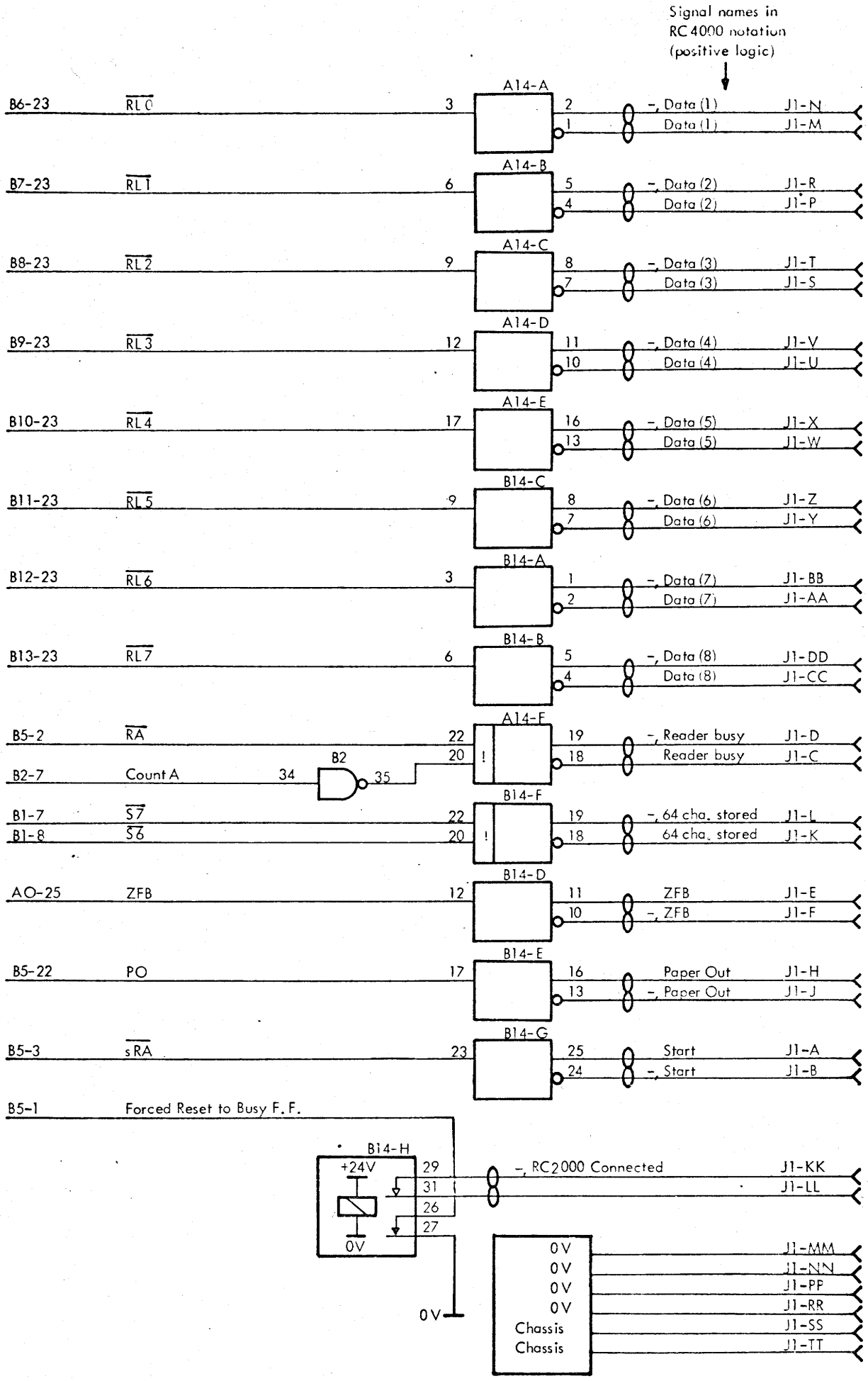
3.2. Diagrams and Wiring Schemes.
The logic diagrams control Circuits should be regarded as a scheme of principle only, as all circuits have been removed from position B0.
Lay-out-drawing and circuit diagrams for PCBA RC 1224 are removed from the manual and several sheets are replaced.

3.3. New Pages Added.
New sheets showing circuit-diagrams of PCBA's RC 0924-1 and RC 0924-2 and the corresponding wiring schedules for pos. A14 and B14 are added.

4. DRAWINGS AND SCHEMES

This section constitutes a list of drawings and schemes which define the option.

1.	RC 4000 Interface Circuits in RC 2000, Logic Diagram,	drwg. no.	R 10097
2.	Survey of Printed Circuit Boards,		R 20159
3.	Circuit Diagram PCBA RC 0924-1		V 11451
4.	Circuit Diagram PCBA RC 0924-2		V 11450
5.	Circuit Diagram Reset of A and B, PCBA RC 1213-2,	page 1	R 20207
6.		page 2	R 20208
7.		page 3	R 20209
8.	Assembly drawing PCBA RC 1213-2		R 20206
9.	Wiring Schedule pos. A0		R 20142
10.	Wiring Schedule pos. A9		R 20475
11.	pos. A14		R 20143
12.	pos. B0		R 20144
13.	pos. B1		R 20145
14.	pos. B2		R 20146
15.	pos. B5		R 20147
16.	pos. B6		R 20148
17.	pos. B7		R 20149
18.	pos. B8		R 20150
19.	pos. B9		R 20151
20.	pos. B10		R 20152
21.	pos. B11		R 20153
22.	pos. B12		R 20154
23.	pos. B13		R 20155
24.	pos. B14		R 20156
25.	Wiring Schedule Jack 1 for RC 4000		R 20158
26.	Wiring Schedule Jack 6, Jack for input		R 20157


Signal names in RC4000 notation (positive logic)
B6-23 RL0 3 A14-A 2 1 -, Data (1) Data (1) J1-N J1-M
B7-23 RL1 6 A14-B 5 4 -, Data (2) Data (2) J1-R J1-P
B8-23 RL2 9 A14-C 8 7 -, Data (3) Data (3) J1-T J1-S
B9-23 RL3 12 A14-D 11 10 -, Data (4) Data (4) J1-V J1-U
B10-23 RL4 17 A14-E 16 13 -, Data (5) Data (5) J1-X J1-W
B11-23 RL5 9 B14-C 8 7 -, Data (6) Data (6) J1-Z J1-Y
B12-23 RL6 3 B14-A 1 2 -, Data (7) Data (7) J1-BB J1-AA
B13-23 RL7 6 B14-B 5 4 -, Data (8) Data (8) J1-DD J1-CC
B5-2 RA 22 20 A14-F 19 18 -, Reader busy Reader busy J1-D J1-C
B2-7 Count A 34 B2 35
B1-7 S7 22 B14-F 19 18 -, 64 cha. stored 64 cha. stored J1-L J1-K
B1-8 S6 20
AO-25 ZFB 12 B14-D 11 10 ZFB -, ZFB J1-E J1-F
B5-22 PO 17 B14-E 16 13 Paper Out -, Paper Out J1-H J1-J
B5-3 sRA 23 B14-G 25 24 Start -, Start J1-A J1-B
B5-1 Forced Reset to Busy F.F.
B14-H +24V 29 31 26 27 0V
-, RC2000 Connected J1-KK J1-LL
0V
0V 0V 0V 0V Chassis Chassis J1-MM J1-NN J1-PP J1-RR J1-SS J1-TT

OPTION TR 22

CENTRALEN

Unit: RC 2000

Designed BN

Approved

Checked

Last Revision

SURVEY OF PRINTED CIRCUIT BOARDS

Drawing No R 20159

Drawn by 300670 MBC

Checked 300670 LAP

Sheets

Sheet

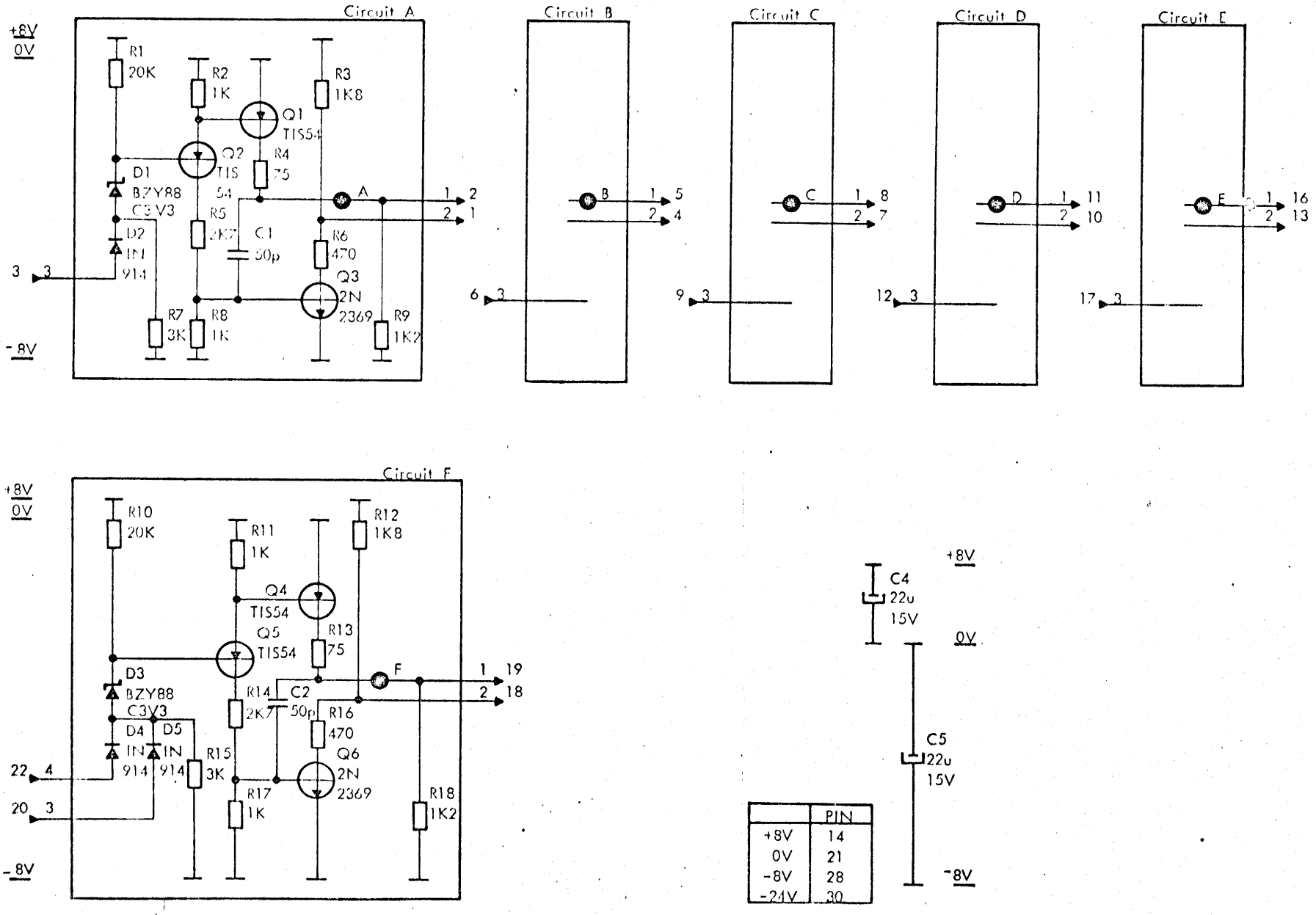
0	1	2	3	4	5	6	7	8	9	10	11	12	13	14
Int. Block Motor	A B Adder 128	A B Adder 64	A B Adder 32	A B Adder 16	A B Adder 8	A B Adder 4	A B Adder 2	A B Adder 1		Read Driver Y A 16 32 64 128	Read Driver X A 1 2 4 8	Write Driver Y B 16 32 64 128	Write Driver X B 1 2 4 8	Matching Transc.
1227 A	1201	1201	1201	1201	1201	1201	1201	1201		1203	1203	1203	1203	0924-1

	Lamp Control Motor Control	Gate	WA WB WC	RB RC Reset	RA WD PO	In Out Track 1 A	In Out Track 2 B	In Out Track 3 C	In Out Track 4 D	In Out Track 5 E	In Out Track 6 F	In Out Track 7	In Out Track 8	Transc., Start-Reset
	1205A-1	1213-2	1371	1200-3	1212-3	1202	1202	1202	1202	1202	1202	1202	1202	0924-2

V11451

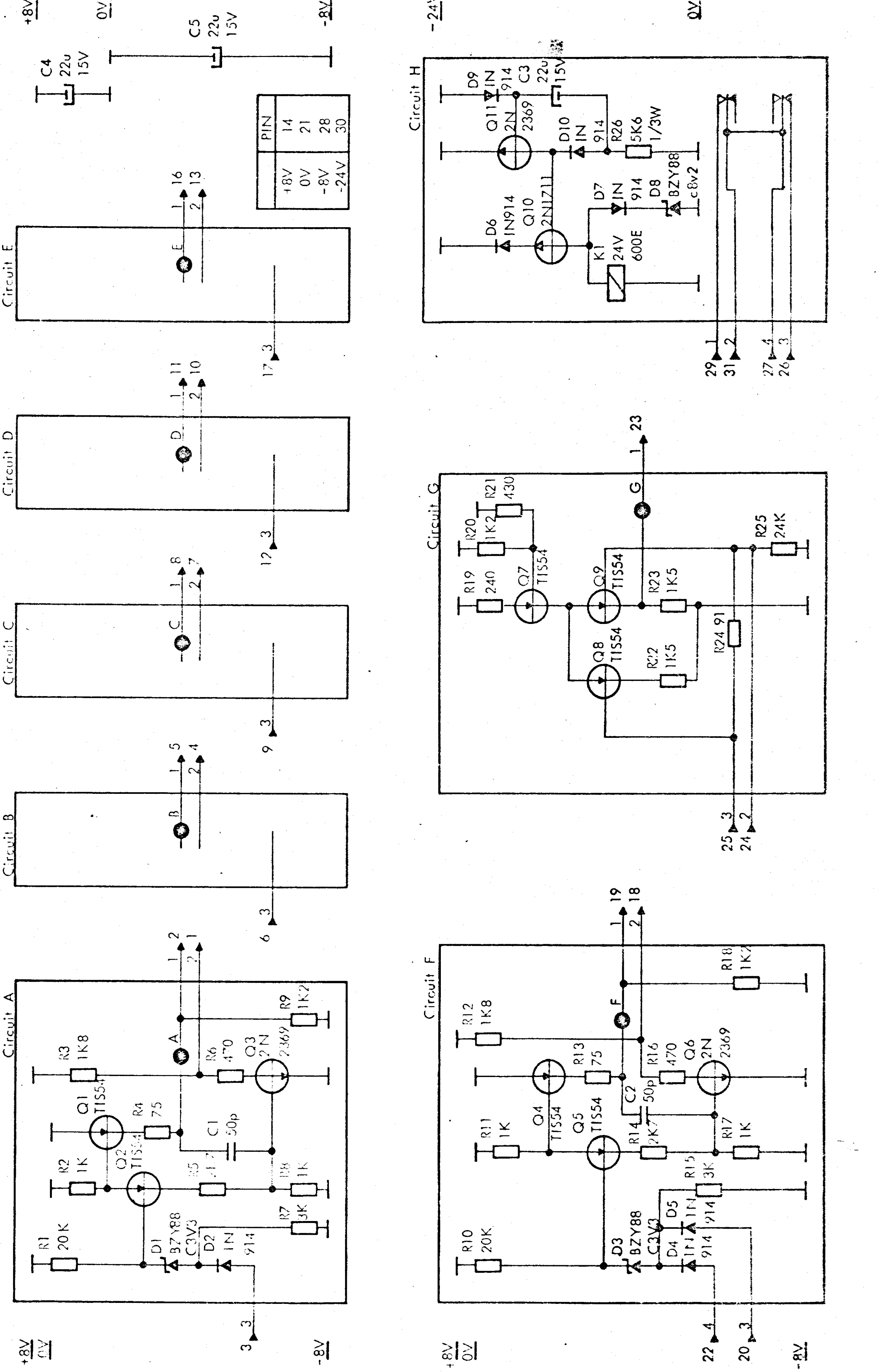
Transceiver matching RC2000 to the TRC interface in RC4000

Circuit Diagram



	PIN
+8V	14
0V	21
-8V	28
-24V	30

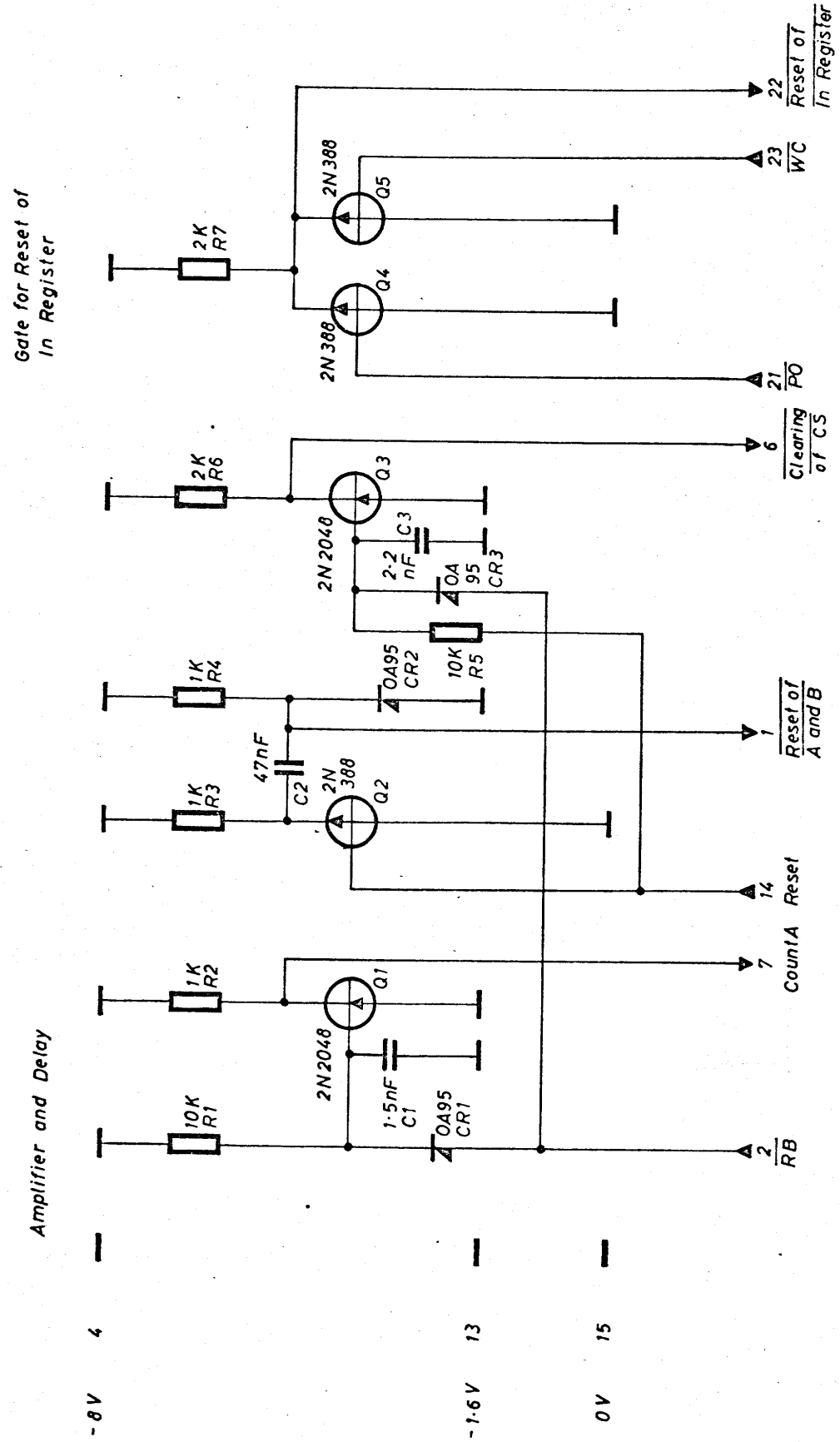
RC0724-1



Transceiver and Start Reset Circuit
matching RC2000 to the TRC interface
in RC4000
Circuit Diagram

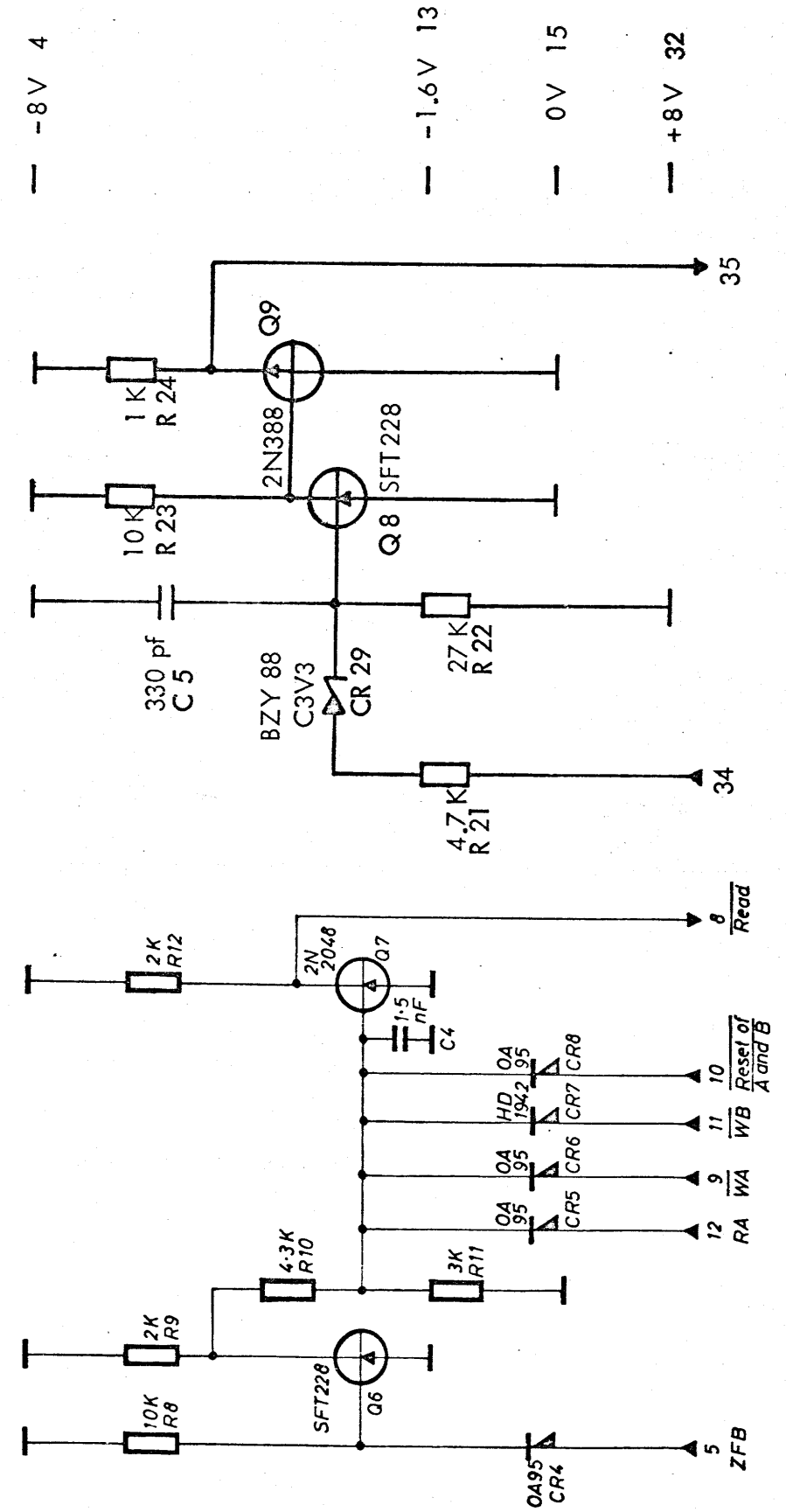
V11450

RC0924-2



OPTION TR 22

Unit: RC 2000	Designed B.N.	AMPLIFIER AND DELAY GATE FOR RESET OF IN REGISTER	Drawing No R 20207
CENTRALEN	Approved		Drawn by I.K. 27.6.1967
	Checked 3.12.1965		Checked F.E. 1·6·67
	Last Revision 130270HAR		3 Sheets / Sheet 1
			B2 / 1213-2



Unit: RC 2000	Designed B. N. JOG	RESET OF A AND B	Drawing No R 20208
A/S CENTRALEN	Approved		Drawn by L.K. 27.6.67.
	Checked 15.1.71.		Checked F.E. 1.9.67.
	Last Revision		3 Sheets / Sheet 2
			B 2 / 1213-2

CENTRALEN

Unit RC 2000

OPTION TR 22

Designed B.N.

Approved

Checked 3.12.1965

Last Revision I.K.10.11.67.

SELECTOR CIRCUIT

Drawing No. R 20209

Drawn by I.K. 19.6.1967

Checked F.E. 1.9.67

Sheet 3 Sheets 3

B2

1213-2

Unmarked Resistors: 1/3W

— Diodes: OA95

Olivetti
8-Channel
7-Channel
5-Channel

CR9 CR11 CR12 CR14 CR16 CR17 CR19 CR21 CR24 CR26 HD 1942 CR27 HD 1942 CR28 HD 1942

1K R13 1K R15 1K R17 1K R19

CR10 OA 200 CR13 OA 200 CR15 OA 200 CR18 OA 200 CR20 OA 200 CR22 OA 200 CR23 OA 200 CR25 OA 200

2K R14 2K R16 2K R18 2K R20

0 V 15

+8V 32

31 Sel.of Olivetti ABCDEF

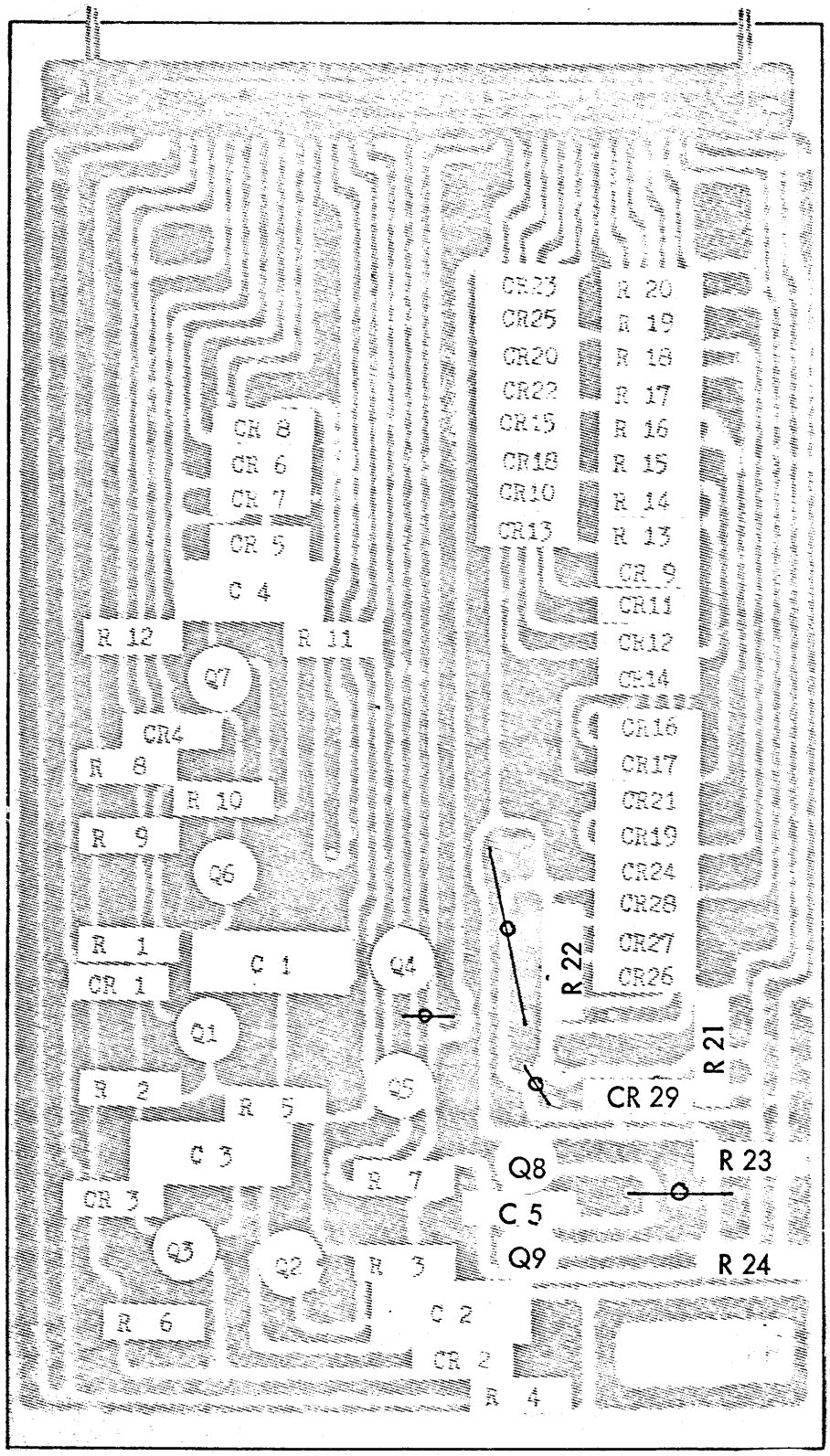
30 Sel.of Tr.8

29 Sel. of Tr.6-7

28 Sel.of Tr.1-2-3 4-5-6 P

33

25 C5 26 C3 27 D8 24 A5 Plug 7



Unit: RC 2000	Designed JØG	PCBA 1213-2 LAY OUT	Drawing No R 20206
REGNE CENTRALEN	Approved		Drawn by ANG 1.11.67.
	Checked 15.1.71.		Checked BJL 1.12.67.
	Last Revision		Sheets / Sheet

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1					1
2					2
3					3
4					4
5					5
6					6
7					7
8					8
9					9
10					10
11				PO x ZFB	11
12	B3-15			PO	12
13	A1-2			ZFB	13
14			- x	+ 8 Volts	14
15			- x	0 Volts	15
16					16
17					17
18					18
19			- x	0 Volts	19
20					20
21			- x	0 Volts	21
22	B0-28			$\overline{\text{Reset}}$	22
23	J6-HH	B 5-30		$\overline{\text{Ext. Control}}$	23
24	B1-5			Block Motor	24
25	P6-AA	B14-12		ZFB*	25
26					26
27					27
28			- x	- 8 Volts	28
29	B2-5			ZFBxExt. control	29
30			- x	- 24 Volts	30
31	B2-27			- 24 V Block 8 Chan.	31
32	B2-26			- 24 V Block 7 Chan.	32
33	B2-24			- 24 V Block Olivetti	33
34	B2-25			- 24 V Block 5 Chan.	34
35	B0-31			- 1.6 Volts	35

Pos. A 0

OPTION TR 22

Unit: RC 2000	Designed BN	WIRING SCHEDULE	Drawing No R 20142
RECONE CENTRALEN	Approved		Drawn by 090770 MBC
	Checked 031265		Checked 090770 HAP
	Last Revision		____ Sheets Sheet ____

RCSL 44-R 1141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1	B6-23			RL 0	1
2	B7-23			RL 1	2
3	B8-23			RL 2	3
4	B9-23			RL 3	4
5					5
6					6
7	B0-30			Parity	7
8					8
9					9
10					10
11					11
12					12
13					13
14					14
15			- x -	0 Volts	15
16					16
17					17
18					18
19	A8-4		- x -	0 Volts	19
20					20
21			- x -	0 Volts	21
22					22
23					23
24					24
25					25
26					26
27					27
28	A10-35	A14-28	x -	-8 Volts	28
29					29
30		A14-30	x -	-24 Volts	30
31	A8-14			+8 Volts	31
32	B10-23			RL 4	32
33	B11-23			RL 5	33
34	B12-23			RL 6	34
35	B13-23			RL 7	35

Pos. A 9

Option 22

Unit: RC 2000	Designed	WIRING SCHEDULE	Drawing No R 20475
REGNE CENTRALEN	Approved		Drawn by
	Checked		Checked
	Last Revision		____ Sheets Sheet ____

RCSL44-A141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1	J1-M				1
2	J1-N				2
3	B6-23			$\overline{RL0}$	3
4	J1-P				4
5	J1-R				5
6	B7-23			$\overline{RL1}$	6
7	J1-S				7
8	J1-T				8
9	B8-23			$\overline{RL2}$	9
10	J1-U				10
11	J1-V				11
12	B9-23			$\overline{RL3}$	12
13	J1-W				13
14	B14-14			+ 8 Volts	14
15			x -	0 Volts	15
16	J1-X				16
17	B10-23			$\overline{RL4}$	17
18	J1-C				18
19	J1-D				19
20	B2-35				20
21			x -	0 Volts	21
22	B5-2			$\overline{RA}$	22
23					23
24					24
25					25
26					26
27					27
28	B14-28	A9-28	x -	- 8 Volts	28
29					29
30	B14-30	A9-30	x -	- 24 Volts	30
31					31
32					32
33					33
34					34
35					35

Pos. A14 0924-1

OPTION TR 22

Unit: RC 2000	Designed JØG	WIRING SCHEDULE	Drawing No R 20143
RECNE CENTRALEN	Approved EK		Drawn by EK 27/10-69
	Checked		Checked
	Last Revision		____ Sheets Sheet ____

RCSL 44-R1141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1					1
2					2
3					3
4					4
5					5
6					6
7					7
8					8
9	B1-1	P 3-HH		- 8 Volts	9
10	B1-2			+ 8 Volts	10
11	B1-17			- 24 Volts	11
12				0 Volts	12
13					13
14					14
15					15
16					16
17					17
18					18
19					19
20					20
21	P7-C7			0 V. Olivetti	21
22	P7-C1			0 V. 5 chan.	22
23					23
24	P7-D4			0 V. 7 chan.	24
25					25
26		P7-A7		0 V. 8 chan.	26
27					27
28	B4-28	A0-22		$\overline{\text{Reset}}$	28
29					29
30	A9-7			Parity	30
31	B2-13	A0-35		- 1.6 Volts	31
32	B5-3			s $\overline{\text{RA}}$	32
33					33
34	B5-2			$\overline{\text{RA}}$	34
35					35

Pos.	B 0

OPTION TR 22

Unit: RC 2000	Designed BN	WIRING SCHEDULE	Drawing No R 20144
			Drawn by 130770 MBC
	Approved		Checked 130770 HAP
A/S RUNE CENTRALEN	Checked 031265		____ Sheets Sheet ____
	Last Revision		

RCSL 44-RT 141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1	B0-9	B2-4		- 8 Volts	1
2	B0-10	B2-32		+ 8 Volts	2
3	B1-23			$\overline{PO}$	3
4	B3-5			WB	4
5	P4-L	A0-24		Block Motor	5
6	P3-BB			Skip N. O.	6
7	A1-3	B14-22		$\overline{S7}$	7
8	A2-3	B14-20		$\overline{S6}$	8
9	A3-3			$\overline{S5}$	9
10	A4-3			$\overline{S4}$	10
11	A5-3			$\overline{S3}$	11
12	A6-3			$\overline{S2}$	12
13	P4-P				13
14	B6-8			Inhibit Time	14
15	P2-K	P3-J	- x	0 Volts	15
16					16
17	B0-11		- x	- 24 Volts	17
18					18
19	B1-21		- x	0 Volts	19
20					20
21	B1-19	P3-JJ		0 Volts	21
22	P7-A1			Light Sense	22
23	B1-3	B2-21		$\overline{PO}$	23
24	P4-A				24
25	P4-D				25
26	P3-T			Light adj.	26
27					27
28					28
29	P7-D6			- 24 Volts*	29
30	B4-1			$\overline{RC}$	30
31	B6-34			$\overline{Strobe-Reset}$	31
32	P2-P			$\overline{PO\ Sense}$	32
33	P3-W	P7-B4		$\overline{UP + PO\ Sense}$	33
34	B5-11	P7B2		Solenoid	34
35	P3-RR			PO Adj.	35

Pos. B 1

OPTION TR 22

Unit: RC 2000	Designed BN	WIRING SCHEDULE	Drawing No R 2014[illegible]
REGNE CENTRALEN	Approved		Drawn by 130770 NSC
	Checked 031265		Checked 130770 HAP
	Last Revision		___ Sheets Sheet ___

RCSL 44-RT 141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1	B2-10	B4-15		Reset A and B	1
2	B4-8			Read Current Time	2
3					3
4	B1-1	B3-23		- 8 Volts	4
5	A0-29	J6-BB		ZFB x Ext. contr.	5
6	B4-10			Clearing of CS	6
7	A8-11	B2-34		Count A	7
8	B4-13			Read	8
9	B3-8			WA	9
10	B2-1	A3-13		Reset A and B	10
11	B3-1			WB	11
12	B5-1			RA	12
13	B5-4	B0-31		- 1.6 Volts	13
14	B4-34			Reset	14
15	B2-19		x -	0 Volts	15
16					16
17	P3-KK		- x -	- 24 Volts	17
18					18
19	B2-15		- x -	0 Volts	19
20					20
21	B1-23	B4-27		PO	21
22	B6-11			Reset of Input Reg.	22
23	B3-21			WC	23
24	P3-N	A0-33		-24 V Olivetti Tapes	24
25	P7-C5	A0-34		-24 V 5-Channel Tapes	25
26	P7-C3	A0-32		-24 V 7-Channel Tapes	26
27	P7-D8	A0-31		-24 V 8-Channel Tapes	27
28	P2-N			Sel. of track 1, 2, 3, 4, 5, R, S	28
29	P2-R			" " " 6, 7	29
30	P2-F			" " " 8	30
31	P2-C			Olivetti and P	31
32	B1-2	B3-9		+ 8 Volts	32
33	P3-C			- 24 Volts for Pot.	33
34		B2-7		Count A	34
35		A14-20		Count A	35

Pos. B 2

OPTION TR 22

Unit: RC 2000	Designed BN	WIRING SCHEDULE	Drawing No R 20146
§ REGNE CENTRALEN	Approved		Drawn by 130770 MBC
	Checked 031265		Checked 130770 HAP
	Last Revision		____ Sheets Sheet ____

RCSL 44-R 141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1	B2-12	B14-26		RA	1
2	B0-34	A14-22		$\overline{RA}$	2
3	B0-32	B14-23		s $\overline{RA}$	3
4	B2-13	B6-12		- 1.6 Volts	4
5	B4-12			RB	5
6	B4-34			Reset	6
7	B3-1			$\overline{WB}$	7
8	B5-12			WD	8
9	B4-15			$\overline{\text{Reset A and B}}$	9
10	P3-CC			$\overline{\text{Read}}$	10
11	B1-34			Solenoid	11
12	B5-8			WD	12
13	A12-33			$\overline{\text{Write Current Time}}$	13
14	B4-27			$\overline{PO}$	14
15	B5-19	P7-B6	- x	0 Volts	15
16					16
17	A6-30		x -	- 24 Volts	17
18					18
19	B5-15		- x -	0 Volts	19
20					20
21			- x	0 Volts	21
22	B3-15	B14-17		PO	22
23					23
24					24
25					25
26					26
27					27
28					28
29					29
30	A0-23			$\overline{\text{Ext. Contr.}}$	30
31					31
32					32
33	A4-14	B6-14		+ 8 Volts	33
34	A5-30			- 24 Volts	34
35	B6-13		- x	- 8 Volts	35

Pos.	B 5

OPTION TR 22

Unit: RC 2000	Designed BN	WIRING SCHEDULE	Drawing No R 20147
REGNE CENTRALEN	Approved		Drawn by 130770 MBC
	Checked 031265		Checked 130770 HAP
	Last Revision		____ Sheets Sheet ____

RCSL 44-R141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1			- x	0 Volts	1
2	P3-B			Adjustment	2
3	P2-A			$\overline{\text{Photocell}}$	3
4	P3-A			Test Point	4
5	B3-34		- x	$\overline{\Sigma\text{Character}}$	5
6	Ci 1b			Inhibit Wire 1b	6
7	Ci 1a			Inhibit Wire 1 a	7
8	B1-14		- x	Inhibit Time	8
9			- x	0 Volts	9
10	J6-A			$\overline{\text{IR 1}}$	10
11	B2-22		- x	$\overline{\text{Reset}}$	11
12	B5-4		- x	- 1,6 Volts	12
13	B5-35		- x	- 8 Volts	13
14	B5-33		- x	+ 8 Volts	14
15			- x -	0 Volts	15
16					16
17					17
18					18
19	J6-B		- x -	0 Volts	19
20					20
21	P4-5		- x -	0 Volts	21
22				Test Point	22
23	A14-3			$\overline{\text{RL0}}$	23
24					24
25					25
26					26
27					27
28					28
29					29
30					30
31					31
32	R 1b			Read Wire 1 b	32
33	R 1a			Read Wire 1 a	33
34	B1-31		- x	$\overline{\text{Strobe Reset}}$	34
35			- x -	- 8 Volts	35

Pos. B 6

OPTION TR 22

Unit: RC 2000	Designed BN	WIRING SCHEDULE	Drawing No R 20148
REGNECENTRALEN	Approved		Drawn by 130770 ABC
	Checked 031265		Checked 130770 HAP
	Last Revision		____ Sheets Sheet ____

RCSL44-RT141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1			- x -	0 Volts	1
2	P3-F			Adjustment	2
3	P2-B			$\overline{\text{Photocell}}$	3
4	P3-E			Test Point	4
5			- x -	$\overline{\Sigma\text{Character}}$	5
6	Ci 2b			Inhibit Wire 2 b	6
7	Ci 2a			Inhibit Wire 2 a	7
8			- x -	Inhibit Time	8
9			- x -	0 Volts	9
10	J6-C			$\overline{\text{IR 2}}$	10
11			- x -	$\overline{\text{Reset}}$	11
12	P4-J		- x -	- 1.6 Volts	12
13			- x -	- 8 Volts	13
14	P4-N		- x -	+ 8 Volts	14
15			- x -	0 Volts	15
16					16
17					17
18					18
19	B7-21		- x -	0 Volts	19
20					20
21	B7-19		- x -	0 Volts	21
22				Test Point	22
23	A14-6	A9-2		$\overline{\text{RL1}}$	23
24					24
25					25
26					26
27					27
28					28
29					29
30					30
31					31
32	R 2b			Read Wire 2 b	32
33	R 2a			Read Wire 2 a	33
34			- x -	$\overline{\text{Strobe Reset}}$	34
35			- x -	- 8 Volts	35

Pos. B 7

OPTION TR 22

Unit: RC 2000	Designed BN	WIRING SCHEDULE	Drawing No R 20149
REGNE CENTRALEN	Approved		Drawn by 130770 MBC
	Checked 031265		Checked 130770 HAP
	Last Revision		____ Sheets Sheet ____

RCSL 44-N141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1			- x -	0 Volts	1
2	P3-K			Adjustment	2
3	P2-D			$\overline{\text{Photocell}}$	3
4	P3-L			Test Point	4
5			- x -	$\overline{\Sigma\text{Character}}$	5
6	Ci 3b			Inhibit Wire 3 b	6
7	Ci 3a			Inhibit Wire 3 a	7
8			- x -	Inhibit Time	8
9			- x -	0 Volts	9
10	J6-E			$\overline{\text{IR 3}}$	10
11			- x -	$\overline{\text{Reset}}$	11
12			- x -	- 1,6 Volts	12
13			- x -	- 8 Volts	13
14	P7-A3		- x -	+ 8 Volts	14
15	B8-19		- x -	0 Volts	15
16					16
17					17
18					18
19	B 8-15		- x -	0 Volts	19
20					20
21			- x -	0 Volts	21
22				Test Point	22
23	A14-9	A9-3		$\overline{\text{RL2}}$	23
24					24
25					25
26					26
27					27
28					28
29					29
30					30
31					31
32	R 3b			Read Wire 3 b	32
33	R 3a			Read Wire 3 a	33
34			- x -	$\overline{\text{Strobe Reset}}$	34
35			- x -	- 8 Volts	35

Pos. B 8

OPTION TR 22

Unit: RC 2000	Designed BN	WIRING SCHEDULE	Drawing No R 20150
			Drawn by 130770 MBC
	Approved		Checked 130770 HAP
CENTRALEN	Checked 031265		____ Sheets Sheet ____
	Last Revision		

RCSL 44-R 141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1			- x -	0 Volts	1
2	P3-P			Adjustment	2
3	P2-E			$\overline{\text{Photocell}}$	3
4	P3-R			Test Point	4
5			- x -	$\overline{\Sigma\text{Character}}$	5
6	Ci 4b			Inhibit Wire 4 b	6
7	Ci 4a			Inhibit Wire 4 a	7
8			- x -	Inhibit Time	8
9	B11-19		- x -	0 Volts	9
10	J6-H			$\overline{\text{IR 4}}$	10
11			- x -	$\overline{\text{Reset}}$	11
12			- x -	- 1,6 Volts	12
13			- x -	- 8 Volts	13
14			- x -	+ 8 Volts	14
15	J6-NN		- x -	0 Volts	15
16					16
17					17
18					18
19	A10-19		- x -	0 Volts	19
20					20
21			- x -	0 Volts	21
22				Test Point	22
23	A14-12	A9-4		$\overline{\text{RL3}}$	23
24					24
25					25
26					26
27					27
28					28
29					29
30					30
31					31
32	R 4b			Read Wire 4 b	32
33	R 4a			Read Wire 4 a	33
34			- x -	$\overline{\text{Strobe Reset}}$	34
35			- x -	- 8 Volts	35

Pos. B 9

OPTION TR 22

Unit: RC 2000	Designed BN	WIRING SCHEDULE	Drawing No R 20151
			Drawn by 130770 MSC
	Approved		Checked 130770 HAP
RBGNE CENTRALEN	Checked 031265		____ Sheets Sheet ____
	Last Revision		

RCSL 44-R1141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1	B10-19		- x -	0 Volts	1
2	P3-U			Adjustment	2
3	P2-H			Photocell	3
4	P3-V			Test Point	4
5			- x -	ΣCharacter	5
6	Ci 5b			Inhibit Wire 5 b	6
7	Ci 5a			Inhibit Wire 5 a	7
8			- x -	Inhibit Time	8
9			- x -	0 Volts	9
10	J6-K			IR 5	10
11			- x -	Reset	11
12			- x -	- 1.6 Volts	12
13			- x -	- 8 Volts	13
14			- x -	+ 8 Volts	14
15			- x -	0 Volts	15
16					16
17					17
18					18
19	B10-1		- x -	0 Volts	19
20					20
21			- x -	0 Volts	21
22				Test Point	22
23	A14-17	A9-32		RL4	23
24					24
25					25
26					26
27					27
28					28
29					29
30					30
31					31
32	R 5b			Read Wire 5 b	32
33	R 5a			Read Wire 5 a	33
34			- x -	Strobe Reset	34
35			- x -	- 8 Volts	35

Pos. B 10

OPTION TR 22

Unit: RC 2000	Designed BN	WIRING SCHEDULE	Drawing No R 20152
			Drawn by 130770 MBC
REGNE CENTRALEN	Approved		Checked 130770 HAP
	Checked 031265		____ Sheets ____ Sheet ____
	Last Revision		

RCSL 44-R141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1			- x -	0 Volts	1
2	P3-AA			Adjustment	2
3	P2-J			Photocell	3
4	P3-X			Test Point	4
5			- x -	$\overline{\Sigma}$Character	5
6	Ci 6b			Inhibit Wire 6 b	6
7	Ci 6a			Inhibit Wire 6 a	7
8			- x -	Inhibit Time	8
9			- x -	0 Volts	9
10	J6-M			$\overline{\text{IR 6}}$	10
11			- x -	$\overline{\text{Reset}}$	11
12			- x -	- 1,6 Volts	12
13			- x -	- 8 Volts	13
14			- x -	+ 8 Volts	14
15			- x -	0 Volts	15
16					16
17					17
18					18
19	B9-9		- x -	0 Volts	19
20					20
21				0 Volts	21
22				Test Point	22
23	B14-9	A9-33		$\overline{\text{RL5}}$	23
24					24
25					25
26					26
27					27
28					28
29					29
30					30
31					31
32	R 6b			Read Wire 6 b	32
33	R 6a			Read Wire 5 a	33
34			- x -	$\overline{\text{Strobe Reset}}$	34
35			- x -	- 8 Volts	35

Pos. B 11

OPTION TR 22

Unit: RC 2000	Designed BN	WIRING SCHEDULE	Drawing No R 20153
	Approved		Drawn by 130770 MBC
REGNE CENTRALEN	Checked 031265		Checked 130770 HAP
	Last Revision		____ Sheets Sheet ____

RCSL 44-R 141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1			- x -	0 Volts	1
2	P3-FF			Adjustment	2
3	P2-L			$\overline{\text{Photocell}}$	3
4	P3-Z			Test Point	4
5			- x -	$\overline{\Sigma\text{Character}}$	5
6	Ci 7b			Inhibit Wire 7 b	6
7	Ci 7a			Inhibit Wire 7 a	7
8			- x -	Inhibit Time	8
9			- x -	0 Volts	9
10	J6-P			$\overline{\text{IR 7}}$	10
11			- x -	$\overline{\text{Reset}}$	11
12			- x -	- 1.6 Volts	12
13			- x -	- 8 Volts	13
14			- x -	+ 8 Volts	14
15			- x -	0 Volts	15
16					16
17					17
18					18
19			- x -	0 Volts	19
20					20
21	P4-R		- x -	0 Volts	21
22				Test Point	22
23	B14-3	A9-34		$\overline{\text{RL6}}$	23
24					24
25					25
26					26
27					27
28					28
29					29
30					30
31					31
32	R 7b			Read Wire 7 b	32
33	R 7a			Read Wire 7 a	33
34			- x -	Strobe Reset	34
35	J6-MM		- x -	- 8 Volts	35

Pos. B 12

OPTION TR 22

Unit: RC 2000	Designed BN	WIRING SCHEDULE	Drawing No R 2015
BEGNE CENTRALEN	Approved		Drawn by 140770
	Checked 031265		Checked 140770
	Last Revision		____ Sheets Sheet ____

RCSL 44-R 141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1	B13-9		x -	0 Volts	1
2	P3-LL			Adjustment	2
3	P2-M			Photocell	3
4	P3-DD			Test Point	4
5			x -	$\overline{\Sigma}$Character	5
6	Ci 8b			Inhibit Wire 8b	6
7	Ci 8a			Inhibit Wire 8a	7
8			x -	Inhibit Time	8
9	B13-1		x -	0 Volts	9
10	P6-S			$\overline{\text{IR 8}}$	10
11			x -	$\overline{\text{Reset}}$	11
12	A13-11		x -	- 1,6 Volts	12
13			x -	- 8 Volts	13
14	P6-PP		x -	+ 8 Volts	14
15			x -	0 Volts	15
16					16
17					17
18					18
19	P6-TT		x -	0 Volts	19
20					20
21			x -	0 Volts	21
22				Test Point	22
23	B14-6	A9-35		$\overline{\text{RL7}}$	23
24					24
25					25
26					26
27					27
28					28
29					29
30					30
31					31
32	R 8b			Read Wire 8b	32
33	R 8a			Read Wire 8a	33
34			x -	$\overline{\text{Strobe Reset}}$	34
35	A13-35		x -	- 8 Volts	35

Pos. B 13

OPTION TR 22

Unit: RC 2000	Designed BN	WIRING SCHEDULE	Drawing No R 20155
REGNECENTRALEN	Approved		Drawn by 140770 MBC
	Checked 031265		Checked 140770 HAP
	Last Revision		____ Sheets Sheet____

RCSL 44-R141

PIN	Wired To	Wired To	- x -	Name of Signal	PIN
1	J1-AA				1
2	J1-BB				2
3	B12-23			$\overline{RL6}$	3
4	J1-CC				4
5	J1-DD				5
6	B13-23			$\overline{RL7}$	6
7	J1-Y				7
8	J1-Z				8
9	B11-23			$\overline{RL5}$	9
10	J1-F				10
11	J1-E				11
12	A0-25			ZFB*	12
13	J1-J				13
14	A14-14		x	+ 8 Volts	14
15	B14-21		x -	0 Volts	15
16	J1-H				16
17	B5-22			PO	17
18	J1-K				18
19	J1-L				19
20	B1-8			$\overline{S6}$	20
21	B14-15	B14-27		0 Volts	21
22	B1-7			$\overline{S7}$	22
23	B5-3			$\overline{sRA}$	23
24	J1-B				24
25	J1-A				25
26	B5-1			Forced Reset	26
27	B14-21			0 Volts	27
28	A14-28			- 8 Volts	28
29	J1-KK				29
30	A14-30			- 24 Volts	30
31	J1-LL				31
32					32
33					33
34					34
35					35

Pos. B14 0924-2

OPTION TR 22

Unit: RC 2000	Designed JØG	WIRING SCHEDULE	Drawing No R 20156
	Approved EK		Drawn by EK 27/10-69
ASEA CENTRALEN	Checked		Checked
	Last Revision		____ Sheets Sheet ____

RCSL 44-R 141

PIN	Wired To	Wired To	Name of Signal	PIN
A	B14-25	8	Start	A
B	B14-24		-, Start	B
C	A14-18	8	Reader Busy	C
D	A14-19		-, Reader Busy	D
E	B14-11	8	ZFB	E
F	B14-10		-, ZFB	F
H	B14-16	8	Paper Out	H
J	B14-13		-, Paper Out	J
K	B14-18	8	64 char. stored	K
L	B14-19		-, 64 char. stored	L
M	A14-1	8	Data(1)	M
N	A14-2		-, Data(1)	N
P	A14-4	8	Data(2)	P
R	A14-5		-, Data(2)	R
S	A14-7	8	Data(3)	S
T	A14-8		-, Data(3)	T
U	A14-10	8	Data(4)	U
V	A14-11		-, Data(4)	V
W	A14-13	8	Data(5)	W
X	A14-16		-, Data(5)	X
Y	B14-7	8	Data(6)	Y
Z	B14-8		-, Data(6)	Z
AA	B14-1	8	Data(7)	AA
BB	B14-2		-, Data(7)	BB
CC	B14-4	8	Data(8)	CC
DD	B14-5		-, Data(8)	DD
EE				EE
FF				FF
HH				HH
JJ				JJ
KK	B14-29	8	-, RC 2000 Connected	KK
LL	B14-31		RC 2000 Connected	LL
MM	B10-21		0 V	MM
NN	B11-21		0 V	NN
PP	B12-21		0 V	PP
RR	B13-21		0 V	RR
SS	Chassis			SS
TT	Chassis			TT

Signal Names in RC 4000 notation

Jack 1

OPTION TR 22

Unit: RC 2000	Designed JOG	Jack 1 for RC 4000	Drawing No R 20155
	Approved EK		Drawn by EK/11 26.
REGNECENTRALEN	Checked		Checked
	Last Revision		Sheets / Sheet

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PIN	Wired To	Wired To	Name of Signal	PIN
A	B6-10		$\overline{IR\ 1}$	A
B	B6-19	→	0 Volts	B
C	B7-10		$\overline{IR\ 2}$	C
D			0 Volts	D
E	B8-10		$\overline{IR\ 3}$	E
F			0 Volts	F
H	B9-10		$\overline{IR\ 4}$	H
J			0 Volts	J
K	B10-10		$\overline{IR\ 5}$	K
L			0 Volts	L
M	B11-10		$\overline{IR\ 6}$	M
N			0 Volts	N
P	B12-10		$\overline{IR\ 7}$	P
R			0 Volts	R
S	B13-10		$\overline{IR\ 8}$	S
T			0 Volts	T
U	B3-10		$\overline{\Sigma Characters}$	U
V	B3-14	→	0 Volts	V
W				W
X				X
Y				Y
Z				Z
AA	A0-25		ZFB*	AA
BB	B2-5		ZFB x Ext. Contr.	BB
CC	A1-3		$\overline{S\ 7}$	CC
DD				DD
EE	B4-21		$\overline{Ext.\ Reset}$	EE
FF				FF
HH	A0-23		$\overline{Ext.\ Control}$	HH
JJ				JJ
KK				KK
LL				LL
MM	B12-35		-8 Volts	MM
NN	B9-15		0 Volts	NN
PP	B13-14		+8 Volts	PP
RR	B4-17		-24 Volts	RR
SS	⌐		⌐	SS
TT	⌙ B13-19		⌙ Screen	TT

Jack 6

OPTION TR 22

Unit: RC 2000	Designed B. N.	Jack for Input	Drawing No R 20157
CENTRALEN	Approved		Drawn by L. N. 1.27.6.67
	Checked 3.12.65		Checked F. E. 1.9.67
	Last Revision 3.10.67		1 Sheets / Sheet 1

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