

TITLE:

CR80 AMOS  
COMPLETION CODES

DOCUMENT NO:

CSS/006/USM/0057

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CR80 AMOS COMPLETION CODES

sign/date	page
SMA/810319	
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1	810319	SMA	JHØ	JHØ

SECTION

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1. SCOPE

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2. COMPLETION CODES

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1. SCOPE

This document is a compilation of a number of standard completion codes generated by various AMOS system components.

## 2. COMPLETION CODES

A completion code is composed of two fields.

bit 0-7 : error code  
bit 8-14 : subsystem code

The following subsystem codes are used:

0 : user, kernel  
1 : kernel  
2 : I/O system  
3 : file management system  
4 : file management system  
5 : file management system  
6 : device driver  
7 : pascal runtime

Kernel Error Codes

Kernel errors corresponds to completion codes

£0XX,£1XX

## Kernel Completion codes from Critical Region Procedures

CODE	DESCRIPTION
001	UNKNOWN FUNCTION
002	REF VIOLATES ADDR SPACE OF PROC
003	UNKNOWN REGION
004	REGION IS NOT ENTERED
005	INVALID PROCESS INDEX (PCB INDEX)
006	INVALID REGION NAME
007	ADDR VIOLATION IN VARIABLE SPACE
008	TOO MANY REGIONS

## Kernel Completion codes from Create Process

CODE	DESCRIPTION
001	NO VACANT PCB'S (TOO MANY PROCESSES)
002	REF VIOLATES ADDR SPACE OF PROC
003	CLASSIFICATION TOO HIGH
004	TOO MANY CAPABILITIES
005	INVALID NAME
006	INVALID CPU
007	INVALID PRIORITY
008	MANAGE OVERRUN THREAT

CODE	DESCRIPTION
101	TRAP OR ILLEGAL INSTR EXECUTED
102	PARITY ERROR ENCOUNTERED
103	TIME OUT (ILLEGAL ADDRESSING)
105	REF IS MADE TO A NOT EXISTING PROC
106	PARAM REF EXCEEDS THE LOCAL PROC MEMORY AREA
107	INVALID EVENT PARAMETER
108	CALLING PROC IS NOT SENDER OR RECEIVER OF THIS MESSAGE BUFFER
109	INVALID MESSAGE BUFFER STATE FOR THIS FUNCTION
10A	INVALID INTRPT PARAMETER
10B	INVALID INTRPT PARAMETER
10C	INVALID ITEM TYPE
10D	ATTEMPT TO USE TOO MANY MESSAGE BUFFERS
10E	NOT IMPLEMENTED FUNCTION
11E	PROC NOT ALLOWED TO CALL CREATE PROCESS

I/O System Error Codes

I/O system errors correspond to completion codes

£2XX

CODE	DESCRIPTION
201	END OF FILE
202	NO FILE DESCRIPTORS
203	ILLEGAL FILE DESCRIPTOR
204	NO I/O CONTROL BLOCK
205	ILLEGAL I/O CONTROL BLOCK
206	NO STREAMS
207	ILLEGAL STREAM
208	NO TRANSFER LIST ELEMENTS
209	ILLEGAL ADDRESS
20A	ILLEGAL BLE
20B	FILE NOT OPEN
20C	DIFFERENT FILE SYSTEM
20D	UNKNOWN FILE SYSTEM
20E	ILLEGAL COMMAND
20F	I/O SYSTEM ERROR
210	NOT ENOUGH SPACE
211	ILLEGAL MODE
212	ILLEGAL MEMORY PARAMETER
213	NO BUFFER SPACE
214	NOT CONNECTED
215	NOT OUTPUT MODE
216	NOT INPUT MODE
217	ELEMENT OVERFLOW
218	SYNTAX ERROR



File Management System Error Codes

The FMS error codes correspond to completion codes

£3XX, £4XX, £5XX

## System Errors

CODE	DESCRIPTION
300	DCB POOL EMPTY
301	UCB POOL EMPTY
302	FCB POOL EMPTY
303	CAP POOL EMPTY
304	DDCB POOL EMPTY
306	ALLOCSIZE TOO LARGE
307	NO PUBLIC ACCESS RIGHTS
308	ILLEGAL PAGE NUMBER
309	ILLEGAL BFD ENTRY
30A	ILLEGAL FCB LINKS
30C	INCONSISTENT LIST
30D	ILLEGAL SYSTEM FILE
30E	RECOVERY STACK OVERFLOW
310	UNEXPECTED DRIVER ANSWER
311	CANNOT BAD MARK
313	UNKNOWN OPTYPE
314	INVALID SECT DESC PTR
315	ILLEGAL WRITE ID
316	ALREADY WRITE CLAIMED
317	NOT WRITE CLAIMED
318	INVALID DEVICE
319	ILLEGAL SECTOR NBR

## User Errors

CODE	DESCRIPTION
400	NON-EXISTING DEVICE
401	ILLEGAL DEVICE KIND
402	ILLEGAL CR80 ADDRESS
403	DEVICE NAME IN USE
404	ILLEGAL UNIT
405	ILLEGAL SUBUNIT
406	WRONG VOLUME NAME
407	NON-EXISTING VOLUME
408	VOLUME MOUNTED
409	DIFFERENT VOLUMES
40A	ILLEGAL FILE DESCRIPTOR
40B	ILLEGAL FILE ORGANISATION
40C	ILLEGAL ALLOCATION SIZE
40D	ILLEGAL AREA SIZE
40E	ILLEGAL WRITE
40F	FILE CLOSED
410	FILES OPEN
411	NO FILE TO ACCEPT
412	NON-EXISTING USER
413	USER ALREADY ACTIVE
414	NO CONNECTION
416	ILLEGAL CALLER
417	OTHER USERS
419	OUT OF RANGE
41B	FILE FULL
41C	ACCESS CONTROL LIST FULL
41D	PROTECTION FAILURE

CODE	DESCRIPTION
420	ILLEGAL DIRECTORY
421	NAME ALREADY EXISTS
422	NON-EXISTING NAME
423	NOT ALLOCATABLE
424	VOLUME THRESHOLD EXCEEDED
425	FILE THRESHOLD EXCEEDED
426	VOLUME THRESHOLD TOO LARGE
428	ILLEGAL FILE INFORMATION TYPE
429	NOT A DUAL DISK
42A	ILLEGAL DISCARD
42C	UNKNOWN COMMAND
42E	BAD SECTOR TABLE FULL
42F	UNIT IS USED
430	VOLNAME IS USED
431	NOT RESERVED
432	ALREADY RESERVED
433	TAPE NOT INITIATED
434	TAPE WRITE PROTECTED
435	BEGINNING OF TAPE SENSED
436	END OF TAPE SENSED
437	LOGICAL END OF TAPE SENSED
438	FILEMARK SENSED
439	INVALID RECORD LENGTH
43A	SHORT DATA RECORD
43B	LONG DATA RECORD
43C	TAPE MUST BE LABELLED
43D	FILE MUST BE OPEN FOR INPUT

CODE	DESCRIPTION
442	WRITE PROTECTED DRIVE
443	UNEXPECTED DISK INTERFACE STATUS
444	DATA OR SYNC ERROR
445	ADDRESS OR SYNC ERROR
446	BAD SECTOR
447	WRITE PROTECTED SECTOR
448	ILLEGAL SECTOR
449	TIMING ERROR
44A	SUBBUS OVERRUN
44B	PARITY ERROR
44C	BAD SECTOR READ
44D	BAD HOME BLOCK
452-45D	Same as 442-44D except that these are for disk 0 of dual disk.
462-46D	Same as 442-44D except that these are for disk 1 of dual disk.

## DMA Errors

CODE	DESCRIPTION
501	PORT ERROR
502	ILLEGAL DMA COMMAND
503	TRANSFER LIST ERROR
504	TRANSMISSION ERROR
505	ABORTED
506	STOPPED

Device Driver Error Codes

Completion codes corresponding to device driver error codes are

£6XX

CODE	DESCRIPTION
600	DEVICE FAILURE

## TDX Driver

CODE	DESCRIPTION
601	CONSECUTIVE NAK'S,GIVEUP
605	BUFFER OVERFLOW(INPUT) FRAME BUFFER TIMEOUT(OUTPUT)
606	DMA ERROR
607	TIMEOUT
609	FORMAT ERROR IN COMMAND
60B	ILLEGAL LOG-LINE-ID
60C	UNKNOWN PROTOCOL TYPE
60D	NO AVAILABLE PROTOCOL COROUTINE
60E	NO AVAILABLE PROTOCOL DESCRIPTOR
60F	NO AVAILABLE DATA BUFFER LINK
613	LINE BUSY
615	ILLEGAL SPEED INDEX

Pascal Runtime Error Codes

Pascal runtime error codes correspond to completion codes

£7XX

CODE	DESCRIPTION
701	ARITHMETIC OVERFLOW
702	POINTER ERROR
703	RANGE ERROR
704	VARIANT ERROR
705	HEAP LIMIT
706	STACK LIMIT
720	MISMATCH
721	TRACE ERROR
722	NOT A PASCAL PROGRAM
723	INITIALIZATION ERROR
724	I/O ERROR IN PREFIX ROUTINE RUN

**TITLE:** DAMOS COMPLETION CODES,  
VERSION 0303

**DOCUMENT NO :** CPS/USM/008

**PREPARED BY :** Lise Schmidt

*L. R. Schmidt*

**AUTHORIZED BY:** Kurt Nybroe-Nielsen



**DISTRIBUTION :** SW Team, SYS I+T, LIB 5, Conf. mngt., JD

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**TITLE:** DAMOS COMPLETION CODES,  
VERSION 0302

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**PREPARED BY :** Lise Schmidt *L.R. Schmidt*

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## REVISION RECORD

CHANGE	DATE	AUTHORIZATION OF CHANGE	PAGES AFFECTED	BRIEF DESCRIPTION OF CHANGE
	840202		All	Original Issue of Document concerning version 0302 of DAMOS Completion Codes.
Issue 1	840223	Change no. 1 New version 0303 of DAMOS	8a, 8b, 34, 35	New CC
Issue 1.1		Change No.2	46	New TDX FW CC

DAMOS COMPLETION CODES

update	840223	page	3
replace	ISSUE 1	project	CAMPS

"completion codes  
"=====

```

const
  OK = 0;

  security_violation_position = 15;

  security_violation = 1 SHIFTL security_violation_position;
    
```

"module identifiers  
"=====

	<u>CC</u>	<u>Page</u>
TMS HANDLER	= #34, #38, #39;	4
CPU_interrupt_firmware_error	= #0;	5
IOS_error	= #2;	7
FMS_internal_error	= #3;	8a
FMS_user_error	= #4;	8a
FMS_DMA_error	= #5;	8b
MULDIV_LONG_error	= #8;	9
File-Name_error	= #9;	10
Process_Management_error	= #A;	11
Page_Manager_error	= #B;	13
Process_Communication_error	= #C;	16
Directory_Functions_error	= #D;	17
Real_Time_Clock_error	= #E;	18
Device_Management_error	= #10;	19
Error_Processing_error	= #11;	20
Resource_Management_error	= #12;	21
Initialization_error	= #14;	22
ROOT_error	= #15;	23
SM_error	= #16;	24
Xfer_Module_error	= #1F;	25
CMON_error	= #21;	26
TMS_error	= #28;	27
LOG_module_error	= #2A;	28
Trace_error	= #2B;	29
MT_Handler_error	= #31;	30
OC_Handler_error	= #32;	31
LP_Handler_error	= #33;	32
LTU_Handler_error	= #34;	33
Disk_Handler_error	= #35;	34
Floppy_Disk_Handler_error	= #36;	36
DMA_Handler_error	= #37;	37
STI_Handler_error	= #38;	38
TDX_Handler_error	= #39;	39
VDU_1_Protocol_error	= #3A;	41
HLR_support_error	= #3B;	42
TOS_error	= #40;	43
Ada_runtime_error	= #41;	45
TDX_FW_error	= #43;	46

```

MODULE NAME:          TMS HANDLER
MODULE ID NMB:       CSS/9125
MODULE VERSION:      02
MODULE RELEASE:      02
RELEASE DATE:        831006

```

#24, = 38, #29

%NOLIST %NOLIST %NOLIST

"Standard completion codes.  
 "The codes must be prefixed by the device handler  
 "or the protocol cc prefix

Version 630705                      84:01:26 13:51:00.689                      Page: 51                      Source: TMS\_H

```

const
"request_queued          = #01 ;
unknown_function        = #02 ;
not_implemented         = #03 ;
device_fault           = #04 ;
not_allowed            = #05 ;
bad_subdevice_state    = #06 ;
no_resources           = #07 ;
bad_param_size         = #08 ;
bad_protocol           = #09 ;
bad_h_scid             = #0A ;
too_many_requests      = #0B ;
bad_h_opid             = #0C ;
bad_blocksize          = #0D ;
invalid_se             = #0E ;
bad_sda                = #0F ;
bad_device_state       = #10 ;
bad_buffer_type        = #11 ;
buffer_layout_error    = #12 ;
bad_param              = #13 ;
subdevice_in_use       = #14 ;
DCB_too_small          = #15 ;
external_length_too_small
                        = #16 ;
bad_function           = #17 ;
protocol_fault         = #18 ;
too_many_subdevices    = #19 ;
no_buffer_resources    = #1A ;

                        "bad parameter in INITIALIZE or OPEN_SUBDEVICE
                        "subdevice already opened
                        "DCB size is less than required by the handler
                        "
                        "bad function in control_output (assumed
                        "that one of the data bytes holds a function code
                        "protocol fault has occurred
                        "the requested number of subdevices could not
                        "be allocated (specification in include_record)
                        "the buffers could not be allocated

```

"Standard status codes.  
 "The codes must be prefixed by the device handler or the protocol  
 "cc prefix.

```

const
device_failure = device_fault;

```

03.00075 " Constants related to interrupt processing by CPU firmware:

03.00076

03.00077 const

03.00078 unassigned = 00;  
03.00079 CPU\_interrupt = 01;  
03.00080 IO\_interrupt = 02;  
03.00081 p\_pagefault = 03;  
03.00082 d\_pagefault = 04;  
03.00083 timer\_interrupt = 05;  
03.00084 trace\_interrupt = 06;  
03.00085 illegal\_instruction = 07;  
03.00086 p\_parity = 08;  
03.00087 d\_parity = 09;  
03.00088 io\_parity = #A;  
03.00089 parity\_on\_map\_access = #B;  
03.00090 map\_ttbl\_parity = #C;  
03.00091 p\_timeout = #D;  
03.00092 d\_timeout = #E;  
03.00093 io\_timeout = #F;  
03.00094 map\_timeout = #10;  
03.00095 p\_protection = #11;  
03.00096 d\_protection = #12;

#0 (k)

HX

Swell Compiler Version 830705

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03.00097 bound\_violation = #13;  
03.00098 p\_privilege = #14;  
03.00099 pp\_privilege = #15;  
03.00100 context\_stack\_overflow = #16;  
03.00101 context\_stack\_underflow = #17;  
03.00102 emergency = #18;  
03.00103 inconsistency = #19;

03.00105 " Addresses of GPS locations where interrupt views are found:

03.00106

03.00107 const

03.00108 CPU\_interrupt\_view = 32;  
03.00109 error\_interrupt\_view = 33;  
03.00110 page\_interrupt\_view = 34;  
03.00111 timer\_interrupt\_view = 35;  
03.00112 io\_interrupt\_view = 36;

03.00114 " Addresses of GPS locations where interrupt entry points  
03.00115 " are found:

03.00116

03.00117 const

03.00118 CPU\_interrupt\_location = 55;  
03.00119 emergency\_location = 56;  
03.00120 trace\_interrupt\_location = 57;  
03.00121 error\_interrupt\_location = 58;  
03.00122 page\_interrupt\_location = 59;  
03.00123 timer\_interrupt\_location = 60;  
03.00124 context\_stack\_overflow\_location = 61;  
03.00125 context\_stack\_underflow\_location = 62;

03.00127 " Addresses of GPS locations where addresses of IVT and  
03.00128 " LDT are found:

03.00129

03.00130 const

03.00131 IVT\_address\_location = 37;  
03.00132 LDT\_address\_location = 38;  
03.00133

03.00056

03.00057

03.00058 " Definitions related to PSW register:

03.00059

03.00060 const

03.00061 timer\_mask\_position = 15;

03.00062 notification\_mask\_position = 14;

03.00063 alternative\_instruction\_set\_position = 12;

03.00064 CPU\_state\_position = 11;

03.00065 CPU\_number\_position = 8;

03.00066 CPU\_number\_width = 3;

03.00067 Z\_position = 7;

03.00068 S\_position = 6;

03.00069 V\_position = 5;

03.00070 C\_position = 4;

03.00071 trace\_mask\_position = 1;

03.00072 modification\_position = 0;

03.00073

#0 2/2

IDS\_CC.I

```

%LIST      %LIST      %LIST
-----
"
"
"  MODULE NAME:          ID System Completion Codes
"  MODULE ID NMB:       CSS/9109
"  MODULE VERSION:      02
"  MODULE RELEASE:      01
"  RELEASE DATE:        821019
"
-----
%NOLIST    %NOLIST    %NOLIST

```

```

"IDS COMPLETION CODES"
-----

```

CONST

```

ID_OK                = 0;          EOF                = #201;
no_FDCBs_available  = #202;        illegal_FDCB      = #203;
no_IDCBs_available  = #204;        illegal_IDCB      = #205;
no_streams_available = #206;        illegal_stream    = #207;
no_TLEs_available   = #208;        illegal_address   = #209;
illegal_SLE         = #20A;        file_not_open     = #20B;
different_file_systems = #20C;
illegal_command     = #20E;        IO_system_error   = #20F;
not_enough_space    = #210;        illegal_mode      = #211;

```

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Source:

```

not_connected       = #214;        not_output_mode   = #215;
not_input_mode      = #216;        element_overflow  = #217;
syntax_error        = #218;
no_FSCBs_available  = #21A;        no_TTCBs_available = #21B;
no_TPEs_available   = #21C;
invalid_IDCB_index_received = #21D;
not_a_sync_element  = #21E;        buffer_too_short  = #21F;
no_of_IDCBs_lt_2    = #220;        no_buffers_available = #221;

inconsistent_IDCB   = #222;        inconsistent_IDCB_a_c_b = #223;
inconsistent_TTBL   = #224;        unable_to_deallocate_port = #225;
unable_to_allocate_IDCB = #226;    unable_to_unlock_pages = #227;
unable_to_dismantle_sync_element = #228;

only_one_sector_allowed = #229;    xfer_exceeds_size = #22A;

```

```

"IDS COMPATIBILITY DECLARATIONS OF ERROR CODES"
-----

```

```

no_FDs_available    = no_FDCBs_available;
illegal_FD          = illegal_FDCB;
no_XFELEMs_available = no_TLEs_available;
unknown_file_system = #D0B;

```

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	replace ISSUE 1	project CAMPS

MODULE NAME: FMS COMPLETION CODES  
 MODULE ID NMB: CSS/9107  
 MODULE VERSION: 03  
 MODULE RELEASE: 05  
 RELEASE DATE: 840120

#3, #4, #5  
(1/2)

%NOLIST %NOLIST %NOLIST

onst

"system errors"

-----"

DCB_pool_empty	= £300;		
allocation_size_too_large	= £306;	ACL_public_user	= £307;
illegal_page_nbr	= £308;	illegal_BFD_entry	= £309;
illegal_FC2_links	= £30A;		
inconsistent_list	= £30C;	illegal_system_file	= £30D;
recovery_stack_overflow	= £30E;		
unexpected_driver_answer	= £310;		
sector_not_present	= £312;	unknown_dc_op_type	= £313;
invalid_sector_descriptor	= £314;	illegal_writer_identity	= £315;
already_write_claimed	= £316;	not_write_claimed	= £316;
invalid_device	= £318;	illegal_sector_number	= £319;

#3

"user errors"

-----"

nonexisting_device	= £400;	illegal_device_kind	= £401;
nonexisting_handler	= £402;	device_name_in_use	= £403;
illegal_unit	= £404;	illegal_subunit	= £405;
wrong_volume_name	= £406;	nonexisting_volume	= £407;
volume_mounted	= £408;	different_volumes	= £409;
illegal_file	= £40A;	illegal_organization	= £40B;
illegal_alloc_size	= £40C;	illegal_area_size	= £40D;
illegal_write	= £40E;	file_closed	= £40F;
files_open	= £410;	illegal_offer_id	= £411;
nonexisting_user	= £412;	user_already_active	= £413;
no_connection	= £414;	illigal_usergroup	= £415;
illegal_caller	= £416;	other_users	= £417;
not_contiguous	= £418;	out_of_range	= £419;
		file_full	= £41B;

#4

CONTINUED

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## DAMOS COMPLETION CODES

#4 (2/2)

```

acl_full = £41C;      protection_failure = £41D;
illegal_directory = £420;  name_exists = £421;
nonexisting_name = £422;  not_allocatable = £423;
                               kind_unit_conflict = £425;

kind_ctrlr_conflict = £426;
illegal_file_inf_type = £428;  not_a_dual_disk = £429;
illegal_discard = £42A;  incompatible_devices = £42B;
unknown_command = £42C;  bad_sector_during_format = £42D;
                               unit_is_in_use = £42F;
                               device_not_reserved = £431;
                               device_not_single = £433;
                               illigal_icd_index = £435;
                               illigal_finish_dualize = £437;
                               insufficient_ucbs = £439;
                               illegal_start_dualize = £43B;
                               dualizingwithoutmount = £43D;

volume_name_is_in_use = £430;
device_already_reserved = £432;
device_not_dual = £434;
unknown_volume_use = £436;
illegal_user_type = £438;
insufficient_files = £43A;
not_dualizing = £43C;
tomanysubunits = £43E;

                               bad_sector_read = £441;
                               bad_sector_in_asf = £443;

corrupt_homeblock = £442;
BST_full = £444;
bad_sector_replaced = £448;
different_volume_names = £44A;
not_assigned_dual = £44C;
not_mounted_dual = £44E;
bst_size_too_big = £450;
no_disk_change = £452;

                               last_bad_sector_replaced = £449;
                               different_access_dates = £44B;
                               not_dismounted_dual = £44D;
                               sector_count_conflict = £44E;
                               not_empty_directory = £451;

```

## transfer errors"

#5

```

source_xferlist_exhausted = £500;  dest_xferlist_exhausted = £501;
port_mismatch = £502;  no_awaiting_port = £503;

```

end of fms\_cc.i"



MULDIV\_CC.I

#8

%LIST %LIST %LIST

```

"
"
" MODULE NAME:          MULDIVLONG
" MODULE ID NMB:       CSS/9113
" MODULE VERSION:      02
" MODULE RELEASE:      01

```

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```

" RELEASE DATE:      821019
"

```

%NCLIST %NCLIST %NCLIST

```

const
  muldiv_illegal_address = muldiv_long_error shiftl 3 ior #01;

```



IST %LIST %LIST

=P(1/2)

MODULE NAME: PROCESS MANAGEMENT AND DISPATCHER COMPLETION CODES
MODULE ID NMB: CSS/9116
MODULE VERSION: 02
MODULE RELEASE: 02
RELEASE DATE: 840117

%NOLIST %NOLIST %NOLIST

"Process Management and Dispatcher completion codes:
"=====

const

PMD\_cc= Process\_Management\_Error shiftl 8;

const

" Completion Codes returned to the caller:

- PMD\_illegal\_ready = #01 + PMD\_cc;
PMD\_too\_many\_caps = #02 + PMD\_cc;
PMD\_process\_not\_cleaned = #03 + PMD\_cc;
PMD\_remote\_access\_not\_allowed = #04 + PMD\_cc;
PMD\_illegal\_PCB\_index = #05 + PMD\_cc;
PMD\_time\_limit = #06 + PMD\_cc;
PMC\_impossible\_to\_access\_child\_PPS = #07 + PMD\_cc;
PMD\_illegal\_UGI = #08 + PMD\_cc;
PMD\_illegal\_low\_UGI = #09 + PMD\_cc;
PMD\_illegal\_high\_UGI = #0A + PMD\_cc;
PMD\_illegal\_child\_level = #0B + PMD\_cc;
PMD\_no\_IT\_available = #0C + PMD\_cc;
PMD\_only\_one\_IT\_available = #0D + PMD\_cc;
PMD\_passivate\_idle\_not\_allowed = #0E + PMD\_cc;
PMD\_illegal\_state\_for\_passivation = #0F + PMD\_cc;
PMD\_illegal\_state\_for\_resume = #10 + PMD\_cc;
PMD\_process\_not\_passive = #11 + PMD\_cc;
PMD\_illegal\_priority = #12 + PMD\_cc;
PMD\_unknown\_change\_type = #13 + PMD\_cc;
PMD\_illegal\_reg\_no = #14 + PMD\_cc;
PMC\_unknown\_processor\_pool\_change\_type = #15 + PMD\_cc;
PMD\_illegal\_creation\_block\_address = #16 + PMD\_cc;
PMC\_illegal\_RAT\_spec\_address = #17 + PMD\_cc;
PMD\_illegal\_process\_attributes\_address = #18 + PMD\_cc;
PMC\_illegal\_local\_attributes\_address = #19 + PMD\_cc;
PMD\_illegal\_process\_context\_address = #1A + PMD\_cc;
PMD\_illegal\_process\_change\_attributes\_address = #1B + PMD\_cc;
PMD\_illegal\_CPU\_pool\_attributes\_address = #1C + PMD\_cc;
PMD\_illegal\_CPU\_pool\_change\_attributes\_address = #1D + PMD\_cc;
PMD\_illegal\_CPU\_attributes\_address = #1E + PMD\_cc;
PMD\_wrong\_level = #1F + PMD\_cc;
PMD\_illegal\_user\_params\_address = #20 + PMD\_cc;
PMC\_params\_too\_large = #21 + PMD\_cc;
PMD\_too\_many\_remote\_caps = #22 + PMD\_cc;
PMD\_illegal\_BOUND\_map = #23 + PMD\_cc;
PMD\_environment\_error = #24 + PMD\_cc;

61	PMD_illegal_return_from_init_call	= #25 + PMD_cc;
62	PMD_illegal_return_from_clean_call	= #26 + PMD_cc;
63	PMD_init_error	= #27 + PMD_cc;
64	PMD_not_yet_passivated	= #28 + PMD_cc;
65	PMD_program_segment_location_conflict	= #29 + PMD_cc;
66	PMD_change_idle_not_allowed	= #2A + PMD_cc;
67	PMD_not_implemented	= #2B + PMD_cc;
68	PMD_bad_environment_pointer	= #2C + PMD_cc;
69	PMD_illegal_state_for_clean_up	= #2D + PMD_cc;
70	PMD_process_was_never_executed	= #2E + PMD_cc;
71	PMD_checksum_error	= #2F + PMD_cc;
72	PMD_already_ready	= #30 + PMD_cc;

" Initialization Errors calling init\_error:

76	PMD_no_space_for_CPU_pool_CS	= #40 + PMD_cc;
77	PMD_no_space_for_CPUCB	= #41 + PMD_cc;
78	PMD_unknown_CPU_pool	= #42 + PMD_cc;
79	PMD_no_space_for_PCB	= #43 + PMD_cc;
80	PMD_no_space_for_TTCB	= #44 + PMD_cc;
81	PMD_no_obj_index_for_CPU	= #45 + PMD_cc;
82	PMD_insert_CPU_not_allowed	= #46 + PMD_cc;
83	PMD_no_obj_index_for_CPU_pool	= #47 + PMD_cc;
84	PMD_insert_CPU_pool_not_allowed	= #48 + PMD_cc;
85	PMD_create_idle_data_not_allowed	= #49 + PMD_cc;
86	PMD_no_obj_index_for_idle_data	= #4A + PMD_cc;
87	PMD_insert_idle_data_not_allowed	= #4B + PMD_cc;
88	PMD_create_idle_program_not_allowed	= #4C + PMD_cc;
89	PMD_no_obj_index_for_idle_program	= #4D + PMD_cc;
90	PMD_insert_idle_program_not_allowed	= #4E + PMD_cc;
91	PMD_convert_idle_program_not_allowed	= #4F + PMD_cc;
92	PMD_unlock_idle_program_not_allowed	= #50 + PMD_cc;
93	PMD_create_idle_PPS_not_allowed	= #51 + PMD_cc;
94	PMD_no_obj_index_for_idle_PPS	= #52 + PMD_cc;
95	PMD_insert_idle_PPS_not_allowed	= #53 + PMD_cc;
96	PMD_no_obj_index_for_idle	= #54 + PMD_cc;
97	PMD_insert_idle_not_allowed	= #55 + PMD_cc;
98	PMD_initializing_CPU_not_included	= #56 + PMD_cc;

" Initialization Errors calling PU\_error:

0	PMD_ROOT_CPU_pool_not_found	= #60 + PMD_cc;
1	PMD_ROOT_PCB_not_found_at_get_basic	= #61 + PMD_cc;
2	PMD_CPUCB_not_found_at_get_basic	= #62 + PMD_cc;
3	PMD_CPU_pool_CS_not_found_at_get_basic	= #63 + PMD_cc;
4	PMD_ROOT_PCB_not_found_at_install	= #64 + PMD_cc;
5	PMD_PM_create_idle_error	= #65 + PMD_cc;
6	PMD_CPUCB_not_found_at_install	= #66 + PMD_cc;
7	PMD_create_idle_resources_not_allowed	= #67 + PMD_cc;
8	PMD_idle_stack_overflow	= #68 + PMD_cc;
9	PMD_ROOT_PCB_not_found_at_start_system	= #69 + PMD_cc;
0	PMD_CPUCB_not_found_at_notify	= #6A + PMD_cc;

" Errors causing PU\_error:

1	PMD_queue_inconsistency	= #80 + PMD_cc;
2	PMD_free_PCB_inconsistency	= #81 + PMD_cc;
3	PMD_TTCB_inconsistency	= #82 + PMD_cc;
4	PMD_unlock_pages_not_allowed	= #83 + PMD_cc;
5	PMD_ROOT_retire	= #84 + PMD_cc;
6	PMD_idle_retire	= #85 + PMD_cc;
7	PMD_cur_CPU_not_found	= #86 + PMD_cc;
8	PMD_cur_proc_not_found	= #87 + PMD_cc;
9	PMD_context_stack_error	= #88 + PMD_cc;

```

06.00001
06.00002 %LIST      %LIST      %LIST
06.00003 "-----# 2 3
06.00004 "
06.00005 "  MODULE NAME:          PAGE MANAGER, (COMPLETION-CODES)
06.00006 "  MODULE ID NMB:       CSS/9117
06.00007 "  MODULE VERSION:      02
06.00008 "  MODULE RELEASE:      04
06.00009 "  RELEASE DATE:       831104
06.00010 "
06.00014 "PM Completion Codes
06.00015 "=====
06.00016
06.00017 const
06.00018   pm_cc = page_manager_error SHIFTL 8;
06.00019
06.00020 "errors are returned to the caller
06.00021 "range #01..#3F
06.00022
06.00023   PM_segment_size_out_of_range           = #01 + pm_cc;
06.00024   PM_segment_subtype_out_of_range       = #02 + pm_cc;
06.00025   PM_mem_type_out_of_range              = #03 + pm_cc;
06.00026   PM_segment_already_mapped_in         = #04 + pm_cc;
06.00027   PM_lpn_out_of_range                   = #05 + pm_cc;
06.00028   PM_no_place_for_mapping_segment_in    = #06 + pm_cc;
06.00029   PM_segment_already_mapped_out        = #07 + pm_cc;
06.00030   PM_segment_locked                     = #08 + pm_cc;
06.00031   PM_put_free_not_allowed_on_own_resident_segment = #09 + pm_cc;
06.00032   PM_put_free_segment_not_mapped_out   = #0A + pm_cc;
06.00033   PM_put_free_segment_locked            = #0B + pm_cc;
06.00034   PM_exclude_resident_segment_from_children = #0C + pm_cc;
06.00035   PM_mmcbl_lock_cnt_overflow            = #0D + pm_cc;
06.00036   PM_no_segment_mapped_in_to_lock        = #0E + pm_cc;
06.00037   PM_other_users_of_segment             = #0F + pm_cc;
06.00038   PM_process_lock_cnt_underflow         = #10 + pm_cc;
06.00039   PM_no_segment_mapped_in_to_unlock     = #11 + pm_cc;
06.00040   PM_lock_start_or_number_out_of_range = #12 + pm_cc;
06.00041   PM_segment_not_accessed               = #13 + pm_cc;
06.00042   PM_program_segment_too_big            = #15 + pm_cc;
06.00043   PM_data_segment_too_big                = #16 + pm_cc;
06.00044   PM_program_segment_misplaced         = #17 + pm_cc;
06.00045   PM_segment_not_mapped_out            = #18 + pm_cc;
06.00046   PM_is_ancestor_comparison_failure   = #19 + pm_cc;
06.00047   PM_no_special_mem_segment           = #1A + pm_cc;
06.00048   PM_scb_ref_cnt_overflow             = #1B + pm_cc;
06.00049   PM_no_us_to_reclaim                  = #1C + pm_cc;
06.00050   PM_no_place_on_parameter_stack       = #1D + pm_cc;
06.00051
06.00052   PM_bitmap_too_fragmented               = #30 + pm_cc;
06.00053   PM_bs_in_use                           = #31 + pm_cc;
06.00054
06.00056 "errors which causes the calling process to retire
06.00057 "range #40..#4F
06.00058
06.00059   PM_no_fault_page_mapped_in             = #40 + pm_cc;
06.00060   PM_lack_of_resources                    = #41 + pm_cc;
06.00061   "secondary cause code is resource type lacking;
06.00062
06.00063 "internal codes
06.00064   PM_depaged                              = #50 + pm_cc;
06.00065   PM_to_cleanup                          = #51 + pm_cc;

```

"errors which causes security violation to be indicated  
 "range #60..#7F

= 8 70

PM_segment_level_out_of_range	= #60 + pm_cc + security_violation;
PM_prohibited_segment_level	= #61 + pm_cc + security_violation;
PM_attempt_to_create_special_segment	= #62 + pm_cc + security_violation;
PM_pps_segment_attempted_touched	= #63 + pm_cc + security_violation;
PM_stbl_mismatch	= #64 + pm_cc + security_violation;
PM_child_pps_mapped_in_by_parent	= #65 + pm_cc + security_violation;
PM_child_pps_not_resident	= #66 + pm_cc + security_violation;
PM_child_pps_referenced_by_others	= #67 + pm_cc + security_violation;
PM_security_violation_at_locking	= #68 + pm_cc + security_violation;
PM_security_violation_at_unlocking	= #69 + pm_cc + security_violation;
PM_lock_purpose_out_of_range	= #6A + pm_cc + security_violation;
PM_segment_not_created_by_ancestor	= #6B + pm_cc + security_violation;
PM_no_write_access_at_destination	= #6C + pm_cc + security_violation;
PM_no_read_access_to_attributes	= #6D + pm_cc + security_violation;
PM_seg_access_cap_out_of_range	= #6E + pm_cc + security_violation;
PM_not_allowed_to_call_procedure	= #6F + pm_cc + security_violation;
PM_function_out_of_range	= #70 + pm_cc + security_violation;
PM_no_access	= #71 + pm_cc + security_violation;

"errors from the BS-process  
 "range #80..#9F

PM_backing_storage_area_too_small	= #80 + pm_cc;
PM_buffer_segment_too_small	= #81 + pm_cc;
PM_illegal_paging_device_kind	= #82 + pm_cc;
PM_illegal_paging_unit_number	= #83 + pm_cc;
PM_illegal_paging_subunit_number	= #84 + pm_cc;
PM_illegal_disk_address	= #85 + pm_cc;

"errors which causes a call of PU\_error  
 "range #A0..#FF

PM_free_le_list_empty	= #A0 + pm_cc;
PM_free_mmcbl_list_empty	= #A1 + pm_cc;
PM_free_scb_list_empty	= #A2 + pm_cc;
PM_mismatch_in_own_resident_segment_size	= #A3 + pm_cc;
PM_mismatch_in_ref_cnt	= #A4 + pm_cc;
PM_mismatch_in_page_list_count	= #A5 + pm_cc;
PM_mismatch_in_process_indices	= #A6 + pm_cc;
PM_illegal_page_state	= #A7 + pm_cc;
PM_illegal_ws_resource_shortage	= #A8 + pm_cc;
PM_illegal_ws_reference	= #A9 + pm_cc;
PM_expected_segment_not_on_rsl	= #AA + pm_cc;
PM_expected_page_not_on_lpl	= #AB + pm_cc;
PM_locked_page_not_in_ws	= #AC + pm_cc;
PM_vm_list_inconsistent	= #AD + pm_cc;
PM_wrong_view_at_page_fault	= #AE + pm_cc;
PM_undo_segment_impossible	= #B0 + pm_cc;
PM_delete_process_impossible	= #B1 + pm_cc;
PM_recovery_from_locking_fault_impossible	= #B2 + pm_cc;
PM_wrong_cause_code	= #B3 + pm_cc;
PM_rsl_of_process_not_released	= #B4 + pm_cc;
PM_ws_of_process_not_released	= #B5 + pm_cc;
PM_mmcbl_lock_cnt_underflow	= #B6 + pm_cc;
PM_index_error	= #B8 + pm_cc;
PM_segment_table_inconsistent	= #B9 + pm_cc;
PM_vm_of_process_not_released	= #BA + pm_cc;



```

09.00001
09.00002 %LIST      %LIST      %LIST
09.00003 "-----"
09.00004 "
09.00005 "  MODULE NAME:          PROCESS COMMUNICATION FACILITY
09.00006 "  MODULE ID NMB:       CSS/9115
09.00007 "  MODULE VERSION:      02
09.00008 "  MODULE RELEASE:      01
09.00009 "  RELEASE DATE:        821019
09.00010 "
09.00011 "-----"
09.00012 %NOLIST    %NOLIST    %NOLIST
09.00013
09.00014 "PCF completion codes:
09.00015 "=====
09.00016
09.00017 const
09.00018     PCF_error = (Process_communication_error shiftl 8);
09.00019     PCF_too_low_level          = #01 + PCF_error;
09.00020     PCF_xfer_exceeds_max       = #02 + PCF_error;
09.00021     PCF_xfer_exceeds_size     = #03 + PCF_error;
09.00022     PCF_bad_address           = #04 + PCF_error;
09.00023     PCF_not_semaphore         = #05 + PCF_error;
09.00024     PCF_not_info_sync_el      = #06 + PCF_error;
09.00025     PCF_sync_el_overflow    = #07 + PCF_error;
09.00026     PCF_no_waiting_process   = #08 + PCF_error;
09.00027     PCF_no_send_received     = #09 + PCF_error;
09.00028     PCF_sync_el_deleted     = #0A + PCF_error;
09.00029     PCF_ppp_claims_exceeded = #0B + PCF_error;
09.00030     PCF_sync_el_remote       = #0C + PCF_error;
09.00031     PCF_no_send_to_regret   = #0D + PCF_error;
09.00032     PCF_bad_process_state    = #0E + PCF_error;
09.00033     PCF_await_error         = #0F + PCF_error;
09.00034     PCF_bad_subtype         = #10 + PCF_error;
09.00035     PCF_bad_init_value      = #11 + PCF_error;
09.00036     PCF_bad_send_count      = #12 + PCF_error;
09.00037     PCF_init_error         = #13 + PCF_error;
09.00038     PCF_not_used            = #14 + PCF_error;

```

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```

09.00039     PCF_insufficient_sends_received = #15 + PCF_error;
09.00040     PCF_no_init_await              = #16 + PCF_error;
09.00041     PCF_insufficient_level        = #17 + PCF_error;
09.00042     PCF_activity_list_inconsistency = #18 + PCF_error;
09.00043     PCF_curr_proc_not_found       = #19 + PCF_error;
09.00044     PCF_conditional_ready_fail   = #1A + PCF_error;
09.00045     PCF_bad_index                 = #1B + PCF_error;
09.00046     PCF_no_processor_assigned    = #1C + PCF_error;
09.00047     PCF_delete_account_inconsistency = #1D + PCF_error;
09.00048     PCF_sync_el_max_exceeded     = #1E + PCF_error;
09.00049     PCF_list_el_max_exceeded   = #1F + PCF_error;
09.00050     PCF_info_el_max_exceeded     = #20 + PCF_error;
09.00051     PCF_init_sync_el_exceed_limit = #21 + PCF_error;
09.00052     PCF_init_list_el_exceed_limit = #22 + PCF_error;
09.00053     PCF_init_info_el_exceed_limit = #23 + PCF_error;
09.00054

```



#D

LIST %LIST %LIST

MODULE NAME: DIRECTORY FUNCTION
MODULE ID NMB: CSS/9100
MODULE VERSION: 02
MODULE RELEASE: 01
RELEASE DATE: 821019

LIST %NOLIST %NOLIST

completion codes:
=====

st
DF\_bad\_view = #01 + (Directory\_functions\_error shiftll 8 );
DF\_bad\_00\_table = #02 + (Directory\_functions\_error shiftll 8 );

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DF\_bad\_index = #03 + (Directory\_functions\_error shiftll 8 );
DF\_bad\_level = #04 + (Directory\_functions\_error shiftll 8 );
DF\_no\_capability = #05 + (Directory\_functions\_error shiftll 8 );
DF\_bad\_sequence\_no = #06 + (Directory\_functions\_error shiftll 8 );
DF\_bad\_type = #07 + (Directory\_functions\_error shiftll 8 );
DF\_entry\_not\_free = #08 + (Directory\_functions\_error shiftll 8 );
DF\_bad\_00\_location = #09 + (Directory\_functions\_error shiftll 8 );
DF\_bad\_scope = #0A + (Directory\_functions\_error shiftll 3 );
DF\_not\_found = #0B + (Directory\_functions\_error shiftll 3 );
DF\_already\_cataloged = #0C + (Directory\_functions\_error shiftll 8 );
DF\_name\_in\_use = #0D + (Directory\_functions\_error shiftll 8 );
DF\_not\_cataloged = #0E + (Directory\_functions\_error shiftll 3 );
DF\_illegal\_caller = #0F + (Directory\_functions\_error shiftll 8 );
DF\_remote = #10 + (Directory\_functions\_error shiftll 8 );
DF\_not\_implemented = #11 + (Directory\_functions\_error shiftll 3 );
DF\_security\_violation = #12 + (Directory\_functions\_error shiftll 8 );
DF\_bad\_address = #13 + (Directory\_functions\_error shiftll 3 );
DF\_free\_00 = #14 + (Directory\_functions\_error shiftll 8 );
DF\_bad\_00\_table\_size = #15 + (Directory\_functions\_error shiftll 3 );
DF\_fail\_in\_release\_parameter\_stack = #16 + (Directory\_functions\_error shiftll 8 );
DF\_fail\_in\_reserve\_parameter\_frame = #17 + (Directory\_functions\_error shiftll 3 );
DF\_fail\_in\_free\_parameter\_frame = #18 + (Directory\_functions\_error shiftll 3 );
DF\_fail\_in\_allocate\_frame\_space = #19 + (Directory\_functions\_error shiftll 8 );

#E

RTC\_CC.I

```

%LIST      %LIST      %LIST
-----
"
"  MODULE NAME:          RTC (COMPLETION CODES)
"  MODULE ID NMB:       CSS/9119
"  MGDULE VERSION:      02
"  MCDULE RELEASE:      01
"  RELEASE DATE:        821019
"
-----

```

%NOLIST %NOLIST %NOLIST

"RTC Completion Codes

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"=====

const

RTC\_cc= Real\_Time\_Clock\_error shiftl 8;

const

```

RTC_set_time_not_allowed           = #01 + RTC_cc;
RTC_illegal_read_address           = #02 + RTC_cc;
RTC_illegal_write_address          = #03 + RTC_cc;
RTC_no_time_signal_scheduled       = #04 + RTC_cc;
RTC_interval_too_small              = #05 + RTC_cc;
RTC_date_out_of_range               = #06 + RTC_cc;
RTC_sync_el_not_sema               = #07 + RTC_cc;
RTC_time_signal_already_scheduled  = #08 + RTC_cc;

RTC_index_error                     = #40 + RTC_cc;
RTC_bad_time_queue_sequence         = #41 + RTC_cc;
RTC_empty_time_queue                = #42 + RTC_cc;
RTC_date_mismatch                   = #43 + RTC_cc;

RTC_not_implemented                 = #7F + RTC_cc;

```

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---

---

===== DVM\_CC.I

#10

```

07.00001
07.00002 %LIST      %LIST      %LIST
07.00003 "-----"
07.00004 "
07.00005 "  MODULE NAME:      Device Management Completion Codes
07.00006 "  MODULE ID NMB:    CSS/9104
07.00007 "  MODULE VERSION:   02
07.00008 "  MODULE RELEASE:   03
07.00009 "  RELEASE DATE:    831230
07.00010 "
07.00011 "-----"
07.00012 %NOLIST    %NOLIST    %NOLIST
07.00013

```

onst

DVM\_cc= Device\_Management\_Error shiftl 8;

```

DVM_device_already_created      = DVM_cc + #01;
DVM_bad_device_type             = DVM_cc + #02;
DVM_bad_iv                      = DVM_cc + #03;
DVM_bad_logical_device_level    = DVM_cc + #04;
DVM_wrong_device_type          = DVM_cc + #05;
DVM_no_capability               = DVM_cc + #06;
DVM_device_already_initialized  = DVM_cc + #07;
DVM_device_not_initialized      = DVM_cc + #08;
DVM_device_not_shutdown_yet     = DVM_cc + #09;
DVM_device_not_created          = DVM_cc + #0A;
DVM_remote_access_not_allowed   = DVM_cc + #0B;
DVM_device_still_initialized    = DVM_cc + #0C;
DVM_bad_device_priority         = DVM_cc + #0D;
DVM_device_function_not_allowed = DVM_cc + #0E;
DVM_too_low_level_to_create     = DVM_cc + #0F;
DVM_read_device_param_not_allowed = DVM_cc + #10;
DVM_illegal_device_param_pointer = DVM_cc + #11;
DVM_illegal_device_param_address = DVM_cc + #12;
DVM_device_param_overflow       = DVM_cc + #13;
DVM_sync_el_index_missing       = DVM_cc + #14;
DVM_sync_el_not_found           = DVM_cc + #15;
DVM_object_not_a_sync_el        = DVM_cc + #16;
DVM_handler_not_same_priority   = DVM_cc + #17;
DVM_unknown_device_type        = DVM_cc + #18;
DVM_params_not_on_PST           = DVM_cc + #19;
DVM_return_to_handler           = DVM_cc + #1A;
DVM_system_fix_up               = DVM_cc + #1B;
DVM_iv_not_included             = DVM_cc + #1C;
DVM_bad_object_index            = DVM_cc + #1D;
DVM_device_failing              = DVM_cc + #1E;

```

DVM initialization errors:

```

DVM_no_room_for_IVT             = DVM_cc + #80;
DVM_no_room_for_LDT             = DVM_cc + #81;
DVM_no_more_logical_devices     = DVM_cc + #82;
DVM_too_small_iv                = DVM_cc + #83;
DVM_iv_not_free                 = DVM_cc + #84;
DVM_initialization_record_exceeds_internal_length = DVM_cc + #85;
DVM_no_room_for_power_failure_handler = DVM_cc + #86;
DVM_too_big_DCB                 = DVM_cc + #87;
DVM_illegal_device_kind         = DVM_cc + #88;
DVM_no_views                     = DVM_cc + #89;
DVM_wrong_view                  = DVM_cc + #8A;

```



```

08.00001
08.00002 %LIST      %LIST      %LIST
08.00003 "-----"
08.00004 "
08.00005 "  MODULE NAME:          RESOURCE MANAGEMENT COMPLETION CODES
08.00006 "  MODULE ID NMB:      CSS/9118
08.00007 "  MODULE VERSION:     02
08.00008 "  MODULE RELEASE:     02
08.00009 "  RELEASE DATE:      830616
08.00010 "
08.00011 "-----"
08.00012 %NOLIST    %NOLIST    %NOLIST
08.00013
08.00014 "RM completion codes:
08.00015 "=====
08.00016
08.00017 const
08.00018     RM_cc= Resource_management_error shiftll 8;
08.00019     RM_negative_claim                = #02 + RM_cc;
08.00020     RM_BAD_RESOURCE                   = #03 + RM_cc;
08.00021     RM_BAD_INDEX                      = #04 + RM_cc;
08.00022     RM_BAD_TYPE                      = #05 + RM_cc;
08.00023     RM_NOT_ALLOWED                   = #06 + RM_cc;
08.00024     RM_POOL_STILL_IN_USE            = #07 + RM_cc;
08.00025     RM_TOO_LOW                     = #08 + RM_cc;
08.00026     RM_BAD_LEVEL                  = #09 + RM_cc;
08.00027     RM_resource_pool_pool_exceeded = #10 + RM_cc;
08.00028     RM_process_pool_exceeded      = #11 + RM_cc;
08.00029     RM_sync_el_pool_exceeded       = #12 + RM_cc;
08.00030     RM_PCF_list_el_pool_exceeded    = #13 + RM_cc;
08.00031     RM_info_el_pool_exceeded       = #14 + RM_cc;
08.00032     RM_segment_pool_exceeded      = #15 + RM_cc;
08.00033     RM_PM_list_el_pool_exceeded    = #16 + RM_cc;
08.00034     RM_internal_RAM_WS_pool_exceeded = #17 + RM_cc;
08.00035     RM_external_RAM_WS_pool_exceeded = #18 + RM_cc;
08.00036     RM_special_mem_WS_pool_exceeded = #19 + RM_cc;
08.00037     RM_VM_pool_exceeded             = #1A + RM_cc;
08.00038     RM_DF_entry_pool_exceeded      = #1B + RM_cc;
08.00039     RM_DF_ACLE_pool_exceeded       = #1C + RM_cc;
08.00040     RM_DMA_pool_exceeded            = #1D + RM_cc;
08.00041     RM_STI_pool_exceeded         = #1E + RM_cc;
08.00042     RM_resource_pool_user_exceeded = #30 + RM_cc;
08.00043     RM_process_user_exceeded        = #31 + RM_cc;
08.00044     RM_sync_el_user_exceeded       = #32 + RM_cc;
08.00045     RM_PCF_list_el_user_exceeded  = #33 + RM_cc;
08.00046     RM_info_el_user_exceeded     = #34 + RM_cc;
08.00047     RM_segment_user_exceeded    = #35 + RM_cc;
08.00048     RM_PM_list_el_user_exceeded    = #36 + RM_cc;
08.00049     RM_internal_RAM_WS_user_exceeded = #37 + RM_cc;
08.00050     RM_external_RAM_WS_user_exceeded = #38 + RM_cc;
08.00051     RM_special_mem_WS_user_exceeded = #39 + RM_cc;
08.00052     RM_VM_user_exceeded            = #3A + RM_cc;
08.00053     RM_DF_entry_user_exceeded     = #3B + RM_cc;
08.00054     RM_DF_ACLE_user_exceeded       = #3C + RM_cc;
08.00055     RM_DMA_user_exceeded         = #3D + RM_cc;
08.00056     RM_STI_user_exceeded         = #3E + RM_cc;
08.00057     RM_bad_object_descriptor     = #3F + RM_cc;
08.00058     RM_fail_in_allocate_resource  = #40 + RM_cc;
08.00059     RM_pool_empty                 = #41 + RM_cc;
08.00060     RM_resource_pool_inconsistency = #42 + RM_cc;
08.00061     RM_no_free_RPCB_resource      = #43 + RM_cc;
08.00062     RM_create_process_fail         = #44 + RM_cc;
08.00063     RM_clean_up_error               = #45 + RM_cc;
08.00064     RM_RPCB_AREA                   = #FD + RM_cc;
08.00065     RM_RATE_AREA                   = #FE + RM_cc;
08.00066     RM_INIT_RESOURCE              = #FF + RM_cc;

```

#12

INIT\_CC.1

#14

-----  
"  
" MODULE NAME: DAMOS INITIALIZATION COMPLETION CODES  
" MODULE ID NBR: CSS/9108  
-----  
" MODULE VERSION: 02  
" MODULE RELEASE: 02  
" RELEASE DATE: 830204  
"  
-----

"INIT Completion Codes  
"=====

```
const  
INIT_no_free_ttbl          = 1 + (Initialization_error shiftll 8);  
INIT_no_free_phys_pages   = 2 + (Initialization_error shiftll 8);  
INIT_no_free_view         = 3 + (Initialization_error shiftll 8);  
INIT_write_not_allowed    = 4 + (Initialization_error shiftll 8);  
INIT_load_module_error    = 5 + (Initialization_error shiftll 8);  
INIT_invalid_frame_addr   = 6 + (Initialization_error shiftll 8);  
INIT_baa_object_index     = 7 + (Initialization_error shiftll 8);  
INIT_invalid_device_type  = 8 + (initialization_error shiftll 8);  
INIT_double_allocated_dvm_view = 9 + (initialization_error shiftll 8);  
INIT_wrong_dvm_view       = 10 + (initialization_error shiftll 8);  
INIT_index_error          = 11 + (Initialization_error shiftll 8);  
INIT_wrong_view           = 12 + (initialization_error shiftll 8);  
INIT_segment_not_present  = 13 + (initialization_error shiftll 8);  
INIT_illegal_segment_name = 14 + (initialization_error shiftll 8);
```

PREF.S


side #15  
Projekt

MODULE NAME: ROOT\_CC.I  
MODULE ID NMB: CSS/9129  
MODULE VERSION: 01  
MODULE RELEASE: 01  
RELEASE DATE: 830118

%NOLIST %NOLIST %NOLIST

const  
ROOT\_cc= ROOT\_error shiftll 8;

root\_bad\_obj\_id = #1 + ROOT\_cc;  
root\_log\_obj\_table\_full = #2 + ROOT\_cc;  
root\_seq\_no\_too\_big = #3 + ROOT\_cc;

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root\_max\_await\_sy\_el\_exceeded = #4 + ROOT\_cc;  
root\_bad\_service\_type = #5 + ROOT\_cc;  
root\_name\_reused = #6 + ROOT\_cc;  
root\_segment\_no\_too\_big = #7 + ROOT\_cc;  
root\_bad\_operation = #8 + ROOT\_cc;

===== SM\_CC.I

#10

```

04.00001
04.00002 %LIST      %LIST      %LIST
04.00003 "-----"
04.00004 "
04.00005 "  MODULE NAME:      SM_CC.I
04.00006 "  MODULE ID NMB:    CSS/9131
04.00007 "  MODULE VERSION:   01
04.00008 "  MODULE RELEASE:   01
04.00009 "  RELEASE DATE:    830314
04.00010 "
04.00011 "-----"
04.00012 %NOLIST    %NOLIST    %NOLIST
04.00013

```

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```

04.00014 const
04.00015     SM_cc= SM_error shift11 3;
04.00016
04.00017     SM_illegal_semaphore_index      = #01 + SM_cc;
04.00018     SM_unknown_semaphore             = #02 + SM_cc;
04.00019     SM_semaphore_overrun            = #03 + SM_cc;
04.00020     SM_no_free_semaphore              = #04 + SM_cc;
04.00021     SM_negative_initial_value      = #05 + SM_cc;
04.00022
04.00023     SM_block_error                    = #40 + SM_cc;
04.00024     SM_assign_error                   = #41 + SM_cc;
04.00025     SM_ready_error                   = #42 + SM_cc;
04.00026     SM_reschedule_error               = #43 + SM_cc;
04.00027     SM_not_timed                    = #44 + SM_cc;
04.00028
04.00029     SM_semaphore_space_exceeds_limit  = #80 + SM_cc;
04.00030

```



XFER\_CC.I

%LIST %LIST %LIST

#1F

```
"
" MODULE NAME: XFER
" MODULE ID NMB: CSS/9122
" MODULE VERSION: 02
" MODULE RELEASE: 01
" RELEASE DATE: 821019
"
"
"
```

%NOLIST %NOLIST %NOLIST

"XFER Completion Codes  
"=====

const

```
XFER_level_error = #01 + (Xfer_module_error shiftl 8);
XFER_PPS_size_error = #02 + (Xfer_module_error shiftl 8);
```

```
"The following completion codes derives directly from the DMA/STI Handler
XFER_source_not_cancelled = #FD + (XFER_module_error shiftl 8);
XFER_destination_not_cancelled = #FE + (XFER_module_error shiftl 8);
XFER_source_destination_not_called = #FF + (XFER_module_error shiftl 8);
```

#21

5  
" MODULE NAME: COROUTINE MONITOR  
" MODULE ID NMB: CSS/9490  
" MODULE VERSION: 02  
" MODULE RELEASE: 01  
" RELEASE DATE: 821019  
"

"completion codes from CMON

const

CMON_process_blocked	=	1	+	(CMON_error	shiftll	3);	_____
CMON_remove_empty_queue	=	2	+	(CMON_error	shiftll	3);	_____
CMON_xfer_exceeds_size	=	3	+	(CMON_error	shiftll	3);	_____

```

"
" MODULE NAME:                TERMINAL MANAGEMENT SYSTEM
" MODULE ID NMB:              CSS/9120
" MODULE VERSION:             02
" MODULE RELEASE:             02
" RELEASE DATE:               840106
"

```

side

# 28

projekt

```

-----
%NOLIST   %NOLIST   %NOLIST

```

```

"TMS Completion Codes"
"=====

```

```

const

```

```

    TMS_cc = TMS_error shiftl 8;

```

```

    TMS_more_data           = #01 + TMS_cc;
    TMS_cancelled           = #02 + TMS_cc;
    TMS_unknown_operation   = #03 + TMS_cc;

```

```

    TMS_unknown_command     = #10 + TMS_cc;
    TMS_not_applicable      = #11 + TMS_cc;
    TMS_parameter_error     = #12 + TMS_cc;
    TMS_parameter_limit_exceeded = #13 + TMS_cc;

```

```

    TMS_illegal_caller     = #20 + TMS_cc;
    TMS_unknown_user       = #21 + TMS_cc;
    TMS_user_already_on    = #22 + TMS_cc;

```

```

    TMS_unknown_device     = #30 + TMS_cc;
    TMS_device_in_use      = #31 + TMS_cc;

```

```

    TMS_unknown_name       = #40 + TMS_cc;
    TMS_name_already_used  = #42 + TMS_cc;
    TMS_illegal_name       = #43 + TMS_cc;

```

```

    TMS_disconnected       = #50 + TMS_cc;

```

```

    TMS_invalid_connection = #51 + TMS_cc;
    TMS_bad_connection_state = #52 + TMS_cc;

```

```

    TMS_no_access_right     = #60 + TMS_cc;
    TMS_access_level_violation = #61 + TMS_cc;
    TMS_not_trusted        = #62 + TMS_cc;
    TMS_security_violation  = #63 + TMS_cc;
    TMS_invalid_security_profile = #64 + TMS_cc;
    TMS_acl_full            = #65 + TMS_cc;

```

```

    TMS_no_user_resource    = #70 + TMS_cc;
    TMS_no_subdevice_resource = #71 + TMS_cc;
    TMS_no_connection_resource = #72 + TMS_cc;
    TMS_no_operation_resource = #73 + TMS_cc;
    TMS_no_selector_resource = #74 + TMS_cc;

```

```

    TMS_internal_error     = #FF + TMS_cc;

```

LOG\_CC.I

#2A

```

%LIST      %LIST      %LIST
-----
"
"  MODULE NAME:           DAMOS LOG MODULE
"  MODULE ID NMB:        CSS/9110
"  MODULE VERSION:       02
"  MODULE RELEASE:       01
"  RELEASE DATE:         821019
"
-----

```

```

%NOLIST    %NOLIST    %NOLIST
"PAGE

```

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"LOG Completion Codes  
"=====

```

const
LOG_process_index_out_of_range = 1 + (LOG_module_error SHIFTLL 8);
LOG_no_read_access_to_descriptor = 2 + (LOG_module_error SHIFTLL 8);
LOG_no_read_access_to_prio = 3 + (LOG_module_error SHIFTLL 8);
LOG_no_write_access_to_log_data = 4 + (LOG_module_error SHIFTLL 8);
LOG_not_allowed_to_log = 5 + (LOG_module_error SHIFTLL 8);
LOG_not_a_process = 6 + (LOG_module_error SHIFTLL 8);

```

---

---

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---

TRACE\_CC.I

# 23

%LIST %LIST %LIST

```

-----
"
"
"  MODULE NAME:          TRACE INTERRUPT HANDLING COMPLETION CODES
"  MODULE ID NMB:       CSS/9493
"  MODULE VERSION:      02
"  MODULE RELEASE:      01
"  RELEASE DATE:        821019
"
-----

```

%NOLIST %NOLIST %NOLIST

"Trace interrupt handling completion codes

```

const
  trace_cc = trace_error shiftl 8;

```

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Sol

```

TRACE_singlastep           = #01 + trace_cc;
TRACE_condition_met        = #02 + trace_cc;
TRACE_after_break          = #10 + trace_cc;

```

"might be IDR'ed with other TRACE cc's.

```

"
" MODULE NAME:          MAG-TAPE HANDLER COMPLETION CODES
" MODULE ID NMB:       CSS/9128
" MODULE VERSION:      02
" MODULE RELEASE:      01
" RELEASE DATE:        821019
"
"-----
%NOLIST   %NOLIST   %NOLIST

```

```

const
  mt_cc = MT_handler_error shiftll 8;

```

```

"Completion codes given in replies (info elements)
"after execution of a function.

```

```

MT_okay                = #00;
MT_recoverable_device_failure = #01 + mt_cc;
MT_end_of_tape_sensed   = #02 + mt_cc;
MT_tape_write_protected = #03 + mt_cc;
MT_invalid_record_length = #04 + mt_cc;
MT_beginning_of_tape_sensed = #05 + mt_cc;
MT_filemark_sensed     = #06 + mt_cc;
MT_logical_end_of_tape_sensed = #07 + mt_cc;
MT_short_data_record   = #08 + mt_cc;
MT_long_data_record    = #09 + mt_cc;
MT_device_failure      = #0A + mt_cc;
MT_not_ready           = #0B + mt_cc;
MT_offline             = #0C + mt_cc;
MT_selftest_not_ended  = #0D + mt_cc;
MT_selftest_failed     = #0E + mt_cc;
MT_operation_timeout   = #0F + mt_cc;

```

```

"Completion codes for illegal parameters at call of a function

```

```

MT_request_ok          = #00;
MT_device_fault        = #21 + mt_cc;
MT_not_allowed         = #22 + mt_cc;
MT_illegal_function    = #23 + mt_cc;

MT_not_implemented     = #24 + mt_cc;
MT_illegal_unit_no    = #25 + mt_cc;
MT_already_assigned    = #26 + mt_cc;
MT_not_assigned        = #27 + mt_cc;
MT_bad_segment_id     = #28 + mt_cc;
MT_bad_segment_type    = #29 + mt_cc;
MT_bad_sync_elem_id   = #2A + mt_cc;
MT_not_assigner       = #2B + mt_cc;
MT_illegal_buffer_address = #2C + mt_cc;
MT_unit_in_use         = #2D + mt_cc;
MT_already_testing     = #2E + mt_cc;
MT_units_assigned      = #2F + mt_cc;
MT_not_initialized     = #30 + mt_cc;

```

#33

```

"
" MODULE NAME:          OC HANDLER COMPLETION CODES
" MODULE ID NMB:       CSS/9114
" MODULE VERSION:      02
" MODULE RELEASE:      02
" RELEASE DATE:        840113

```

```

"OCH Completion Codes"
"=====

```

```

const OCH_err          = oc_handler_error shiftll 8;

const
  OCH_request_queued   = #01 + (oc_handler_error shiftll 8);
  OCH_unknown_function = #02 + (oc_handler_error shiftll 8);
  OCH_not_implemented  = #03 + (oc_handler_error shiftll 8);
  OCH_device_fault     = #04 + (oc_handler_error shiftll 8);
  OCH_not_allowed      = #05 + (oc_handler_error shiftll 8);
  OCH_bad_subdevice_state = #06 + (oc_handler_error shiftll 8);
  OCH_no_resources     = #07 + (oc_handler_error shiftll 8);
  OCH_bad_param_size   = #08 + (oc_handler_error shiftll 8);
  OCH_bad_protocol     = #09 + (oc_handler_error shiftll 8);
  OCH_bad_h_sdid       = #0A + (oc_handler_error shiftll 8);
  OCH_too_many_requests = #0B + (oc_handler_error shiftll 8);
  OCH_bad_h_opid       = #0C + (oc_handler_error shiftll 8);
  OCH_bad_blocksize    = #0D + (oc_handler_error shiftll 8);
  OCH_invalid_se       = #0E + (oc_handler_error shiftll 8);
  OCH_bad_sda          = #0F + (oc_handler_error shiftll 8);
  OCH_bad_device_state = #10 + (oc_handler_error shiftll 8);
  OCH_bad_buffer_type  = #11 + (oc_handler_error shiftll 8);

```

```

"initialization errors

```

```

OCH_buffer_layout_error = #40 + (oc_handler_error shiftll 8);

```

```

" Watchdog OC handler completion codes.

```

```

OCH_WD_line_id_already_used      = #80 + OCH_err;
OCH_WD_no_free_channels          = #81 + OCH_err;
OCH_WD_user_is_not_creator       = #82 + OCH_err;
OCH_WD_channel_not_open         = #83 + OCH_err;
OCH_WD_illegal_channel_id       = #84 + OCH_err;
OCH_WD_line_id_not_found        = #85 + OCH_err;
OCH_WD_no_free_operation_descriptor = #86 + OCH_err;
OCH_WD_invalid_H_CPID          = #87 + OCH_err;
OCH_WD_no_free_buffer           = #88 + OCH_err;
OCH_WD_bad_status_SE           = #89 + OCH_err;
OCH_WD_subdevice_closed         = #8A + OCH_err;
OCH_WD_other_input_pending      = #8B + OCH_err;
OCH_WD_bad_line_id_received_from_WD = #8C + OCH_err;
OCH_WD_unexpected_output_ack    = #8D + OCH_err;
OCH_WD_output_ack_for_unknown_line = #8E + OCH_err;
OCH_WD_data_received_without_request = #8F + OCH_err;
OCH_WD_status_to_unknown_line  = #90 + OCH_err;
OCH_WD_output_nack              = #91 + OCH_err;
OCH_WD_output_result_timeout    = #92 + OCH_err;
OCH_WD_no_capabilities         = #93 + OCH_err;
OCH_WD_not_implemented         = #94 + OCH_err;
OCH_WD_lack_of_input_buffers    = #95 + OCH_err;
OCH_WD_bad_input_state         = #96 + OCH_err;
OCH_WD_handler_shutdown        = #96 + OCH_err;
OCH_WD_bad_od_kind              = #97 + OCH_err;
OCH_WD_cancelled                = #98 + OCH_err;
OCH_WD_watchdog_status         = #99 + OCH_err;
OCH_WD_bad_protocol_id         = #9A + OCH_err;
OCH_WD_isend_free_error        = #02 + OCH_err;

```

LPH\_CC.I

# 83

```
%LIST      %LIST      %LIST
-----
```

```
"
"  MODULE NAME:          LP HANDLER COMPLETION CODES
"  MODULE ID NMB:       CSS/9111
"  MODULE VERSION:      02
"  MODULE RELEASE:      01
"  RELEASE DATE:        821019
"
```

```
%NOLIST    %NOLIST    %NOLIST
-----
```

```
"LPH Completion Codes"
"====="
```

```
const
```

```
lph_request_queued      = #01 + (lp_handler_error shift11 8);
lph_unknown_function    = #02 + (lp_handler_error shift11 8);
lph_not_implemented     = #03 + (lp_handler_error shift11 8);
lph_device_fault       = #04 + (lp_handler_error shift11 8);
lph_not_allowed        = #05 + (lp_handler_error shift11 8);
lph_bad_subdevice_state = #06 + (lp_handler_error shift11 8);
lph_no_resources       = #07 + (lp_handler_error shift11 8);
lph_bad_param_size     = #08 + (lp_handler_error shift11 8);
```

```
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```

```
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```

```
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```

```
lph_bad_protocol       = #09 + (lp_handler_error shift11 8);
lph_bad_h_sdid         = #0A + (lp_handler_error shift11 8);
lph_too_many_requests  = #0B + (lp_handler_error shift11 8);
lph_bad_h_opid        = #0C + (lp_handler_error shift11 8);
lph_bad_blocksize     = #0D + (lp_handler_error shift11 8);
lph_invalid_se        = #0E + (lp_handler_error shift11 8);
lph_bad_sda           = #0F + (lp_handler_error shift11 8);
lph_bad_device_state  = #10 + (lp_handler_error shift11 8);
lph_bad_buffer_type   = #11 + (lp_handler_error shift11 8);
lph_device_failure    = #12 + (lp_handler_error shift11 8);
```



```

-----
"
" MODULE NAME:          Completion codes for the LTU handler
" MODULE ID NMB:       CSS/9112
" MODULE VERSION:      03
" MODULE RELEASE:      02
" RELEASE DATE:        831006
"
-----

```

# 34

```

%NOLIST   %NOLIST   %NOLIST

```

```

const
  LTUH_err = LTU_HANDLER_ERROR shiftll 8;

```

```

const
  ltuh_unknown_function      = LTUH_err + unknown_function;      "#02"
  ltuh_not_implemented       = LTUH_err + not_implemented;       "#03"
  ltuh_device_fault          = LTUH_err + device_fault;           "#04"
  ltuh_not_allowed           = LTUH_err + not_allowed;            "#05"
  ltuh_bad_subdevice_state   = LTUH_err + bad_subdevice_state;   "#06"
  ltuh_no_resources          = LTUH_err + no_resources;           "#07"
  ltuh_bad_param_size        = LTUH_err + bad_param_size;        "#08"
  ltuh_bad_protocol          = LTUH_err + bad_protocol;           "#09"
  ltuh_bad_h_sdid            = LTUH_err + bad_h_sdid;             "#0A"
  ltuh_too_many_requests     = LTUH_err + too_many_requests;     "#0B"
  ltuh_bad_h_opid            = LTUH_err + bad_h_opid;            "#0C"
  ltuh_bad_blocksize         = LTUH_err + bad_blocksize;         "#0D"
  ltuh_bad_sda                = LTUH_err + bad_sda;               "#0E"
  ltuh_bad_buffer_type       = LTUH_err + bad_buffer_type;       "#11"
  ltuh_protocol_fault        = LTUH_err + protocol_fault;        "#18"
                                "protocol fault has occurred"
  ltuh_too_many_subdevices   = _____
                                LTUH_err + too_many_subdevices; "#19"
                                "the requested number of subdevices could not
                                "be allocated (specification in include_record)

```

```

"Status codes:

```

```

"-----

```

```

const
  LTUH_device_failure        = LTUH_err + device_failure;        "#04"

```

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## DAMOS COMPLETION CODES

```

MODULE NAME:          DISK HANDLER
MODULE ID NMB:        CSS/9102
MODULE VERSION:       03
MODULE RELEASE:       06
RELEASE DATE:         840117

```

#35 (1/2)

```

NOLIST  %NOLIST  %NOLIST

```

```

onst

```

```

    disk_cc = disk_handler_error SHIFTLL 8;

```

Completion codes given in replies (info elements)  
after execution of a function.

```

DISK_okay                = f00;
DISK_drive_busy          = f00 + disk_cc;
                           "Only possible at initialize of CDC-
                           "handler. It is an erroneous completion
                           "code from the controller, as the
                           "ok result is 'fD', which is reset.

DISK_was_seeking         = f01 + disk_cc;
                           "Only possible at initialize of CDC-
                           "handler. See above.

DISK_write_protected_drv = f02 + disk_cc;
DISK_unexpected_status   = f03 + disk_cc;
DISK_data_or_sync_err    = f04 + disk_cc;
DISK_addr_or_sync_err    = f05 + disk_cc;
DISK_bad_sect            = f06 + disk_cc;
DISK_write_protected     = f07 + disk_cc;
DISK_illegal_sect        = f08 + disk_cc;
DISK_timing_err          = f09 + disk_cc;
DISK_subbus_overr        = f0A + disk_cc;
DISK_parity_err          = f0B + disk_cc;
DISK_selftest_running    = f0C + disk_cc;
DISK_reset               = f0D + disk_cc;
DISK_selftest_failed     = f0E + disk_cc;
DISK_volume_change       = f10 + disk_cc;
DISK_controller_timeout  = f11 + disk_cc;

```

Completion codes for illegal parameters at call of a function

```

DISK_request_ok          = f00;
DISK_device_fault        = f21 + disk_cc;

```

CONTINUED

## DAMOS COMPLETION CODES

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```

DISK_not_allowed           = £22 + disk_cc;
DISK_illegal_function      = £23 + disk_cc;
DISK_not_implemented       = £24 + disk_cc;
DISK_illegal_unit_no      = £25 + disk_cc;
DISK_illegal_subunit_no   = £26 + disk_cc;
DISK_illegal_drive_kind   = £27 + disk_cc;
DISK_illegal_privacy      = £28 + disk_cc;
DISK_already_included     = £29 + disk_cc;
DISK_not_included         = £2A + disk_cc;
DISK_no_unit_records      = £2B + disk_cc;
DISK_bad_segment_id       = £2C + disk_cc;
DISK_bad_segment_type     = £2D + disk_cc;
DISK_bad_sync_elem_id     = £2E + disk_cc;
DISK_not_includor        = £2F + disk_cc;
DISK_no_request_resources = £30 + disk_cc;
DISK_illegal_buffer_address = £31 + disk_cc;
DISK_illegal_sector_address = £32 + disk_cc;
DISK_illegal_sector_count = £33 + disk_cc;
DISK_already_testing      = £34 + disk_cc;
DISK_change_not_cleared   = £35 + disk_cc;
DISK_inconsistent_parameters = £36 + disk_cc;
DISK_illegal_subunit_id   = £37 + disk_cc;
DISK_subunit_in_use       = £38 + disk_cc;
DISK_subunit_included     = £39 + disk_cc;

```

## Disk handler completion codes for fatal internal errors

```

DISK_no_controller_requests = £40 + disk_cc;
DISK_controller_queue_empty = £41 + disk_cc;
DISK_process_queue_empty    = £42 + disk_cc;
DISK_lost_process_request   = £43 + disk_cc;
DISK_lost_controller_request = £44 + disk_cc;
DISK_lost_unit_request      = £45 + disk_cc;
DISK_unknown_function       = £46 + disk_cc;
DISK_handler_queue_error    = £47 + disk_cc;
DISK_hdlr_error              = £48 + disk_cc;

```

#36

```

"
"  MODULE NAME:          FLOPPY DISK HANDLER COMPLETION CODES
"  MODULE ID NMB:       CSS/9103
"  MODULE VERSION:      02
"  MODULE RELEASE:      01
"  RELEASE DATE:        821019
"

```

%NOLIST %NOLIST %NOLIST

se DISK-HANDLER #36



#38

```

"
"
" MODULE NAME:           STIH COMPLETION CODES
" MODULE ID NMB:        CSS/9133
" MODULE VERSION:       01
" MODULE RELEASE:       02
" RELEASE DATE:         831104
"

```

STIH\_err = STI\_handler\_error\*256;

"Standard completion codes:

STIH_unknown_function	= STIH_err + unknown_function;	_____
STIH_not_implemented	= STIH_err + not_implemented;	_____
STIH_device_fault	= STIH_err + device_fault;	_____
STIH_not_allowed	= STIH_err + not_allowed;	_____
STIH_invalid_SE	= STIH_err + invalid_SE;	_____
STIH_send_error	= STIH_err + #30;	_____

```

"-----"
"
" MODULE NAME:          TDXH COMPLETION CODES
" MODULE ID NMB:       CSS/9124
" MODULE VERSION:     02
" MODULE RELEASE:     03
" RELEASE DATE:       840106
"
"-----"

```

#39 (1/2)

```

const

```

```

  TDXH_err = TDX_handler_error*256;

```

```

"Standard completion codes:

```

```

  TDXH_unknown_function      = TDXH_err + unknown_function;
  TDXH_not_implemented       = TDXH_err + not_implemented;
  TDXH_device_fault          = TDXH_err + device_fault;
  TDXH_not_allowed           = TDXH_err + not_allowed;
  TDXH_bad_subdevice_state   = TDXH_err + bad_subdevice_state;
  TDXH_no_resources          = TDXH_err + no_resources;
  TDXH_bad_param_size        = TDXH_err + bad_param_size;
  TDXH_bad_protocol          = TDXH_err + bad_protocol;
  TDXH_bad_h_sdid            = TDXH_err + bad_h_sdid;
  TDXH_too_many_requests     = TDXH_err + too_many_requests;
  TDXH_bad_h_opid            = TDXH_err + bad_h_opid;
  TDXH_bad_blocksize         = TDXH_err + bad_blocksize;
  TDXH_bad_sda                = TDXH_err + bad_sda;
  TDXH_bad_device_state      = TDXH_err + bad_device_state;
  TDXH_bad_buffer_type       = TDXH_err + bad_buffer_type;
  TDXH_buffer_layout_error   = TDXH_err + buffer_layout_error;
  TDXH_bad_param              = TDXH_err + bad_param;

```

```

  TDXH_subdevice_in_use      = TDXH_err + subdevice_in_use;

```

```

  TDXH_bad_function          = TDXH_err + bad_function;

```

```

  TDXH_protocol_fault        = TDXH_err + protocol_fault;

```

```

  TDXH_too_many_subdevices   = TDXH_err + too_many_subdevices;

```

```

  TDXH_no_buffer_resources   = TDXH_err + no_buffer_resources;

```

```

  TDXH_invalid_SE            = TDXH_err + invalid_SE;

```

TDXH\_CC.I

sign/dato

side # 1

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projekt

"Special completion codes:

```

const
  TDXH_bad_looptest_data      = TDXH_err + #41;
  TDXH_no_bandwidth_resources = TDXH_err + #42;
  TDXH_bad_controller_result  = TDXH_err + #43;

  TDXH_no_free_tdx_channel    = TDXH_err + #44;
  TDXH_reservation_failure    = TDXH_err + #45;
  TDXH_TIA_not_connected      = TDXH_err + #47;
  TDXH_bus_not_connected      = TDXH_err + #48;
  TDXH_bad_sass_def           = TDXH_err + #49;

```

```

"bad data in looptest datagram
"not possible to allocate bandwidth in controller
"unknown result from Controller in
"connection to allocation of bandwidth
"no free tdx channels
"failed to reserve device channel
"TIA not connected to STI
"BUS not connected or TIA or CONTROLLER failure

```

"Special status codes, that signals internal software errors.

```

"they are all specified in calls of PROTOCOL_ERROR,
TDXH_impossible      = TDXH_err + #50;
TDXH_impossible_cancel = TDXH_err + #51;
TDXH_bad_channel_index = TDXH_err + #52;
TDXH_no_DBD_resources = TDXH_err + #53;
TDXH_no_CD_resources  = TDXH_err + #54;

```

```

"impossible situation
"no request to cancel
"bad index in delete_TDX_channel
"no DBD's available
"no CD's available

```

"Status codes generated by TDX handler:

```

  TDXH_STI_device_failure = TDXH_err + device_failure; "STI failure
  TDXH_unrequested_datagram = TDXH_err + #61; "unrequested datagram received

```



```

MODULE NAME:          VDU_1 PROTOCCL COMPLETION CODES
MODULE ID NMB:       CSS/9126
MODULE VERSION:      03
MODULE RELEASE:      02
RELEASE DATE:        831006

```

```

%NQLIST %NQLIST %NQLIST

```

```

" This prefix file defines the error codes returned by the VDU_1 protocol.
"

```

```

const
  vdu_1_err
    = VDU_1_PROTOCOL_ERROR shiftll 8;

  vdu_1_not_implemented
    = vdu_1_err + not_implemented;
  vdu_1_device_fault
    = vdu_1_err + device_fault;
  vdu_1_not_allowed
    = vdu_1_err + not_allowed;
  vdu_1_bad_subdevice_state
    = vdu_1_err + bad_subdevice_state;
  vdu_1_bad_h_sdid
    = vdu_1_err + bad_h_sdid;
  vdu_1_too_many_requests
    = vdu_1_err + too_many_requests;
  vdu_1_bad_h_opid
    = vdu_1_err + bad_h_opid;
  vdu_1_bad_blocksize
    = vdu_1_err + bad_blocksize;
  vdu_1_bad_buffer_type
    = vdu_1_err + bad_buffer_type;
  vdu_1_no_init_buffer
    = vdu_1_err + #30; "no buffer available for initialization

"Special completion codes in asynchronous operation replies:
const
  vdu_1_bad_transmission_status = vdu_1_err + #40;
  vdu_1_bad_reception_status   = vdu_1_err + #41;
  vdu_1_bad_device_status      = vdu_1_err + #42;

"Special status message codes:
const
  vdu_1_subdevice_online
  vdu_1_subdevice_offline

  vdu_1_tx_status_sequence_error
  vdu_1_rx_status_sequence_error
  vdu_1_rx_buffer_type_bad
  vdu_1_rx_ldu_sequence_error
  vdu_1_rx_byte_count_too_small
  vdu_1_unrequested_data_received
  vdu_1_rx_device_failure
  vdu_1_tx_device_failure
  vdu_1_unrelated_tx_status

  status
    = vdu_1_err + #50;
    = vdu_1_err + #51;

  info
    "none
    "none

    "expected, received seq.no.
    "expected, received seq.no.
    "buffer type
    "none
    "byte count in buffer
    "none
    "status code
    "status code
    "seq.no.

```

```

"
" MODULE NAME:          HANDLER SUPPORT COMPLETION CODES
" MODULE ID NMB:      CSS/9127
" MODULE VERSION:    03
" MODULE RELEASE:    02
" RELEASE DATE:      831006
"
-----
  
```

#3B

-----  
%NOLIST %NOLIST %NOLIST

" This module contains definitions of completions and status codes for  
" the HLR\_SUPPORT procedures  
"

"Completion codes:  
"-----

const  
HLRS\_err = HLR\_SUPPORT\_ERROR shiftll 3;

```

const
HLRS_no_child_resources = HLRS_err + no_resources;
HLRS_invalid_SE        = HLRS_err + invalid_SE;
HLRS_dummy_procedure   = HLRS_err + not_implemented;
HLRS_protocol_event    = HLRS_err + protocol_fault;
HLRS_send_error        = HLRS_err + #30;
  
```

"Contents of info field in h\_status\_message when status = protocol\_event:  
"-----

```

stack_error = 1;
unstack_error = 2;
  
```

---

---

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#40(1/2)

```

29.00003 "-----
29.00004 "
29.00005 "  MODULE NAME:          TOS_CC.I
29.00006 "  MODULE ID NMB:       CSS/9121
29.00007 "  MODULE VERSION:      02
29.00008 "  MODULE RELEASE:       04
29.00009 "  RELEASE DATE:        831211
29.00010 "-----
29.00011 "
29.00012 %NOLIST  %NOLIST  %NOLIST
29.00013
29.00014 " TOS completion codes:
29.00015 " =====
29.00016
29.00017 const
29.00018     TOS_cc = TOS_error shiftl 8;
29.00019
29.00020     TOS_init_error
29.00021     TOS_declaration_error           = #01 + TOS_cc;
29.00022     TOS_wrong_profile               = #02 + TOS_cc;
29.00023     TOS_no_CPU_pool                 = #03 + TOS_cc;
29.00024     TOS_no_PS_handle                = #04 + TOS_cc;
29.00025     TOS_no_DS_handle                = #05 + TOS_cc;
29.00026     TOS_no_Pascal_RTS              = #06 + TOS_cc;
29.00027     TOS_CMI_init_error              = #07 + TOS_cc;
29.00028     TOS_syntax_error                = #08 + TOS_cc;
29.00029     TOS_illegal_kind                = #09 + TOS_cc;
29.00030     TOS_filenamelist_too_long       = #0A + TOS_cc;
29.00031     TOS_already_logged_in           = #0B + TOS_cc;
29.00032     TOS_not_directory                = #0C + TOS_cc;
29.00033     TOS_range_error                  = #0D + TOS_cc;

```

```

29.00034     TOS_not_integer                  = #0F + TOS_cc;
29.00035     TOS_too_many_parameters          = #10 + TOS_cc;
29.00036     TOS_wrong_version                = #11 + TOS_cc;
29.00037     TOS_lack_of_workarea            = #12 + TOS_cc;
29.00038
29.00039
29.00040
29.00041     TOS_too_many_FMS_devices           = #16 + TOS_cc;
29.00042     TOS_too_many_TMS_devices          = #17 + TOS_cc;
29.00043     TOS_too_many_TMS_subdevices       = #18 + TOS_cc;
29.00044     TOS_username_missing              = #19 + TOS_cc;
29.00045     TOS_load_error                    = #1A + TOS_cc;
29.00046     TOS_entry_not_found               = #1B + TOS_cc;
29.00047     TOS_file_not_found                = #1C + TOS_cc;
29.00048     TOS_illegal_parameter             = #1D + TOS_cc;
29.00049     TOS_unknown_device                = #1E + TOS_cc;
29.00050     TOS_not_dual                      = #1F + TOS_cc;
29.00051     TOS_initialization_error         = #20 + TOS_cc;

```

===== TOS\_CC.I continued

sign/data

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#40 (2/2)

oraltter

projekt

29.00052	TOS_unknown_command	= #21 + TOS_cc;
29.00053	TOS_wrong_password	= #22 + TOS_cc;
29.00054	TOS_too_many_users	= #23 + TOS_cc;
29.00055	TOS_unknown_volume	= #24 + TOS_cc;
29.00056	TOS_delete_subdevices_first	= #25 + TOS_cc;
29.00057	TOS_programsegment_missing	= #26 + TOS_cc;
29.00058	TOS_datasegment_missing	= #27 + TOS_cc;
29.00059	TOS_too_many_processes	= #28 + TOS_cc;
29.00060	TOS_too_many_levels_of_DO	= #29 + TOS_cc;
29.00061	TOS_unknown_process	= #2A + TOS_cc;
29.00062	TOS_Not_allowed	= #2B + TOS_cc;
29.00063	TOS_too_many_filesystems	= #2C + TOS_cc;
29.00064	TOS_illegal_command	= #2D + TOS_cc;
29.00065	TOS_runtime_error	= #2E + TOS_cc;
29.00066	TOS_wrong_userparametersize	= #2F + TOS_cc;
29.00067	TOS_illegal_address	= #30 + TOS_cc;
29.00068	TOS_bootload_error	= #31 + TOS_cc;
29.00069	TOS_wrong_level	= #32 + TOS_cc;
29.00070	TOS_wrong_pool_type	= #33 + TOS_cc;
29.00071	TOS_pool_not_private	= #34 + TOS_cc;
29.00072	TOS_missing_pool	= #35 + TOS_cc;
29.00073	TOS_max_claim_too_small	= #36 + TOS_cc;
29.00074	TOS_too_few_resources	= #37 + TOS_cc;
29.00075	TOS_lack_of_resources	= #38 + TOS_cc;

29.00076	TOS_load_request_queued	= #39 + TOS_cc;
29.00077	TOS_not_contiguous	= #3A + TOS_cc;
29.00078	TOS_resourceclaim_too_small	= #3B + TOS_cc;
29.00079	TOS_no_TOS_BS_sync_el	= #3C + TOS_cc;
29.00080	TOS_no_BS_TOS_sync_el	= #3D + TOS_cc;
29.00081	TOS_not_reserved	= #3E + TOS_cc;
29.00082	TOS_unknown_filesystem	= #3F + TOS_cc;
29.00083	TOS_illegal_fileorganization	= #40 + TOS_cc;
29.00084	TOS_simultaneous_dualization_not_allowed	= #41 + TOS_cc;
29.00085	TOS_not_mounted	= #42 + TOS_cc;
29.00086	TOS_stack_overflow	= #43 + TOS_cc;
29.00087	TOS_active_processes	= #44 + TOS_cc;
29.00088		
29.00089	" TOS services:	
29.00090	TOS_service	= #80 + TOS_cc;
29.00091	TOS_cml_create_and_load	= #81 + TOS_cc;
29.00092	TOS_create_and_load	= #82 + TOS_cc;
29.00093	TOS_get_user_params	= #83 + TOS_cc;
29.00094	TOS_get_system_fms_params	= #84 + TOS_cc;
29.00095		
29.00096	type	
29.00097	" parameters for TOS_get_system_fms_params:	
29.00098	gsfp_par = (gsfp_fsn, gsfp_vol, gsfp_name, gsfp_dev01);	
29.00099	"FF	



## DAMOS COMPLETION CODES

sign/date  
840321page  
46replace  
ISSUE 1.1project  
CAMPS

#43

MODULE NAME: TDX FW COMPLETION CODES  
 MODULE ID NMB: CSS/9134  
 MODULE VERSION: 99  
 MODULE RELEASE: 99  
 RELEASE DATE: 340105

```

TDX_FW_err = TDX_FW_error*256;
output_err = £0000;
input_err = £0030;

TDX_FW_buffer_overflow = 1;
TDX_FW_frame_timeout = 2;
TDX_FW_datagram_maxsize = 3;
TDX_FW_overflow = 4;
TDX_FW_DMA_error = 5;

"Failure in TDX controller, TDX bus or STI-TIA
"A TDXH buffer were not able to receive a complete TDX packet.

"Timeout, parity_error or other errors has
"occurred during DMA transfer between STI and CR80 memory

TDX_FW_packet_timeout = 6;

"Transmitted data has not been received by remote device
"or STI reception of a packet in progress
"has not been completed within a certain time

TDX_FW_flow_timeout = 7;
TDX_FW_datagram_timeout = 3;

TDX_FW_output_buffer_overflow = TDX_FW_err + output_err + TDX_FW_buffer_overflow;
TDX_FW_output_frame_timeout = TDX_FW_err + output_err + TDX_FW_frame_timeout;
TDX_FW_output_datagram_maxsize = TDX_FW_err + output_err + TDX_FW_datagram_maxsize;
TDX_FW_output_overflow = TDX_FW_err + output_err + TDX_FW_overflow;
TDX_FW_output_DMA_error = TDX_FW_err + output_err + TDX_FW_DMA_error;
TDX_FW_output_packet_timeout = TDX_FW_err + output_err + TDX_FW_packet_timeout;
TDX_FW_output_flow_timeout = TDX_FW_err + output_err + TDX_FW_flow_timeout;
TDX_FW_output_datagram_timeout = TDX_FW_err + output_err + TDX_FW_datagram_timeout;

TDX_FW_input_buffer_overflow = TDX_FW_err + input_err + TDX_FW_buffer_overflow;
TDX_FW_input_frame_timeout = TDX_FW_err + input_err + TDX_FW_frame_timeout;
TDX_FW_input_datagram_maxsize = TDX_FW_err + input_err + TDX_FW_datagram_maxsize;
TDX_FW_input_overflow = TDX_FW_err + input_err + TDX_FW_overflow;
TDX_FW_input_DMA_error = TDX_FW_err + input_err + TDX_FW_DMA_error;
TDX_FW_input_packet_timeout = TDX_FW_err + input_err + TDX_FW_packet_timeout;
TDX_FW_input_flow_timeout = TDX_FW_err + input_err + TDX_FW_flow_timeout;
TDX_FW_input_datagram_timeout = TDX_FW_err + input_err + TDX_FW_datagram_timeout;

```