

ROCS

3270
MAPPING MODULE
SLAVE

SIMS
SW 3805/1

 RCNET

General

SIMS is interfacing ROCS to the 3270 slave protocol module (SW 3807) which enables the connection of Host computers to ROCS networks via a number of BSC links. The main task of SIMS is to handle the buffer administration when datastreams are mapped between ROCS and the 3270 slave protocol module (S3270).

Furthermore SIMS is supervising the state of the Host computers, and if one is not accessible actions are taken accordingly.

The SIMS module defines the actual Control unit configurations simulated by the S3270 module. The module operates autonomously without any local operator interventions.

Functional description

SIMS can be divided into three logical parts: ROCS interface, S3270 interface and operator control.

ROCS interface

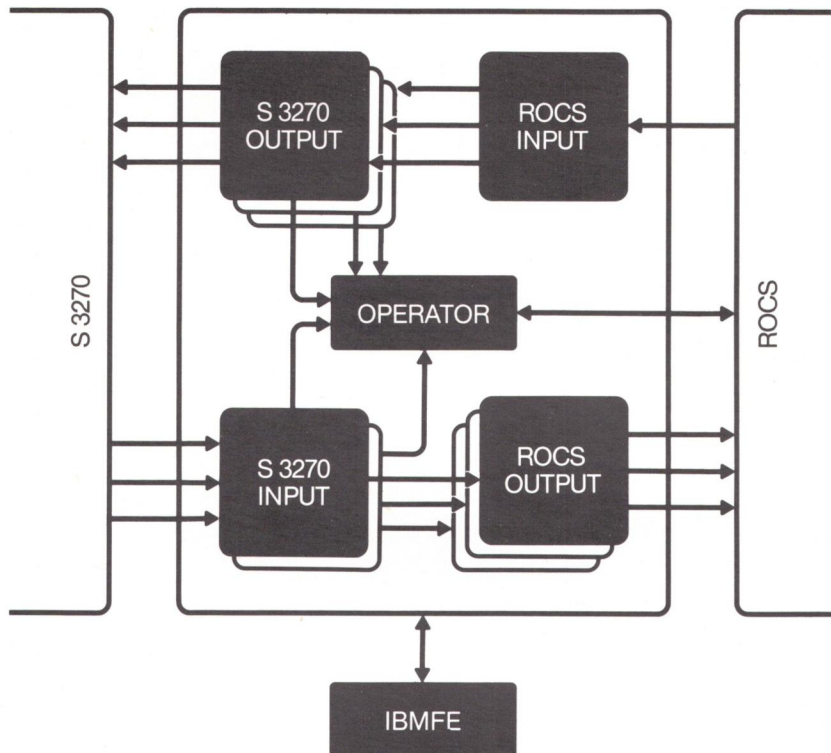
Input: A number of input buffers are sent to ROCS, and they will be returned when ROCS has data available. Together with data ROCS delivers a destination address, and after a validity check data is guided to the correct port of the S3270 interface.

Output: When data is received from the S3270 interface it will be sent to ROCS. If ROCS does not accept data, an error message will be generated and data will be discarded.

S3270 Interface:

Input: S3270 receives eight input buffers for each line connected to a Host computer. This ensures that the Host computer can deliver the maximum number of blocks belonging to one transaction without getting short of buffers. Together with data an identification of the sending device is received and is used to guide data to the correct port of the ROCS interface.

Output: When data is received from ROCS, it is investigated if the Host computer is connected or disconnected. If the Host computer is disconnected, an error message is created and returned to the sending device in the remote station telling that the Host is disconnected. If the Host computer is connected data is sent to S3270. If S3270 does not accept data due to address or format error the transaction will be discarded, and an error message is generated.



Internal structure

The internal program and buffer structure is described in ref. 1.

Operators Messages:

Most operator messages are informative and will not influence the program run. Operator messages are used in connection with errors to indicate the type of errors detected. All operator messages are described and explained in ref. 2.

Configurationtable:

SIMS is using a configuration table called IBMFE. This table contains information of all existing control units at the front-end station. A description of the configurationtable is found in ref. 1.

Only the table has to be changed if the front-end has to be reconfigured. SIMS remains unchanged as long as the number of control units it is generated to support is greater than or equals the number of control units in IBMFE.

Environments/program size

Hardware requirements: RC3803 CPU, 64KB or 128Kb memory.

Software requirements: MUS or DOMUS operating system.

MUSIL coroutine monitor: CM011 or later versions.

Configuration table IBMFE.

MUSIL code procedure module CPM RC36-00995.01 or compatible versions.

Program size for a version supporting 1 line and 1 CU:

NSIZE: 7886 byte.

XSIZE: 3850 bytes.

One extra CU requires approximately 186 bytes (NREL).

One extra 3271 line needs approximately 5006 bytes (NREL).

Documentation

1. SIMS Programmers Reference. Manual RCSL: 43-GL10613.

2. ROCS/3270 Operators Reference Manual RCSL: 43-GL10936.



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AS REGNECENTRALEN af 1979

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