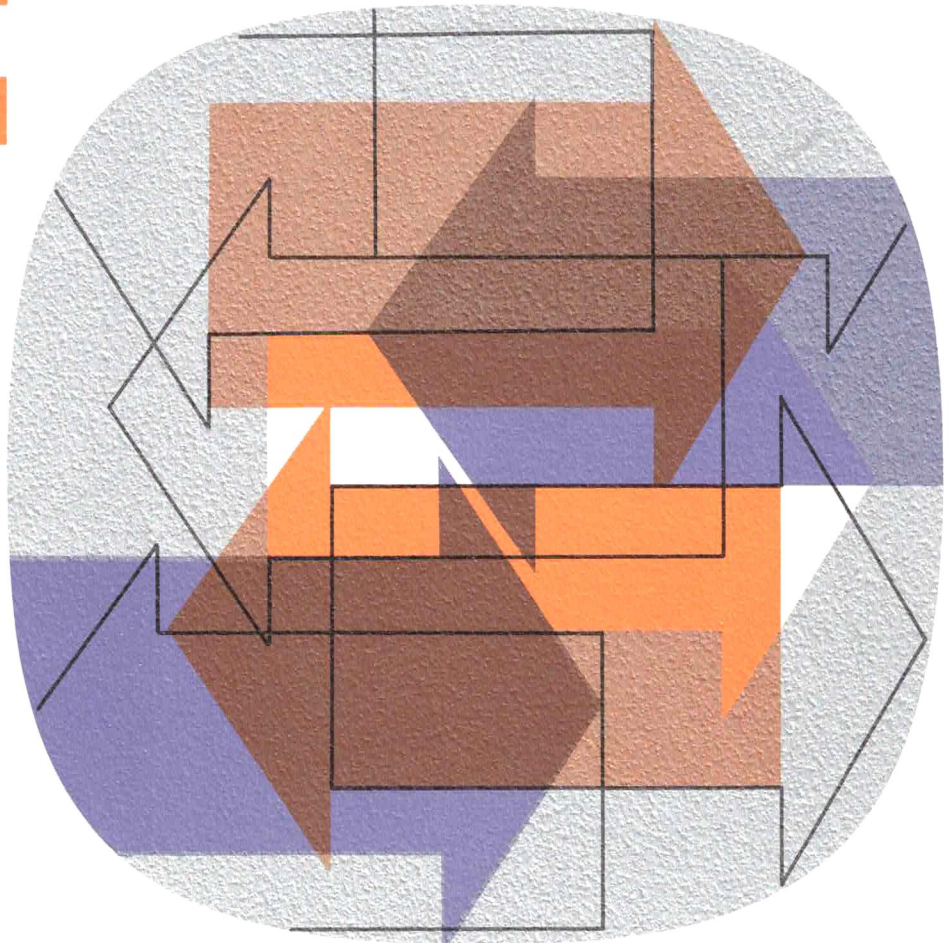


SALES DIVISION



NEW
RC 3500

RC 3500 DATA COMMUNICATIONS CONTROLLER

The basic controller system consists of a micro processor implemented in fourth-generation MSI technology for reliability, low cost, and compactness.

Printed circuits are used for both back-plane and main board connections, thus eliminating wire-wraps for all purposes except connection of power supply.

Finally checked out programs can be implemented in Read-Only Memory for complete security, whilst standard alterable memory is fully protected against power failure.



SPECIFICATION

Store Sizes: 4 K, 8 K, 16 K x 8 bit bytes.

Store Cycle: 2.7 microseconds.

Store Width: 16 bits (2 x 8 bit bytes).

Instructions: 16 bits.

Registers: 4 x 16 bit accumulators/index registers.

Addressing: direct, 8 bit displacement; indirect, multi-level; relative; indexed.

Typical Store/Register Instruction Time: 5.2 microseconds.

Hardware Priority Interrupt Levels: 16.

I/O Bus Addresses: 64.

Direct Memory Channel Capacity: over 450 K bytes/second.

Interface: paper tape reader (RC 2000); additional interfaces being developed.



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