

- C 256 Kbyte RAM
- E 64 Kbyte Optional EPROM

 Multible Bit Error Detection

- Cone Bit Error Correction
- E Error Log
- Typical Access Time 720 ns

RC3515 is a memory module for the RC3502 computer system. It holds 256 Kbytes of dynamic RAM with error detection and one bit correction facility. An error log is provided for collecting information about detected errors. Optionally up to 64 Kbyte EPROM can also be mounted.



RC3515

1. Specifications:

1.1	Organization:	RAM:	 256 Kbytes organized in 128 Kwords each with 16 data bits and 6 check bits Max. 64 Kbytes organized in 32 Kwords each with 16 data bits, optionally 12 bits wide, 16 locations deep FIFO (first in first out register)
		EPROM:	
		Error Log:	
1.2	Address Area:	RAM:	Startaddress modulus 64 Kbytes from 80.0000 to FE.0000 Reduced memory size if startaddress is greater than F8.0000, i.e. max. address is FE.FFFF Startaddress modulus 64 Kbytes from 00.0000 to FE.0000; occupies 64 Kbytes, applicable size is from 4 Kbytes to 64 Kbytes. May be disabled by switch
		EPROM:	
1.3	Access Types:	RAM:	Word and byte read Word and byte write
		EPROM: Error Log:	Word and byte read Byte read
1.4	Typical Access Timing:	RAM read: RAM write: EPROM read Error log read	
1.5	Power Consumption:	+5V/ 4.5A max.	
1.6	Heat Dissipation:	20 kcal/h = 84 kJoule/h	
1.7	Panel Width:	15.24 mm (3T)	



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