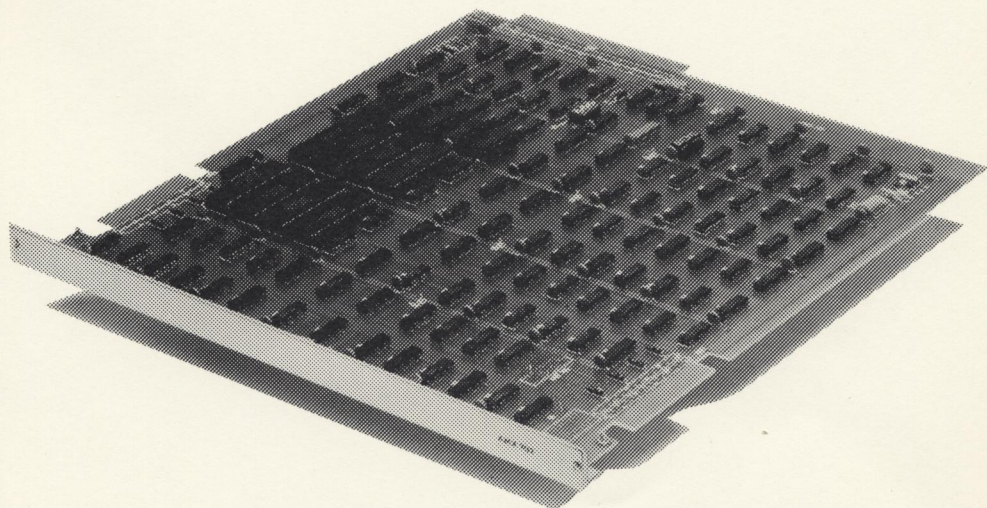# RC 3682
8-Line
Asynchronous
Multiplexer

- ● **ASYNCHRONOUS TRANSMISSION**

- ● **UP TO 9600 BITS PER SECOND**

- ● **COMPLETE COMPATIBILITY WITH CCITT V. 24 RECOMMENDATION**

- ● **FULL OR HALF DUPLEX**

- ● **EXPANDABLE TO 32 LINES**

GENERAL

The RC3682 asynchronous multiplexer interfaces the RC3600 system to any 8 asynchronous half- or full duplex lines, according to CCITT V. 24 recommendations at speeds up to 9600 bits per second. A channel can either be connected to a modem, to another asynchronous communication controller or to an asynchronous terminal, by use of the appropriate features.

CHARACTERISTICS

The RC3682 asynchronous multiplexer consists of 8 separate receivers, 8 transmitters, 8 input buffers and 8 output buffers, each buffer is 32 bytes deep. The bit rate is selected by the program and can be different for receiver and transmitter. Also the character length, the number of stop bits and the parity mode are selected by the program.

The F82B 8-line V. 24 junction panel serves to separate the output lines from the RC3682 into 8 connectors fulfilling the CCITT V. 24 recommendations (ISO 2110).

The F86 8-line current-loop adapter serves to separate the output lines from the RC3682 into 8 connectors fulfilling the RC current-loop specifications.

SPECIFICATIONS

	per channel
Data format:	Serial asynchronous with 5, 6, 7 or 8 bits per character, and 1 or 2 stop bits. Generation/detection of parity, or no parity
Speeds:	40, 50, 75, 110, 134.5, 150, 200, 220, 300, 600, 1200, 2400, 4800 or 9600 bps
Signal levels:	As specified in CCITT recommendation V. 28
Signals used (V. 24):	Signal Ground (102)
	Transmitted Data (103)
	Received Data (104)
	Request to Send (105)
	Ready for Sending (106)
	Dataset Ready (107)
	Data Terminal Ready (108/2)
	Received Carrier (109)
	Calling Indicator (125)
	Power On (not V. 24)
Features:	F 82B 8-line V. 24 junction panel
	F86 8-line current-loop adapter