

GENERAL

Up to four F 83 16 Line V. 24 Adaptors may be connected to the RC 3683 64 Channel Asynchronous Multiplexer. The F 83 is a 2 x 8 line V. 24 adaptor. For each group of eight lines a number of parameters can be independently selected: channel mode, transmission speed, number of data bits per character, and number of stop bits.

CHARACTERISTICS

Channel mode may be half- or full-duplex. The F 83 may, therefore, be configured for 2 x 8 full-duplex lines, 8 full- and 8 half-duplex lines, or 2 x 8 half-duplex lines, occupying respectively 32, 24, or 16 multiplexer channels on the RC 3683.

The F 83 is connected to a communications device via the F 82 8 Line V. 24 Junction Panel or the F 86 8 Line Current Loop Junction Panel.

The F 82 8 Line V. 24 Junction Panel serves to separate the output lines from the F 83 into 8 connectors fulfilling the CCITT V. 24 recommendations.

The F 86 8 Line Current Loop Junction Panel serves to separate the output lines from the F 83 into 8 connectors fulfilling the RC current loop specifications.

SPECIFICATIONS				
F 83 16 Line Modem Adaptor Channel Mode		2 x 8 full-duplex	8 half-duplex 8 half-duplex	2 x 8 half-duplex
Number of Multiplexer Channels Required		32	24	16
Junction panel: Number of lines: Signal levels: Transmission speeds: Signals used:	As in C 2400/r betwee Signal Transn Receiv Datase Data T Receiv	of full-duplex	F 86 8 half- or full-duplex V. 28 Logical one: 20 mA Logical zero: 0 mA 2400/n, where n is an integer between 1 and 64 Two current loops are used in full-duplex and One current loop is used in half-duplex	
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