

GENERAL

The RC 3683 asynchronous multiplexer consists of a control module housed in its own special controller chassis. It is the controller for the F 83 16 Line V. 24 Adaptor and the F 84 4 Line Telex Adaptor. When used with the F 83, it consists of two boards, but when used with the F 84, it consists of three boards.

CHARACTERISTICS

The RC 3683 can simultaneously control both full- and half-duplex equipment. This is done under the following principle: each half-duplex line accommodates one multiplexer channel and each full-duplex line accommodates two multiplexer channels.

The adaptors enable the RC 3683 to simultaneously control communications devices with different transmission speeds, number of data bits per character, and number of stop bits. This is further explained on the pages on adaptors for the RC 3683.

The RC 3683 special controller chassis has room for up to three adaptors. Additional adaptors can be housed in F 09 Additional Controller Chassis, in a special version.

SPECIFICATIONS (Per Channel)

Data format:	Serial asynchronous with 1 start bit,
	5, 6, 7, or 8 data bits, and
	1, 1.5 or 2 stop bits
Transmission speeds:	Depends on adaptors (see F 83 or F 84)
Speed stability:	Better than 0.01 %
Distortion:	35 % maximum on received data
	0.2 % maximum on transmitted data
	Spurious start pulses with a duration of less than
	a half bit time are rejected
Features:	F 83 16 Line V. 24 Adaptor
	F 84 4 Line Telex Adaptor

REGNECENTRALEN Scanips COMPUTER

RCSL 42-i 0491