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**Title:**

CPU823  
Microprogram

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**Keywords:**

RC8000, CPU823, RC8006-MP, Listing.

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**Abstract:**

Listing of the microprogram and additional tables for CPU823.

The CPU823 is used for RC8000 in connection with Multi Processor configuration.

(266 printed pages)

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INTRODUCTION

This manual is the listings of the micro program for the CPU823, which is the CPU used in MP configurations of RC8000. This manual gives only a survey of the micro instructions, the different formats and the layout (page 2). A detailed description can be found in the Technical manual for the CPU821.

The manual consists of 5 parts:

1. Microinstruction format, definition of microinstruction parameters and Microinstructions.
2. CPU823 Microprogram listing.
3. Address calculation decoding map.
4. Instruction decoding map.
5. Prefetch and operandfetch decoding map.

The microprogram is written in symbolic assembly language using the RC3600 macro assembler DOMAC, and additional programs for formatting of listings and for generating the PROM's.

The microinstruction definitions consists of, the definitions of parameters and comments for the instruction definitions.

The microprogram listing consists of 5 columns with the following information.

1. Line number, 2 decimal digits
2. Control store address, 5 octal digits
3. Contents of control store location, 16 octal digits
4. Microprogram instruction
5. Comments preceded by a semicolon

FORMAT 0: Read/Load Scratchpad

0	1	4	5	6	7	9	10	19	20	24	25	29	30	35	36	41	42	47
P	NEXT	CL	F		ALU				A	B	UNIT FUNC/TEST		SCRATCHP. ADDR.		INT/EXT REG DEST			
			0	0	0	D	OP	FUNC	C									

FORMAT 1: Read/Load Register

0	1	4	5	6	7	9	10	19	20	24	25	29	30	35	36	41	42	47
P	NEXT	CL	F		ALU				A	B	UNIT FUNC/TEST		INT REG SOURCE		INT/EXT REG DEST			
			0	0	1	D	OP	FUNC	C									

FORMAT 2: Read Constant

0	1	4	5	6	7	9	10	19	20	24	25	29	30	35	36	41	42	47
P	NEXT	CL	F		ALU				A	B	UNIT FUNC/TEST		CONSTANT ADDR		INT/EXT REG DEST			
			0	1	0	D	OP	FUNC	C									

FORMAT 3: Read External Register

0	1	4	5	6	7	9	10	19	20	24	25	29	30	35	36	41	42	47
P	NEXT	CL	F		ALU				A	B	UNIT FUNC/TEST		EXT REG SOURCE		INT/EXT REG DEST			
			0	1	1	D	OP	FUNC	C									

FORMAT 4: Half-word Manipulator

0	1	4	5	6	7	9	10	19	20	24	25	29	30	35	36	41	42	47
P	NEXT	CL	F		ALU				A	B	NOT USED		MANIPULATOR FUNCTION		NOT USED			
			1	0	0	D	OP	FUNC	C									

FORMAT 5: Load Immediate

0	1	4	5	6	7	9	10	19	20	24-BIT IMMEDIATE OPERAND						43	44	47			
P	NEXT	CL	F		ALU														NOT USED		
			1	0	1	D	OP	FUNC	C												

FORMAT 6: Conditional Jump

0	1	4	5	6	7	9	10	19	20	24	25	27	28	29	30	31	35	36	47
P	NEXT	CL	F		ALU				A	NOT USED	S	X	T	COND SELECT		JUMP ADDRESS			
			1	1	0	D	OP	FUNC	C										

FORMAT 7: Shift, Multiply, Divide

0	1	4	5	6	7	9	10	19	20	24	25	29	30	31	35	36	37	38	40	41	42	43	44	47
P	NEXT	CL	F		ALU				A	B	T	COND SELECT		N	S	SI	M,D	TST	NOT USED					
			1	1	1	D	OP	FUNC	C															

Figure 1: Microinstruction formats.

```

01 GC01 .MAIN DOMUS MACRO ASSEMBLER REV 02.00
02
03 ;FILE: TMAC3, EDITED: 850328/LBJ
04 ; CPU820 DEFINITION OF MICROINSTRUCTIONS
05 ; AND MICROINSTRUCTION PARAMETERS
06 .XPNG
07 .NOMA 1
08 000000 LIST=0 ; 0: MACROS ARE NOT LISTED
09
10 ;*****
11 ;DEFINITION OF MICROINSTRUCTION PARAMETER
12 ;*****
13
14 ;ALU FUNCTION AND CARRY, MIR(15:19)
15
16 000000 ADD=C ; H+S
17 000001 ADD=1 ; R+S+1
18 000002 ADDC=2 ; R+S+C
19 000003 ADDX=3 ; R+S+ADDCND
20 000004 SUN=4 ; -R+S-1
21 000005 SUNO=5 ; -R+S
22 000006 SUNC=6 ; -R+S-1+C
23 000007 SUNX=7 ; -R+S-1+ADDCND
24 000010 SUB=10 ; R-S-1
25 000011 SUBO=11 ; R-S
26 000012 SUBC=12 ; R-S-1+C
27 000013 SUBX=13 ; R-S-1+ADDCND
28 000014 OR=14 ; R OR S
29 000020 AND=20 ; R AND S
30 000024 CAND=24 ; -R AND S
31 000030 EXOR=30 ; R EXOR S
32 000034 EXNOR=34 ; -(R EXOR S)
33

```

10002 .MAIN

01 ;GENERAL REGISTER ADDRESSES, MIR(20:24) AND MIR(25:29)  
02  
03

04 00000 W=0 ; W-FIELD OF INSTR. REGISTER  
05 00001 WPRE=1 ; WPRE OF INSTR. REGISTER  
06 00002 X=2 ; X-FIELD OF INSTRUCTION REGISTER  
07 00003 LAX=3 ; LAST(21:22)  
08 000020 WU=20 ; WU  
09 000021 W1=21 ; W1  
10 000022 W2=22 ; W2  
11 000023 W3=23 ; W3  
12 000024 STAT=24 ; CPU STATUS  
13 000025 CAUSE=25 ; CAUSE REGISTER  
14 000026 SB=26 ; SB REGISTER  
15 000027 INF=27 ; INF REGISTER  
16 000030 =2=30 ; 2  
17 000031 CNTR=31 ; CONTROL OUTPUT COPY REGISTER  
18 000032 WRKC=32 ; WORK REGISTER  
19 000033 WRK1=33 ; D0.  
20 000034 WRK2=34 ; D0.  
21 000035 WRK3=35 ; D0.  
22 000036 WRK4=36 ; D0.  
23 000037 WRK5=37 ; D0.

24 ;SCRATCHPAD ADDRESSES, MIR(36:41)

25  
26  
27 00000 CPAC=0 ; CPA LIMIT COPY  
28 00001 BASEC=1 ; BASE COPY  
29 00002 LLIMC=2 ; LOWER LIMIT COPY  
30 00003 ULIMC=3 ; UPPER LIMIT COPY  
31 00004 ILIMC=4 ; INTERRUPT LIMIT COPY  
32 00005 SIZE=5 ; SIZE REGISTER  
33 00006 MONT=6 ; MONITOR TOP REGISTER  
34 00007 CUR=7 ; CURRENT PROCESS REGISTER  
35 000010 PUIX=10 ; PU INDEX  
36 000011 PUTAB=11 ; PU TABLE  
37 000012 EXOFS=12 ; OFFSET IN PROC. DESC. OF EXCEPTION ADDRESS  
38 000013 DMOFS=13 ; OFFSET IN PROC. DESC. OF DUMP AREA  
39 000014 SWK1=14 ; WRK REGISTER 0  
40 000015 SWRK1=15 ;  
41 000016 DATSW=16 ; DATA SWITCH  
42 000017 REGSW=17 ; REGISTER SWITCH

100C3 .MAIN

```
01 000035 ;CONSTANT ;BIT 0
02 000033 ;BIT 1
03 000032 ;BIT 2
04 000031 ;BIT 3
05 000027 ;BIT 4
06 000026 ;BIT 5
07 000025 ;BIT 6
08 000024 ;BIT 7
09 000023 ;BIT 12
10 000022 ;BIT 14
11 000021 ;BIT 16
12 000020 ;BIT 17
13 000017 ;BIT 18
14 000016 ;BIT 19
15 000036 ;BIT 20
16 000015 ;BIT 21
17 000037 ;BIT 23
18 000037 ; 1
19 000014 ; 3
20 000015 ; 4
21 000030 ; 6
22 000034 ; 7
23 000036 ; 8
24 000013 ; 10
25 000012 ; 11
26 000011 ; 12
27 000010 ; 14
28 000007 ; 15
29 000016 ; 16
30 000006 ; 21
31 000005 ; 22
32 000004 ; 23
33 000017 ; 32
34 000002 ; 38
35 000020 ; 64
36 000003 ;BIT(12:23)
37 000001 ;BIT(12:19)
38 000000 ;BIT(12:14)
39
40
```

100C4 .MAIN

01 ;SOURCE REGISTER ADDRESSES, MIR(36:41)

02 ;INTERNAL REGISTERS

03 000000 DISP=0 ; INSTRUCTION DISPLACEMENT, D  
04 000010 ICS=10 ; INSTRUCTION COUNTER, SOURCE  
05 000020 DIC=20 ; DISP+IC  
06 000030 LCS=30 ; LOOP COUNTER, SOURCE  
07 000040 STOC=40 ; DBUS(0:21) CON OVERFLOW CON CARRY  
08 000041 STSZ=41 ; DBUS(0:21) CON SHIFT OFL. CON ZERO  
09 000042 STZZ=42 ; DBUS(0:21) CON ZERO CON ZERO  
10 000050 SWITCH=50; SWITCHES AND CONFIGURATION

11 ;EXTERNAL REGISTERS

12 000000 CDATI=0 ; CPU DATAIN  
13 000001 CIOST=1 ; CPU I/O STATUS  
14 000002 RTC=2 ; REAL TIME CLOCK  
15 000003 ILEV=3 ; INTERRUPT LEVEL  
16 000004 TPIN=4 ; TECHNIITIANS CONSOLE, DATAIN  
17 000005 TCPST=5 ; TECHNIITIANS CONSOLE, STATUS

18 000040 DATI=40 ; CACHE/CPU DATAIN  
19 000041 IOSTA=41; CACHE/CPU I/O STATUS

20 000070 FPURO=70; FPU FRACTION(0:23)  
21 000071 FPUR1=71; FPU FRACTION(24:35) CON EXP(0:11)  
22 000072 FPUST=72; FPU EXCEPTION(22:23)

23  
24  
25  
26  
27  
28  
29



```

10005 .MAIN
01 ;DESTINATION REGISTER ADDRESSES, MIR(42:47)
02
03
04 ; 0=NO LOAD
05 ; LOGICAL ADDRESS.STATUS
06 ; 2= SCRATCH PAD
07 ; INSTRUCTION COUNTER, DESTINATION
08 ; LOOP COUNTER, DESTINATION
09 ; CPU STATUS REGISTER
10 ; CONTROL OUTPUT REGISTER
11 ; NOT USED
12 ; NOT USER
13 ; LOGICAL ADDRESS & LOG ADDR.STATUS
14 ; DATA OUT
15 ; BASE
16 ; LOWER LIMIT
17 ; UPPER LIMIT
18 ; CPA LIMIT
19 ; REAL TIME CLOCK, READ COMMAND
20 ; CLEAR ADDRESSED INTERRUPT LEVEL
21 ; INTERRUPT LIMIT
22 ; INTERRUPT LEVEL, READ COMMAND
23 ; TECHNICIANS CONSOLE, DATAOUT
24 ; CACHE BYPASS CONTROL
25 ; CLEAR TC INTERRUPT
26
27 ; 000040 CAMTD=40; CACHE TEST DATA
28 ; 000041 CAMCN=41; CACHE CONTROL
29
30 ; 000070 FPOP0=70; FPU FRACTION(0:23)
; 000071 FPOP1=71; FPU FRACTION(24:35) CON EXP(0:11)

```

10006 .MAIN

```
01 ;JUMP CONDITIONS, MIR(31:35)
02
03
04 FALSE=0 ; THIS CONDITION IS ALWAYS FALSE
05 NEG=1 ; RESULT<C, F(O)
06 NZ=2 ; RESULT<>C
07 OVFL=3 ; ARITHMETIC OVERFLOW
08 CARRY=4 ; ARITHMETIC CARRY
09 SIGN=5 ; ARITHMETIC SIGN, F(O) EXOR OVERFLOW
10 NORMC=6 ; RES(O)<>RES(1)
11 NORM1=7 ; RES(1)<>RES(2)
12 DIVOF=10; DIVISION OVERFLOW, LINK<>CARRY(O)
13 LCN=11 ; LC(O)
14 MAXLO=12; LC=0 OR LC>48
15 NwADR=13; NOT W-ADDRESS, LAST(O:20)<>O
16 NGADR=14; LA(O)
17 ;
18 MMODE=16; MONITOR MODE
19 EMODE=17; ESCAPE MODE
20 CPUIN=20; CPU INTERRUPT
21 EXINT=21; DEVICE INTERRUPT
22 NTCPI=22; NOT TCP INPUT
23 NAUXI=23; NOT AUX INTERRUPT
24 NNAME=24; NOT CAM ERROR
25 NOCPA=25; NOT OCP AUTOLoad
26 NREMA=26; NOT REMOTE AUTOLoad
27 PLOW=27 ; POWER LOW
28
29 ;JUMP CONDITION CONTROL, MIR(30)
30
31 F=0 ; THE COMPLEMENTED VALUE OF THE COND. IS SELECTED
32 T=1 ; THE TRUE VALUE OF THE COND. IS SELECTED
33
34 ;SHIFT-IN CONTROL, MIR(37:38)
35
36 Z=0 ; ZERO
37 LNK=1 ; SHIFT LINK
38 ADC=2 ; ADDCOND, LEFT SHIFTS ONLY
39 FU=2 ; F(O), RIGHT SHIFTS ONLY
40 SGN=3 ; SIGN EXTENSION, RIGHT SHIFTS ONLY
```

100C7 .MAIN

```
01
02 ;TEST CONTROL, MIR(42:43)
03
04 000000 DVS=C ; DIVSIGN:=F(C)
05 000001 DC=1 ; ADDCOND:=DIVIDE CONDITION
06 000002 MC=2 ; ADDCOND:=MULTIPLY CONDITION
07 000003 NL=3 ; NO LOAD
08
09 ;SEQUENCE CONTROL, MIR(1:4)
10
11 000003 CONT=3 ; CONTINUE
12 000004 POP=C ; POP AND CONTINUE
13 000005 PUSH=1 ; PUSH AND CONTINUE
14 000006 RTN=10 ; SUBROUTINE RETURN
15 000007 LRTN=13 ; LOOP RETURN, CONDITIONAL
16 000008 NEXT=16 ; GOTO ADDRESS CALCULATION
17 000009 EXEC=17 ; GOTO INSTRUCTION EXECUTION
18
19 ;TEST FUNCTIONS, MIR(30:35)
20
21 000060 TSTIN=60; INTERNAL INTERRUPT + DEV. INTERRUPT
22 000050 TSTBE=50; I/O ERROR
23 000041 TSTLI=41; LIMIT VIOLATION
24 000051 TSTIO=51; I/O ERROR + LIMIT VIOLATION
25 000044 TSTOV=44; INTEGER MASK & OVERFLOW
26 000042 TSTSH=42; INTEGER MASK & SHIFT OVERFLOW
27 000071 TIOIN=71; I/O ERROR+ LIMIT VIOLATION + INTERRUPT
28 000064 TOVIN=64; INTEGER MASK & OVERFLOW + INTERRUPT
29 000062 TSHIN=62; INTEGER MASK & SHIFT OVERFLOW + INTERRUPT
30 000061 TLIIN=61; INTERRUPT + LIMIT VIOLATION
```

10008 .MAIN

;UNIT FUNCTIONS, MIR(30:35)

000021 READ=21 ; READ, NO LIMIT CHECK  
000020 READP=20; READ, W. LIMIT CHECK  
000023 WRT=23 ; WRITE, NO LIMIT CHECK  
000022 WRTP=22 ; WRITE, W. LIMIT CHECK  
000010 READC=10; READ W. PROTECTION IF MEM. REF. INSTRUCTION  
000024 READJ=24; READ W. PROTECTION, FETCHED OP. IS USED AS INSTRUCTION  
000025 REAJ=25 ; READ NO PROTECTION, FETCHED OP IS USED AS INSTRUCTION  
000000 ; UNIT FUNCTIONS FOR FPU:  
000000 FAOP=0 ; FLOATING ADD  
000001 FSOP=1 ; FLOATING SUBTRACT  
000002 FMOP=2 ; FLOATING MULTIPLY  
000003 FDOP=3 ; FLOATING DIVIDE  
000004 WMOP=4 ; INTEGER WORD MULTIPLY

;HALF-WORD MANIPULATOR FUNCTIONS, MIR(36:41)

000000 HLOAD=0 ; LOAD HALF WORD  
; ODD=0; SBUS:= 12 EXT 0 CON DBUS(0:11)  
; ODD=1; SBUS:=12 EXT 0 CON DBUS(12:23)  
000001 EXLOA=1 ; LOAD EXTENDED  
; ODD=0; SBUS:=12 EXT DBUS(0) CON DBUS(0:11)  
; ODD=1; SBUS:=12 EXT DBUS(12) CON DBUS(12:23)  
000002 HSTOR=2 ; STORE HALF WORD  
; ODD=0; SBUS:= DBUS(12:23) CON 12 EXT 0  
; ODD=1; SBUS:= 12 EXT 0 CON DBUS(12:23)  
000003 MASK=3 ; SET MASK  
; ODD=0; SBUS:= 12 EXT 0 CON 12 EXT 1  
; ODD=1; SBUS:= 12 EXT 1 CON 12 EXT 0  
000004 EXT=4 ; EXTEND  
; SBUS:=12 EXT DBUS(12) CON DBUS(12:23)  
000005 SWAP=5 ; SWAP  
; SBUS:= DBUS(12:23) CON DBUS(0:11)  
000006 LSWAP=6 ; SWAP LEFT HALF WORD  
; SBUS:=12 EXT 0 CON DBUS(0:11)  
000007 RSWAP=7 ; SWAP RIGHT HALF WORD  
; SBUS:=DBUS(12:23) CON 12 EXT 0

10009 .MAIN

!VALUE OF EXTERNAL ADDRESSES

01	000060	IDLE=20*3
02	000071	INTIN=23*3
03	000113	ESCAP=31*3
04	000116	INTC=32*3
05	000121	INTB1=33*3
06	000124	SYFL1=34*3
07	000127	INTF=35*3
08	000132	PFINT=36*3
09	000135	LOCKM=37*3
10	006000	CHCLK=2000*3
11	006003	DISPA=2001*3
12	014000	POWUP=4000*3
13	014003	TCPIN=4001*3
14	014006	OCPAL=4002*3
15	014011	REMAL=4003*3
16	014014	SINGL=4004*3
17	014017	TCPER=4005*3
18	014022	TPOCT=4006*3
19	014025	CRLF=4007*3
20	014030	GCHAR=4010*3
21	014033	TCPI2=4011*3
22	014036	DELAY=4012*3
23	014041	TPTXT=4013*3
24	014044	GTNUM=4014*3
25	014047	RAUTO=4015*3
26	014052	CINTR=4016*3
27	014055	CAMTEST=4017*3
28	014060	CAMER=4020*3
29	014063	SYFLT=4021*3
30	022000	TX=6000*3
31	022003	ATEST=6001*3
32	022006	TERROR=6002*3
33	022011	LOOP=6003*3
34	022014	SIZEM=6004*3
35	022017	T0.4=6005*3

!0011 -MAIN

\*\*\*\*\*  
; DEFINITIONS OF MICROINSTRUCTIONS  
\*\*\*\*\*

\*\*\*\*\*  
;FORMAT 0: ARITHMETIC-LOGIC INSTRUCTIONS WITH SCRATCHPAD  
; SOURCE/DESTINATION.  
\*\*\*\*\*

-----  
;LOAD SCRATCHPAD

;       AQS   ALUFUNC,GRA,SP           NEXT UF/TEST CL  
;       ABS   ALUFUNC,GRA,GRB,SP       NEXT UF/TEST CL  
;       ZQS   ALUFUNC,SP               NEXT UF/TEST CL  
;       ZBS   ALUFUNC,GRB,SP           NEXT UF/TEST CL  
;       ZAS   ALUFUNC,GRA,SP           NEXT UF/TEST CL

-----  
;

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10012 .MAIN

```
01 ; INSTRUCTIONS WITH SCRATCHPAD SOURCE
02 ; *****
03 ; *****
04 ; *****
05 ; NO LOAD
06 ;
07 ; SA ALUFUNC,SP,GRA NEXT UF/TEST CL
08 ; SQ ALUFUNC,SP NEXT UF/TEST CL
09 ; SZ ALUFUNC,SP NEXT UF/TEST CL
10 ; *****
11 ; *****
12 ; *****
13 ; *****
14 ; LOAD GRB
15 ;
16 ; SAB ALUFUNC,SP,GRA,GRB NEXT UF/TEST CL
17 ; SQB ALUFUNC,SP,GRB NEXT UF/TEST CL
18 ; SZB ALUFUNC,SP,GRB NEXT UF/TEST CL
19 ; *****
20 ; *****
21 ; *****
```

10013 .MAIN

```
01 ;-----  
02 ;LOAD Q  
03  
04 ; SAQ ALUFUNC,SP,GRA NEXT UF/TEST CL  
05 ; SQQ ALUFUNC,SP NEXT UF/TEST CL  
06 ; SZQ ALUFUNC,SP NEXT UF/TEST CL  
07  
08 ;-----  
09  
10  
11 ;-----  
12 ;LOAD DESTINATIONS REGISTER  
13  
14 ; SAR ALUFUNC,SP,GRA,DR NEXT UF/TEST CL  
15 ; SQR ALUFUNC,SP,DR NEXT UF/TEST CL  
16 ; SZR ALUFUNC,SP,DR NEXT UF/TEST CL  
17  
18 ;-----  
19
```

10014 .MAIN

```
01 ;-----  
02 ;LOAD GRB, DESTINATIONS REGISTER  
03  
04  
05 ; SABR ALUFUNC,SP,GRA,GRB,DR NEXT UF/TEST CL  
06 ; SQBR ALUFUNC,SP,GRB,DR NEXT UF/TEST CL  
07 ; SZBR ALUFUNC,SP,GRB,DR NEXT UF/TEST CL  
08  
09 ;-----  
10  
11 ;-----  
12 ;LOAD Q, DESTINATION REGISTER  
13  
14  
15 ; SAQR ALUFUNC,SP,GRA,DR NEXT UF/TEST CL  
16 ; SQQR ALUFUNC,SP,DR NEXT UF/TEST CL  
17 ; SZQR ALUFUNC,SP,DR NEXT UF/TEST CL  
18  
19 ;-----
```



10015 .MAIN

```

01 *****
02 ;FORMAT 1: ARITHMETIC-LOGIC INSTRUCTIONS WITH INT. SOURCE
03 ; REGISTER AND INT./EXT. DESTINATION REGISTER
04 *****
05 *****
06 *****
07 *****
08 *****
09 *****
10 *****
11 *****
12 *****
13 *****
14 *****
15 *****
16 *****
17 *****
18 *****
19 *****
20 *****
21 *****

```

```

;-----
;NO LOAD
;
; AQ ALUFUNC,GRA NEXT UF/TEST CL
; AB ALUFUNC,GRA,GRB NEXT UF/TEST CL
; ZQ ALUFUNC NEXT UF/TEST CL
; ZB ALUFUNC,GRB NEXT UF/TEST CL
; ZA ALUFUNC,GRA NEXT UF/TEST CL
; RA ALUFUNC,SR,GRA NEXT UF/TEST CL
; RQ ALUFUNC,SR NEXT UF/TEST CL
; RZ ALUFUNC,SR NEXT UF/TEST CL
;-----

```

10016 .MAIN

```

;-----
;LOAD GRB
;
; AGB ALUFUNC,GRA,GRB NEXT UF/TEST CL
; ABB ALUFUNC,GRA,GRB NEXT UF/TEST CL
; ZQB ALUFUNC,GRB NEXT UF/TEST CL
; ZBB ALUFUNC,GRB NEXT UF/TEST CL
; ZAB ALUFUNC,GRA,GRB NEXT UF/TEST CL
; RAB ALUFUNC,SR,GRA,GRB NEXT UF/TEST CL
; RQB ALUFUNC,SR,GRB NEXT UF/TEST CL
; RZB ALUFUNC,SR,GRB NEXT UF/TEST CL
;-----

```

14 15

10017 .MAIN

```

;-----
;LOAD Q
;
;   AQQ   ALUFUNC,GRA
;   ABQ   ALUFUNC,GRA,GRB
;   ZQQ   ALUFUNC
;   ZBQ   ALUFUNC,GRB
;   ZAQ   ALUFUNC,GRA
;   RAQ   ALUFUNC,SR,GRA
;   RQQ   ALUFUNC,SR
;   RZQ   ALUFUNC,SR
;-----
NEXT UF/TEST CL
NEXT UF/TEST CL
NEXT UF/TEST CL
NEXT UF/TEST CL
NEXT UF/TEST CL
NEXT UF/TEST CL
NEXT UF/TEST CL
NEXT UF/TEST CL

```

10018 .MAIN

```

;-----
;LOAD DESTINATION REGISTER
;
;   AQR   ALUFUNC,GRA,DR
;   ABR   ALUFUNC,GRA,GRB,DR
;   ZQR   ALUFUNC,DR
;   ZBR   ALUFUNC,GRB,DR
;   ZAR   ALUFUNC,GRA,DR
;   RAR   ALUFUNC,SR,GRA,DR
;   RQR   ALUFUNC,SR,DR
;   RZR   ALUFUNC,SR,DR
;-----
NEXT UF/TEST CL
NEXT UF/TEST CL
NEXT UF/TEST CL
NEXT UF/TEST CL
NEXT UF/TEST CL
NEXT UF/TEST CL
NEXT UF/TEST CL
NEXT UF/TEST CL

```

01  
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14  
15

10019 .MAIN

01

02

03

04

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09

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11

12

13

14

10020 .MAIN

01

02

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04

05

06

07

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15

10021 .MAIN

01

02

03

04

05

06

07

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09

```

;-----;
;LOAD GRB, DESTINATION REGISTER
;
;      AQBR  ALUFUNC,GRA,GRB,DR      NEXT UF/TEST CL
;      ABBR  ALUFUNC,GRA,GRB,DR      NEXT UF/TEST CL
;      ZQBR  ALUFUNC,GHB,DR          NEXT UF/TEST CL
;      ZBBR  ALUFUNC,GRB,DR          NEXT UF/TEST CL
;      ZABR  ALUFUNC,GRA,GRB,DR      NEXT UF/TEST CL
;      RABR  ALUFUNC,SR,GRA,GRB,DR   NEXT UF/TEST CL
;      RQBR  ALUFUNC,SR,GRB,DR      NEXT UF/TEST CL
;      RZBR  ALUFUNC,SR,GRB,DR      NEXT UF/TEST CL
;-----;

```

```

;-----;
;LOAD Q, DESTINATION REGISTER
;
;      AQQR  ALUFUNC,GRA,DR          NEXT UF/TEST CL
;      ABQR  ALUFUNC,GRA,GRB,DR      NEXT UF/TEST CL
;      ZQQR  ALUFUNC,DR              NEXT UF/TEST CL
;      ZBQR  ALUFUNC,GRB,DR          NEXT UF/TEST CL
;      ZAGR  ALUFUNC,GRA,DR          NEXT UF/TEST CL
;      RAQR  ALUFUNC,SR,GRA,DR       NEXT UF/TEST CL
;      RQQR  ALUFUNC,SR,DR          NEXT UF/TEST CL
;      RZQR  ALUFUNC,SR,DR          NEXT UF/TEST CL
;-----;

```

```

;-----;
;EXCEPTION MANIPULATION OF GRA, LOAD GRB
;
;      EAB   ALUFUNC,EX-FUNC,GRA,GRB  NEXT UF/TEST CL
;      EQB   ALUFUNC,EX-FUNC,GRA,GRB  NEXT UF/TEST CL
;      EZB   ALUFUNC,EX-FUNC,GRA,GRB  NEXT UF/TEST CL
;-----;

```

10022 .MAIN

01 ;\*\*\*\*\*  
02 ;FORMAT 2: ARITHMETIC-LOGIC INSTRUCTIONS WITH  
03 ; CONSTANT ADDRESS SOURCE.  
04 ;\*\*\*\*\*

05 ;-----  
06 ;NO LOAD  
07 ;

08 ; CA ALUFUNC,CA,GRA NEXT UF/TEST CL  
09 ; CQ ALUFUNC,CA NEXT UF/TEST CL  
10 ;

11 ;-----  
12 ;LOAD GRB  
13 ;

14 ; CAB ALUFUNC,CA,GRA,GRB NEXT UF/TEST CL  
15 ; CQB ALUFUNC,CA,GRB NEXT UF/TEST CL  
16 ; CZB ALUFUNC,CA,GRB NEXT UF/TEST CL  
17 ;

10023 .MAIN

18 ;-----  
19 ;LOAD Q  
20 ;

21 ; CAQ ALUFUNC,CA,GRA NEXT UF/TEST CL  
22 ; CQQ ALUFUNC,CA NEXT UF/TEST CL  
23 ; CZQ ALUFUNC,CA NEXT UF/TEST CL  
24 ;

01

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10024 .MAIN

```

;-----
;LOAD DESTINATION REGISTER
;
;   CAR   ALUFUNC,CA,GRA,DR      NEXT UF/TEST CL
;   CQR   ALUFUNC,CA,DR         NEXT UF/TEST CL
;   CZR   ALUFUNC,CA,DR         NEXT UF/TEST CL
;-----

```

```

;-----
;LOAD GRB, DESTINATION REGISTER
;
;   CABR  ALUFUNC,CA,GRA,GRB,DR  NEXT UF/TEST CL
;   CQBR  ALUFUNC,CA,GRB,DR     NEXT UF/TEST CL
;   CZBR  ALUFUNC,CA,GRB,DR     NEXT UF/TEST CL
;-----

```

```

;*****
;FORMAT 3: ARITHMETIC-LOGIC INSTRUCTIONS WITH EXTERNAL
;SOURCE REGISTER.
;*****

```

```

;-----
;NO LOAD
;
;   XA   ALUFUNC,XR,GRA          NEXT UF/TEST CL
;   XQ   ALUFUNC,XR             NEXT UF/TEST CL
;   XZ   ALUFUNC,XR             NEXT UF/TEST CL
;-----

```

10025 .MAIN

```

01
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```

10026 .MAIN

```
01 ;-----  
02 ;LOAD GRB  
03 ;  
04 ;  
05 ; XAB ALUFUNC,XR,GRA,GRB NEXT UF/TEST CL  
06 ; XQB ALUFUNC,XR,GRB NEXT UF/TEST CL  
07 ; XZB ALUFUNC,XR,GRB NEXT UF/TEST CL  
08 ; XZAB XR,ALUFUNC,GRA,GRB NEXT UF/TEST CL  
09 ; XABB XR,ALUFUNC,GRA,GRB NEXT UF/TEST CL  
10 ; XXZQ XR,ALUFUNC NEXT UF/TEST CL  
11 ;  
12 ;-----  
13 ;
```

10027 .MAIN

```
01 ;-----  
02 ;LOAD Q  
03 ;  
04 ;  
05 ; XAQ ALUFUNC,XR,GRA NEXT UF/TEST CL  
06 ; XQQ ALUFUNC,XR NEXT UF/TEST CL  
07 ; XZQ ALUFUNC,XR NEXT UF/TEST CL  
08 ;  
09 ;-----  
10 ;  
11 ;-----  
12 ;LOAD DESTINATION REGISTER  
13 ;  
14 ; XAR ALUFUNC,XR,GRA,DR NEXT UF/TEST CL  
15 ; XQR ALUFUNC,XR,DR NEXT UF/TEST CL  
16 ; XZR ALUFUNC,XR,DR NEXT UF/TEST CL  
17 ; XZAR XR,ALUFUNC,GRA,DR NEXT UF/TEST CL  
18 ;  
19 ;-----  
20 ;
```

10028 .MAIN

```
01 ;-----  
02 ;LOAD GRB, DESTINATION REGISTER  
03 ;  
04 ; XABR ALUFUNC,XR,GRA,GRB,DR NEXT UF/TEST CL  
05 ; XQBR ALUFUNC,XR,GRB,DR NEXT UF/TEST CL  
06 ; XZBR ALUFUNC,XR,GRB,DR NEXT UF/TEST CL  
07 ;  
08 ;-----  
09 ;
```

10 10029 .MAIN

```
01 ;-----  
02 ;LOAD Q, DESTINATION REGISTER  
03 ;  
04 ; XAQR ALUFUNC,XR,GRA,DR NEXT UF/TEST CL  
05 ; XQQR ALUFUNC,XR,DR NEXT UF/TEST CL  
06 ; XZQR ALUFUNC,XR,DR NEXT UF/TEST CL  
07 ;  
08 ;-----  
09 ;
```

10030 .MAIN

\*\*\*\*\*  
;FORMAT 4: ARITHMETIC-LOGIC INSTRUCTIONS WITH HALF-WORD  
; MANIPULATION OF GRA REGISTER.  
\*\*\*\*\*

-----  
;LOAD GRB  
;     HAB   ALUFUNC,HW=FUNC,GRA,GRB       NEXT CL  
;     HQB   ALUFUNC,HW=FUNC,GRA,GRB       NEXT CL  
;     HZB   ALUFUNC,HW=FUNC,GRA,GRB       NEXT CL  
;-----

\*\*\*\*\*  
;FORMAT 5: LOAD IMMEDIATE  
\*\*\*\*\*

-----  
;NO LOAD  
;     IQ    ALUFUNC,MS=HW,LS=HW       NEXT CL  
;-----

10031 .MAIN

-----  
;LOAD Q  
;     IQQ   ALUFUNC,MS=HW,LS=HW       NEXT CL  
;     IZQ   ALUFUNC,MS=HW,LS=HW       NEXT CL  
;-----

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25  
26  
27  
01  
02  
03  
04  
05  
06  
07  
08



10032 .MAIN

\*\*\*\*\*  
;FORMAT 6: CONDITIONAL JUMP INSTRUCTIONS.  
\*\*\*\*\*

-----  
;CONDITIONAL JUMP.

; JMP T/F,COND,ADDRESS NEXT CL  
; JXAQ ALUFUNC,GRA,T/F,COND NEXT CL  
; JXZQ ALUFUNC,T/F,COND NEXT CL  
; JXZA ALUFUNC,GRA,T/F,COND NEXT CL

-----

10033 .MAIN

-----  
;CONDITIONAL SUBROUTINE CALLS.

; CALL T/F,COND,ADDRESS NEXT CL  
; CXAQ ALUFUNC,GRA,T/F,COND NEXT CL  
; CXZQ ALUFUNC,T/F,COND NEXT CL  
; CXZA ALUFUNC,GRA,T/F,COND NEXT CL

-----

10034 .MAIN

-----  
;CONDITIONAL SUBROUTINE CALLS, LOAD Q

; CXZQQ ALUFUNC,T/F,COND NEXT CL  
; CXAQQ ALUFUNC,GRA,T/F,COND NEXT CL

-----

08

10035 -MAIN

```
01 ;*****  
02 ; FORMAT 7: SHIFT, MULTIPLY AND DIVIDE INSTRUCTIONS  
03 ;*****  
04 ;*****  
05 ;-----  
06 ;SHIFT LEFT GRB, CONDITION TEST  
07 ;  
08 ; SLABB ALUFUNC,GRA,GRB,SI,TST,T/F,COND NEXT CL  
09 ; SLZBB ALUFUNC,GRB,SI,TST,T/F,COND NEXT CL  
10 ; SLZAB ALUFUNC,GRA,GRB,SI,TST,T/F,COND NEXT CL  
11 ;-----  
12 ;  
13 ;-----  
14 ;  
15 ;-----  
16 ;SHIFT LEFT GRB CON Q, CONDITION TEST  
17 ;  
18 ; DLABB ALUFUNC,GRA,GRB,SI,TST,T/F,COND NEXT CL  
19 ; DLZBB ALUFUNC,GRB,SI,TST,T/F,COND NEXT CL  
20 ; DLZAB ALUFUNC,GRA,GRB,SI,TST,T/F,COND NEXT CL  
21 ;-----  
22 ;  
23 ;-----
```

10036 .MAIN

```
01 ;-----  
02 ;SHIFT RIGHT GRB, CONDITION TEST  
03 ;  
04 ; SRABB ALUFUNC,GRA,GRB,SI,TST,T/F,COND NEXT CL  
05 ; SRZBB ALUFUNC,GRB,SI,TST,T/F,COND NEXT CL  
06 ; SRZAB ALUFUNC,GRA,GRB,SI,TST,T/F,COND NEXT CL  
07 ;  
08 ;-----  
09 ;  
10 ;  
11 ;-----  
12 ;SHIFT RIGHT GRB CON Q, CONDITION TEST  
13 ;  
14 ; DRABB ALUFUNC,GRA,GRB,SI,TST,T/F,COND NEXT CL  
15 ; DRZBB ALUFUNC,GRB,SI,TST,T/F,COND NEXT CL  
16 ; DRZAB ALUFUNC,GHA,GRB,SI,TST,T/F,COND NEXT CL  
17 ;  
18 ;-----  
19 ;
```

10037 .MAIN

```

;-----
;NO SHIFT, LOAD GRB, CONDITION TEST
;
;   NSAQB ALUFUNC,GRA,GRB,TST,T/F,COND      NEXT CL
;   NSABB ALUFUNC,GRA,GRB,TST,T/F,COND      NEXT CL
;   NSZQB ALUFUNC,GRB,TST,T/F,COND          NEXT CL
;   NSZBB ALUFUNC,GRB,TST,T/F,COND          NEXT CL
;   NSZAB ALUFUNC,GRA,GRB,TST,T/F,COND      NEXT CL
;-----

```

10038 .MAIN

```

;-----
;MULTIPLY
;
;   MULD  ALUFUNC,GRA,GRB,SI,TST,T/F,COND  NEXT CL; MULTIPLY,
;   MULS  ALUFUNC,GRA,GRB,SI,TST,T/F,COND  NEXT CL; DOUBLE SHIFT
;   MULN  ALUFUNC,GRA,GRB,TST,T/F,COND     NEXT CL; SINGLE SHIFT
;   MULQ  ALUFUNC,GRA,TST,T/F,COND         NEXT CL; NO SHIFT
;                                           NEXT CL; MULTIPLY,
;                                           ; WITH Q-REGISTER
;-----

```

10039 .MAIN

```

;-----
;DIVIDE
;
;   DIVD  ALUFUNC,GRA,GRB,SI,TST,T/F,COND  NEXT CL
;   DIVS  ALUFUNC,GRA,GRB,SI,TST,T/F,COND  NEXT CL
;   DIVN  ALUFUNC,GRA,GRB,TST,T/F,COND     NEXT CL
;-----

```

004C .MAIN

0000 SOURCE LINES IN ERROR

U001 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

:FILE: T1, EDITED: 850814/LBJ

00001 -NOMA 1  
00000 -LOC 0

:MICRO INTERRUPT TABLE  
:\*\*\*\*\*

:THE FIRST 7 LOCATIONS CONTAINS A JUMP TABLE FOR  
:INTERRUPT AND ERROR ACTIONS

12	0000	46310000000000000000000000000000	JMP	F,F,POWUP	;0: POWER UP
13	0001	46310000000000000000000000000023	JMP	F,F,INTIN	;1: CPU INTERRUPT
14	0002	06310000000000000000000000001661	JMP	F,F,INT00	;2: LIMIT VIOLATION
15	0003	46310000000000000000000000001415	JMP	F,F,INTBB	;3: I/O ERROR
16	0004	46310000000000000000000000001671	JMP	F,F,INT1	;4: INTEGER MASK & OVERFLOW
17	0005	46310000000000000000000000001671	JMP	F,F,INT1	;5: INTEGER MASK & SHIFT OVERFLOW
18					;6: EXTERNAL INTERRUPT
19	0006	06052300000000000000000000000022	ZOR	OR,ILEVR	; ILEVR=# Q
20					; DELAY 675 NSEC BEFORE ILEV IS READ
21	0007	0715730000000000000000000000004100	XZ	OR,IOSTA	; TERMINATE A POSSIBLE READ
22	0010	4705230000000000000000000000000000	ZQ	OR	;
23	0011	4724730000000000000000000000007760	IZQ	OR,U,377	;
24	0012	061764003200000000000000000000320	XQBR	AND,ILEV,WRKU,INTRG	; WRKU:=INTRG:= ILEV(16:23)
25	0013	463743152500000000000000000000060	SLZAB	OR,WRKU,CAUSE,Z,NL,F,F	; CAUSE:= WKKU SHIFT 1
26	0014	4651523500000000000000000000003600	CA	SUBO,-8,WRKO	; IF LEVEL=#8 THEN
27	0015	46310000000000000000000000000021434	JMP	T,NZ,ICALL	; BEGIN
28	0016	4605431500000000000000000000000017	ZAR	OR,WRKO,RTCR	; <*> INTERRUPT FROM OTHER PU *>
29	0017	07310000000000000000000000000000040	JMP	F,F,DCLOK	; <*> SAVE RTC IN PUTAB+PUINDEX *>

CONT 0 2  
CONT 0 2  
CONT 2

CONT 2

100C2 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
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31
32
33
34

AUXIN:
;IDLE:
;MUST HAVE LOCATION 8'20
CZBR SUB,.32,CNTR,CNTR0
CALL T,EXINT,CHCLK
JMP F,CPUIN,.-3

;INTIN:
F,NTCPI,TCPIN
F,NAUXI,AUXIN
F,NCAME,CAMER
F,NOCPA,OCPAL
F,NREMA,REMAL
F,F,SINGL
F,F,.SCAP
F,F,.NTU
F,F,.NIB1
F,F,.YFL1
F,F,.NTF
F,F,.FINT
F,F,LOKM

OR,RTC,DATO
OR,PUTAB
ADD,PUIX
ADD,.10,LAD
OR,CIOST
AND,.4,CNTR
T,NZ,DISPA
OR,ICS,LAD
OR,CIOST

XZR
SZQ
SQQ
COR
XZ
CA
JMP
RZR
XZ

DCLOCK:
00020 065372003100C1706
00021 4631000002612000
00022 46310000002C0021
00023 0631000000224001
00024 0631000000230020
00025 0631000000244020
00026 4631000000254002
00027 0631000000264003
00030 06310000000C4004
00031 0631000000001632
00032 4631000000001665
00033 0631000000001424
00034 0631000000001771
00035 0631000000001667
00036 4631000000001401
00037 4631000000001533
00040 0615730000000212
00041 0600730000001100
00042 4640600000001000
00043 06516000000231311
00044 06557300000510100
00045 0611541.440001500
00046 06310000000422001
00047 46057300000241011
00050 35557300000510100

;IDLE:
; CNTR:=CNTR0:= 8'37
; IF EXTERNAL INTERRUPT THEN
; CHECK CLOCK INTERRUPT;
; WAIT FOR CPU INTERRUPT

;CPU INTERRUPTS: C. LOC 23
; TCP INPUT
; AUX INTERRUPT
; CAM ERROR
; OCP AUTOLoad
; REMOTE AUTOLoad
; SINGLE INSTRUCTION INTERRUPT
; GOTO ESCAPE C. LOC 8'31
; GOTO INTU
; GOTO INIB1
; GOTO SYFL1
; GOTO INTF
; GOTO PFINF
; GOTO LOCKMONITOR

; <* DELAY 20C NSEC *>
;
;
; LAD:= PUTAB+PUIX+10, WRT;
; TESTIO;
; IF CNTR(21)<>0 THEN <* NOT RUN*>
; GOTO DISPA;
; LAD:= ICS, READ INSTRUCTION;
; TESTIO, NEXT INSTRUCTION

```

10UC3 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```
01  
02 ;INSTRUCTION EXECUTION PROCEDURES  
03 ;*****  
04 ;ADDRESS HANDLING  
05 ;*****  
06 ;MODIFY NEXT ADDRESS, AM, OPCODE = 11  
07  
08  
09  
10 AME: IZQ OR,0,1101 ;AME:  
11 CC051 46247300000022020 ; ;  
12 CC052 06310000002000031 CALL F,F,ESCAP ; CALL ESCAPE(1101)  
13 AM: CABR OR,.1B2,STAT,CPUST ;AM:  
14 CC053 4613531224C03205 ; AFTER AM: 1  
15 CC054 3445230000600000 ZG OR ;TEST INT, NEXT INSTRUCTION
```

100C4 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 ;LOAD ADDRESS, AL, OPCODE = 13
03
04 ALE:
05 CC055 062473000026020 IZQ OR,U,1301
06 CC056 063100000200031 CALL F,F,ESCAP
07
08 AL:
09 CC057 74074313006C0000 ZAB OR,SB,W NEXT TSTIN
10
11 ;LOAD ADDRESS COMPLEMENTED, AC, OPCODE = 41
12
13
14 ACE:
15 CC060 46247300001C2020 IZQ OR,U,4101
16 CC061 063100000200031 CALL F,F,ESCAP
17 AC:
18 CC062 06074233000C0000 ZAB SUBO,SB,W NEXT TOVIN
19 CC063 7406731224644000 EZB OR,STOC,STAT,STAT
20
21 ;ALE:
;
; CALL ESCAPE(1301)
;
;AL:
; W:=SB, TEST INTERRUPT
; NEXT INSTRUCTION
;
;ACE:
;
; CALL ESCAPE(4101)
;AC:
; W:= -SB
; EX(22:23):=OVERFLOW,CARRY
; TEST OVERFLOW, TEST INT
; NEXT INSTRUCTION

```



IUGG5 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

WORD INSTRUCTIONS WITH MEMORY REFERENCE  
 \*\*\*\*\*

LOAD REGISTER, RL, OPCODE = 24

```

01
02
03
04
05
06
07
08 CC064 4624730000050040
09 CC065 0631000002CC0031
10
11 CC066 0631000000130070
12 CC067 7457730000714000
13
14 CC070 3457430140600000
15
16
17
18
19
20
21
22
23
24
25
26
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29
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31
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37
38
39
  
```

LOGICAL AND, LA, OPCODE = 4

```

01
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27
28
29
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31
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33
34
35
36
37
38
39
  
```

LOGICAL OR, LO, OPCODE = 5

```

01
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16
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35
36
37
38
39
  
```

100C6 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 ;LOGICAL EXCLUSIVE OR, LX, OPCODE = 6
02
03
04 ;LXE:
05 CC103 4624730000014040 IZQ OR,0,0602
06 CC104 0631000002000031 CALL F,F,ESCAP
07 ;LX:
08 CC105 4631000000130107 JMP F,NWADR,LXREG
09 CC106 3457560000714000 XAB EXOR,DATI,W,W
10 ;
11 CC107 3457160140600000 LXREG: XABB CDATI,EXOR,LAX,W
12 ;
13 ;ADD INTEGER WORD, WA, OPCODE = 7
14
15 ;WAE:
16 CC110 0624730000016040 IZQ OR,0,702
17 CC111 0631000002000031 CALL F,F,ESCAP
18 ;WA:
19 CC112 4631000000130115 JMP F,NWADR,WAREG
20 CC113 0717500000514000 XAB ADD,DATI,W,W
21 CC114 7406731224644000 EZB OR,STOC,STAT,STAT
22 ;
23 ;SUBTRACT INTEGER WORD, WS, OPCODE = 10
24
25 CC115 4657100140000000 WAREG: XABB CDATI,ADD,LAX,W
26 CC116 7406731224644000 EZB OR,STOC,STAT,STAT
27 ;
28 ;
29 ;WSE:
30 CC117 0624730000020040 IZQ OR,0,1002
31 CC120 0631000002000031 CALL F,F,ESCAP
32 ;WS:
33 CC121 0631000000130124 JMP F,NWADR,WSREG
34 CC122 0717512000514000 XAB SUNO,DATI,W,W
35 CC123 7406731224644000 EZB OR,STOC,STAT,STAT
36 ;
37 ;
38 ;
39 ;
40 ;

```

```

;LXE:
;
; CALL ESCAPE(0602)
;LX:
; IF W-ADDR THEN GOTO LX-REG
; W:=W EXOR DATAIN, TEST INT, TEST IO
; GOTO NEXT INSTRUCTION
; W:=W EXOR LAX, TEST INT
; GOTO NEXT INSTRUCTION

```

```

;WAE:
;
; CALL ESCAPE(0702)
;WA:
; IF W-ADDR THEN GOTO WA-REG
; W:=W + DATAIN, TEST IO
; STATUS:=STATUS(0:21) CON OVERFLOW,CARRY
; TEST OVERFLOW, TEST INT
; GOTO NEXT INSTRUCTION
; W:=W + LAX
; STATUS:=STATUS(0:21) CON OVERFLOW, CARRY
; TEST OVERFLOW, TEST INT
; GOTO NEXT INSTRUCTION

```

```

;WSE:
;
; CALL ESCAPE(1002)
;WS:
; IF W-ADDR THEN GOTO WS-REG
; W:=W - DATAIN, TEST IO
; STATUS:= STATUS(0:21) CON OVERFLOW,CARRY
; TEST OVERFLOW, TEST INT
; GOTO NEXT INSTRUCTION

```

```

NEXT TIOIN 1
NEXT TSTIN 1

```

```

CONT TSTIO 2
NEXT IOVIN

CONT U 1
NEXT TOVIN

```

```

CONT TSTIO 2
NEXT TOVIN

```

10007 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 00124 4657112140000000 WSREG: XABB CDATI,SUNO,LAX,W          CONT 0 1
03 00125 7406731224644000 EZB  OR,STOC,STAT,STAT          NEXT T0VIN
04
05
06
07
08
09
10 00126 46247300000024040 WME:
11 00127 06310000020000031 WM:
12
13 00130 0631000000130141
14 00131 0657730032514000
15 00132 4604430000000000 WM1:
16 00133 46343400010000040
17
18 00134 06517120000006004
19 00135 4274101501113240
20 00136 2634101501113240
21
22
23
24
25
26 00137 4674113501003260
27
28
29 00140 34072300000600000 WMREG:
30
31
32 00141 06574301720000000
33 00142 06310000000000132
34
35

```

```

; W:=W - LAX
; STATUS:=STATUS(0:21) CON OVERFLOW,CARRY
; TEST OVERFLOW, TEST INT
; GOTO NEXT INSTRUCTION

```

```

;MULTIPLY INTEGER WORD, WM, OPCODE = 12

```

```

;WME:
;
; CALL ESCAPE(1202)
;WM:
; IF W-ADDR THEN GOTO WM-REG
; WRKU:= DATAIN , TEST IO
; Q:= W
; ADC:= IF Q(23)=1 THEN 0 ELSE 1
; WPRE:= 0
; FOR LCD:= -22 STEP 1 UNTIL 0 DO
; BEGIN
; WPRE:= IF ADC=0 THEN WPRE+WRKO
; ELSE
; WPRE:=IF Q(23)=1 THEN 0 ELSE 1
; WPRE,Q:= WPRE(0) CON WPRE,Q SHIFT (-1)
; END
; WPRE:=IF ADC=0 THEN WPRE-WRKO
; ELSE WPRE=0
; WPRE,Q:= WPRE(U) CON WPRE,Q SHIFT (-1)
; W:= Q , TEST INT
; GOTO NEXT INSTR

```

```

;WM-REG:
; WRKU:= LAX
; GOTO WM1

```

IGCC8 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 ;MULTIPLY INTEGER WORD, WM, WHEN THE FPU8U1 IS PRESENT
02
03
04 WMFPE:
05 IZQ OR,0,1202
06 CALL F,F,ESCAP
07
08 WMFPU:
09 ZGR OR,FPOP1
10 ZAR OR,W,FPOPU
11 JMP F,NWADR,WMFPR
12 XZB OR,DATAI,WRKO
13 ZAR OR,WRKU,FPOPU
14 XZ OR,DATAI
15 XZ OR,FPURU
16 XZB OR,FPURU,WPRE
17 XZB OR,FPUR1,W
18
19 WMFPR:
20 XXZQ CDATI,OR
21 ZAR OR,LAX,FPOPU
22 XZ OR,FPURU
23 XZB OR,FPURU,WPRE
24 XZB OR,FPUR1,W
25
26 ; DIVIDE INTEGER WORD, WD, OPCODE = 30
27
28 WDE:
29 IZQ OR,U,3002
30 CALL F,F,ESCAP
31
32 WD:
33 ZAQ OR,W
34 ZAB OR,WPRE,WRK4
35 JMP F,NWADR,WDREG
36 XZB OR,DATAI,WRKO
37
38 ;
39 ; CALL ESCAPE(1202)
40 ;
41 ;
42 ; WMFPE:
43 ;
44 ; CALL ESCAPE(1202)
45 ;
46 ; WMFPU:
47 ; FPUOP1:= Q , WMOP
48 ; FPUOPU:= W , START FPU
49 ; IF W-ADDR THEN GOTO WMFPU-REG
50 ; WRKO:= DATAIN
51 ; FPUOPU:= WRKO , START FPU
52 ; TEST IO
53 ; WAIT FOR FPURO TO BE BUSY
54 ; WPRE:= FPURO
55 ; W:= FPUR1 , TEST INT
56 ; GOTO NEXT INSTR
57 ; WMFPU-REG:
58 ; NO-OP
59 ; FPUOPU:= LAX , START FPU
60 ; WAIT FOR FPURO TO BE BUSY
61 ; WPRE:= FPURO
62 ; W:= FPUR1 , TEST INT
63 ; GOTO NEXT INSTR
64 ;
65 ; WDE:
66 ;
67 ; CALL ESCAPE(3002)
68 ;
69 ; WD:
70 ; Q:= W
71 ; WRK4:= WPRE
72 ; IF W-ADDR THEN GOTO WD-REG
73 ; WRKU:= DATAIN , TESTIO
74 ;
75
76
77
78
79
80
81
82
83
84
85
86
87
88
89
90
91
92
93
94
95
96
97
98
99

```

```

CONT WMOP
CONT 0 2
CONT TSTIO
CONT 0 1
NEXT TSTIN 1
CONT 0 1
NEXT TSTIN 1
CONT 0 1
NEXT TSTIN 1
CONT 0 1
NEXT TSTIN 1
CONT TSTIO 1

```

10009 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 00171 0637330032004000 WD1: NSZBB OR,WRK0,DVS,F,F
03 00172 463100000020220 JMP F,NZ,WDOFL
04 00173 4636330001000020 DLZBB OR,WPRE,Z,DC,F,F
05
06 00174 0676107501002120 DIVD ADDX,WRK0,WPRE,ADC,DC,F,F
07
08
09
10 00175 0631000000500220 JMP T,DIVOF,WDOFL
11 00176 0651511400000604 CAR SUN,-21,-2,LCD
12 00177 4276107501112120 DIVD ADDX,WRK0,WPRE,ADC,DC,F,LCN PUSH
13 00200 2636107501112120 DIVD ADDX,WRK0,WPRE,ADC,DC,F,LCN LRTN 0
14
15
16
17
18
19 00201 0636330035002060 DLZBB OR,WRK3,ADC,NL,F,F
20 00202 4677107501004120 DIVN ADDX,WRK0,WPRE,DC,F,F
21
22
23 00203 0636330035002060 DLZBB OR,WRK3,ADC,NL,F,F
24 00204 0677107501004260 MULN ADD,WRK0,WPRE,NL,F,F
25
26 00205 0605122072000000 AB SUBO,WPRE,WRK0
27 00206 463100000020213 JMP F,NZ,WDCOR
28 00207 4605430040000000 ZA OR,WPRE
29 00210 463100000020216 JMP F,NZ,WDEND
30 00211 4605161701000000 AB EXOR,WRK4,WPRE
31 00212 463100000010216 JMP F,NEG,WDEND
32
33 00213 4607113501000000 WDCOR: SUNO,WRK0,WPRE
34 00214 0604202000000000 ADDO
35 00215 4631000000430220 JMP T,OVFL,WDOFL
36
37 00216 4607230000000000 WDEND:
38 00217 7406751224604200 OR,W
39

```

```

; DVS:= WRKU(0) , DIVISOR SIGN
; IF WRKU=U THEN GOTO WD-OVERFLOW
; ADC:=IF WPRE(U)=DVS THEN 1 ELSE 0
; WPRE,Q:= WPRE,Q SHIFT 1
; WPRE:=IF ADC=1 THEN WPRE-WRKO
; ; ELSE WPRE+WRKO
; ;
; ADC:=IF WPRE(0)=DVS THEN 1 ELSE 0
; WPRE,Q:= WPRE,Q SHIFT 1
; IF CARRY<>LNK THEN GOTO WD-OVERFLOW
; FOR LCD:=-21 STEP 1 UNTIL 0 DO
; ;
; BEGIN
; WPRE:=IF ADC=1 THEN WPRE-WRKO
; ; ELSE WPRE+WRKO
; ;
; ADC:=IF WPRE(0)=DVS THEN 1 ELSE 0
; WPRE,Q:= WPRE,Q SHIFT 1
; ;
; END
; Q:= Q(1:25) CON ADC
; WPRE:=ID ADC=1 THEN WPRE-WRKO
; ; ELSE WPRE+WRKO
; ;
; ADC:=IF WPRE(0)=DVS THEN 1 ELSE 0
; Q:= Q(1:25) CON ADC
; WPRE:=IF ADC=0 THEN WPRE+WRKO
; ; ELSE WPRE
; ;
; IF REMAINDER=DIVISOR
; ; GOTO WDCOR
; ;
; IF REMAINDER=0 THEN
; ; GOTO WDEND
; ;
; IF SIGN(DIVIDEND)=SIGN(REMAINDER) THEN
; ; GOTO WDEND
; ;
; WDCOR:
; ;
; REMAINDER:=REMAINDER-DIVISOR
; ;
; QUOTIENT:= QUOTIENT + 1
; ;
; IF OVERFLOW THEN GOTO WD-OVERFLOW
; ;
; WDEND:
; ;
; W:= Q
; ;
; EX:= 0 , TEST INT
; ;
; GOTO NEXT INSTR
; ;

```

1001C CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 C0220 0607431701000000      WDOFL:      ZAB      OR,WRK4,WPRE      ;WD-OVERFLOW:
04 C0221 0664713000000000      IZQ      SUN0,4000,0      ; WPRE:= WRK4 C. RESTORE DIVIDEND
05 C0222 7406731224644000      EZB      OR,STOC,STAT,STAT      ; SET OVERFLOW
                                NEXT TOVIN      ; EX(22:23):= 1,U , TEST OVERFLOW
06
07
08 C0223 0657430172000000      WDOFL:      XZAB     CDATI,OR,LAX,WRKU      ;WD-REG:
09 C0224 4631000000000171      JMP      F,F,WD1      ; WRKU:= LAX
10

```

10011 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

;HALF-WORD INSTRUCTIONS WITH MEMORY REFERENCE  
 ;\*\*\*\*\*

;LOAD HALF REGISTER, HL, OPCODE = 3

```

07 HLE:      IZQ   OR,0,0302
08          CALL  F,F,ESCAP
09          HL:
10          CAQ   CAND,812,W
11          JMP   F,NWADR,HLREG
12          XZB   OR,DATI,WRKO
13          HGB   OR,HLOAD,WRKO,W
14          ZG    OR
15          CC233 7405230000600000
16
17
18
19 HLREG:    HGB   OR,HLOAD,LAX,W
20          XXZQ  CDAI,OR
21          CC234 0662630140000000
22          CC235 7455230000600000
23
24
25
26
27
28
29
30
31
32
33
34
35
36
37
38
39
  
```

```

;HLE:
; CALL ESCAPE(0302)
;HL:
; Q:= W(U:11) CON 12 EXT 0
; IF W-ADDR THEN GOTO HL-REG
; WRKO:=DATAIN, TEST IO
; W:= IF ODD THEN
; Q OR 12 EXT 0 CON WRKO(12:23)
; ELSE
; Q OR 12 EXT 0 CON WRKO(0:11)
; TEST INT, GOTO NEXT INSTR.
;HLREG:
; W:= IF ODD THEN
; Q OR 12 EXT 0 CON LAX(12:23)
; ELSE
; Q OR 12 EXT 0 CON LAX(U:11)
  
```

;LOAD ZERO EXTENDED HALF-WORD, ZL, OPCODE = 23

```

27 ZLE:      IZQ   OR,0,2302
28          CALL  F,F,ESCAP
29          ZL:
30          JMP   F,NWADR,ZLREG
31          XZB   OR,DATI,WRKO
32          HZB   OR,HLOAD,WRKO,W
33          ZG    OR
34          CC236 0624730000046040
35          CC237 0631000002000031
36          CC240 0631000000130244
37          CC241 0657730032514000
38          CC242 0662731500000000
39          CC243 3445230000600000
40
41
42
43
44
45
46
47
48
49
50
51
52
53
54
55
56
57
58
59
  
```

```

;ZLE:
; CALL ESCAPE(2302)
;ZL:
; IF W-ADDR THEN GOTO ZL-REG
; WRKO:=DATAIN, TEST IO
; W:= IF ODD THEN 12 EXT 0 CON WRKO(12:23)
; ELSE 12 EXT 0 CON WRKO(0:11)
; TEST INT, GOTO NEXT INSTR
;ZLREG:
; W:= IF ODD THEN 12 EXT 0 CON LAX(12:23)
; ELSE 12 EXT 0 CON LAX(0:11)
; TEST INT, GOTO NEXT INSTR
  
```

10012 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

;LOAD EXTENDED HALF-WORD, EL, OPCODE = 2

```

01
02
03
04
05 00246 062473000000004040
06 00247 063100000020000031
07
08 00250 46310000000130254
09 00251 0657730032514000
10 00252 4662731500000100
11 00253 740523000000600000
12
13
14
15
16 00254 0662730140000100
17 00255 745523000000600000
18
19
20
21
22
23
24
25 00256 46247300000044040
26 00257 063100000020000031
27
28 00260 460443000000000000
29 00261 06310000000130265
30 00262 0657730032514000
31 00263 07226015000000100
32
33
34
35 00264 7406731224644000
36
;ELE:
; CALL ESCAPE(0202)
;EL:
; IF W=ADDR THEN GOTO EL-REG
; WRKO:=DATAIN, TEST IO
; W:= IF ODD THEN
; 12 EXT WRKO(12) CON WRKO(12:23)
; ELSE
; 12 EXT WRKO(0) CON WRKO(0:11)
; TEST INT, GOTO NEXT INSTR
;ELREG:
; W:= IF ODD THEN
; 12 EXT LAX(12) CON LAX(12:23)
; ELSE
; 12 EXT LAX(0) CON LAX(0:11)
; TEST INT, GOTO NEXT INSTR
;EAE:
; CALL ESCAPE(2202)
;EA:
; Q:=W
; IF W=ADDR THEN GOTO EA-REG
; WRKO:=DATAIN, TEST IO
; W:= IF ODD THEN
; Q+ 12 EXT WRKO(12) CON WRKO(12:23)
; ELSE
; Q+ 12 EXT WRKO(0) CON WRKO(0:11)
; STAT:=STAT(U:21) +OFL+CARRY
; TEST OFL, TEST INT, GOTO NEXT INSTR

```

;ADD INTEGER HALF-WORD, EA, OPCODE = 22

```

EAE:
IZQ OR,0,2202
CALL F,F,ESCAP
EA:
ZAQ OR,W
JMP F,NWADR,EAREG
XZB OR,DATI,WRKO
HGB ADD,EXLOA,WRKO,W
EVB OR,STOC,STAT,STAT
NEXT TOVIN

```



10013 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 CC265 4722600140CC0100
04 CC266 460673122444000
05
06
07 CC267 3455730000600100
08
09
10
11
12
13 CC270 462473000042040
14 CC271 0631000002000031
15
16 CC272 4604430000000000
17 CC273 0631000000130277
18 CC274 0657730032514000
19 CC275 0722613500000100
20
21
22
23 CC276 7406731224644000
24
25
26 CC277 4722612140CC0100
27 CC300 460673122444000
28
29 CC301 3455730000600100
30
31

```

```

;EAREG:
; W:= IF ODD THEN
; Q + 12 EXT LAX(12) CON LAX(12:23)
; ELSE
; Q + 12 EXT LAX(U) CON LAX(U:11)
; STAT:= STAT(U:21)+ OFL+CARRY
; TEST OFL, TEST INT, GOTO NEXT INSTR
CONT TSTOV
NEXT TSTIN 1

;SUBTRACT INTEGER HALF-WORD, ES, OPCODE = 21
;ESE:
; CALL ESCAPE(2102)
;ES:
; Q:=W
; IF W=ADDR THEN GOTO ES-REG
; WRKU:=DATAIN, TEST IO
; W:= IF ODD THEN
; Q - 12 EXT WRKU(12) CON WRKU(12:23)
; ELSE
; Q - 12 EXT WRKU(0) CON WRKU(0:11)
; STAT:=STAT(U:21) + OFL + CARRY
; TEST OFL, TEST INT, GOTO NEXT INSTR
;ESREG:
; W:= IF ODD THEN
; Q - 12 EXT LAX(12) CON LAX(12:23)
; ELSE
; Q - 12 EXT LAX(U) CON LAX(U:11)
; STAT:= STAT(U:21) + OFL + CARRY
; TEST OFL, TEST INT, GOTO NEXT INSTR

```

10014 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03
04
05 C0302 0624730000040040
06 C0305 063100000200031
07
08 C0304 4613551224003400
09 C0305 463100000130312
10 C0306 0657730032514000
11 C0307 4662751532000000
12
13
14
15 C0310 0613541532003400
16 C0311 7407131524600000
17
18
19 C0312 0657430172000000
20 C0313 4631000000000307

;LOAD EXCEPTION REGISTER, XL, OPCODE = 20

XLE:
CALL OR,0,2002
F,F,ESCAP
XL:
CAND,.7,STAT,STAT
F,NWADR,XLREG
OR,DATI,WRKU
OR,HLOAD,WRKU,WRKU
CAB
JMP
XZB
HZB
I ZQ
CALL
CAB
ABB
XZAB
JMP
; CALL ESCAPE(2002)
; XL:
; STAT:= STAT(0:21) CON 3EXTC
; IF W-ADDR THEN GOTO XL-REG
; WRKU:= DATAIN, TEST IO
; WRKU:= IF ODD THEN
; 12 EXT U CON WRKU(12:23)
; ELSE
; 12 EXT U CON WRKU(U:11)
; WRKU:= 21 EXT 0 CON WRKU(21:23)
; STAT:= STAT OR WRKU
; TEST INT, GOTO NEXT INSTR
; XLREG:
; WRKU:=LAX
; GOTO XL1

CONT TSTIO 1
NEXT TSTIN
CONT U 1

```

10015 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 ;STORE INSTRUCTIONS
02 ;*****
03 ;*****
04 ;*****
05 ;STORE REGISTER, RS, OPCODE = 27
06
07 RSE:
08 CC314 4624730000056100 IZQ OR,U,2704
09 CC315 0631000002000031 CALL F,F,ESCAP
10
11 RS:
12 CC316 0605430000000012 ZAR OR,W,DATO
13 CC317 4613551332223711 CABR CAND,.1,SB,WRKU,LAD
14 CC320 4631000000130324 JMP F,NWADR,RSREG
15 CC321 0645523500000100 RA SUBO,ICS,WRKU
16 CC322 0631000000020325 JMP F,NZ,LOAIR
17 CC323 3455730000710100 XZ OR,CIOST
18
19 RSREG:
20 CC324 3457430003600000 XZAB CDATI,OR,W,LAX
21
22 LOAIR:
23 CC325 06557300000510100 XZ OR,CIOST
24 CC326 4605730000241011 RZR OR,ICS,LAD
25 CC327 75452300000600000 ZQ OR
26
;RSE:
;
; CALL ESCAPE(2704)
;RS:
; DATAOUT:=W
; LAD:=WRKU:=SB(O:22) CON 1 EXT U, WRTP
; IF W-ADDR THEN GOTO RS-REG
;
; IF WRKU=IC THEN GOTO LOAD-IR
; TEST IO, TEST INT
; GOTO NEXT INSTR
;RSREG:
; LAX:=W, TEST INT
; GOTO NEXT INSTR
;LOAD-IR:
; TEST IO
; LAD:=ICS, READ INSTRUCTION
; TEST INT
; GOTO NEXT INSTR

```

10016 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

;STORE DCUBLE REGISTER, DS, OPCODE = 67

```

01
02
03
04
05 CC330 46247300000156300
06 CC331 0631000002000031
07
08 CC332 0605430000000012
09 CC333 4613551332223711
10 CC334 4631000000130353
11 CC335 0645525500001000
12 CC336 4631000000020347
13 CC337 0655730000510100
14 CC340 4605430040000012
15 CC341 4647113426220011
16 CC342 0631000000130032
17 CC343 4613551332003700
18 CC344 0645523500001000
19 CC345 0631000000020325
20 CC346 34557300000710100
21
22
23 CC347 06557300000510100
24 CC350 4605430040000012
25 CC351 4647113426220011
26 CC352 46310000000000325
27
28
29 CC353 46574300003000000
30 CC354 4607113426000001
31 CC355 7407430043600000
32
;DSE:
; CALL ESCAPE(6714)
;DS:
; DATAOUT:= W
; LAD:=WRKU:=SB(0:22) CON 1 EXT 0, WRTP
; IF W-ADDR THEN GOTO DS-REG
; IF WRKU=ICS THEN GOTO DS-LOADIR
;
; TEST IO
; DATAOUT:=WPRES
; LAD:=SB:=SB-2, WRTP
; IF W-ADDR THEN GOTO INTU
; WRKU:= SB(U:22) CON 1 EXT 0
; IF WRKU=ICS THEN GOTO LOAD-IR
;
; TEST IO, TEST INT
; GOTO NEXT INSTRUCTION
;
; TEST IO
; DATAOUT:= WPRES
; SB:=LAD:= SB-2 , WRTP
; GOTO LOAD-IR
;
;DSREG:
; LAX:=W
; LAST:=SB-2
; LAX:=WPRES, TEST INT
; GOTO NEXT INSTR

```

10017 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

;STORE HALF-REGISTER, HS, OPCODE = 32

```

01
02
03
04
05 CC356 062473000064100
06 CC357 0631000002000031
07
08 CC360 0662730033000200
09
10
11
12 CC361 4631000000130371
13 CC362 4654750000514000
14 CC363 4662641532000300
15
16
17
18 CC364 4605131533000012
19 CC365 4613551332223711
20 CC366 0645523500001000
21 CC367 063100000020325
22 CC370 3455730000710100
23
24
25 CC371 4604430140000000
26 CC372 4662640143000300
27
28
29
30 CC373 3457131543600000
31
;HSE:
; CALL ESCAPE(3204)
;HS:
; WRK1:= IF ODD THEN
; 12 EXT 0 CON W(12:23)
; ELSE
; W(12:23) CON 12 EXT 0
; IF W-ADDR THEN GOTO HS-REG
; Q:=DATAIN, TEST IO
; WRKU:= IF ODD THEN
; Q(U:11) CON 12 EXT 0
; ELSE
; 12 EXT 0 CON Q(12:23)
; DATAOUT:=WRKU OR WRK1
; LAD:=WRKU:=SB(O:22) CON 1 EXT 0, WRTP
; IF IC=WRKU THEN
; GOTO LOAD-IR
; TEST IO, TEST INT
; GOTO NEXT INSTR
;HSREG:
; Q:= LAX
; LAX:= IF ODD THEN
; Q(U:11) CON 12 EXT 0
; ELSE
; 12 EXT 0 CON Q(12:23)
; LAX:=LAX OR WRK1, TEST INT
; GOTO NEXT INSTR

```

CONT TSTIO 1

CONT WRTP

NEXT TIOIN 1

NEXT TSTIN 1

10018 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

;STORE EXCEPTION REGISTER, XS, OPCODE = 33

```

01
02
03
04
05 CC374 4624730000066100
06 CC375 0631000002000031
07
08 CC376 0613541233003400
09 CC377 0662731573000200
10
11
12
13 CC400 4631000000130371
14 CC401 46547300000514000
15 CC402 4662641532000300
16
17
18
19 CC403 0607131533000012
20 CC404 4613551332223711
21 CC405 0645523500001000
22 CC406 0631000000020325
23 CC407 34557300000710100
24
25
26
27
28
29
30 CC410 4624730000062140
31 CC411 0631000002000031
32 CC412 0631000000130424
33 CC413 0605430000000012
34 CC414 4657730033514000
35 CC415 4613551332223711
36 CC416 0645523500001000

;XSE:
;
; CALL ESCAPE(3304)
;XS:
; WRK1:= 21 EXT 0 CON STAT(21:23)
; WRK1:= IF ODD THEN
; 12 EXT 0 CON WRK1(12:23)
; ELSE
; WRK1(12:23) CON 12 EXT 0
; IF W-ADDR THEN GOTO HS-REG
; Q:=DATAIN, TEST IO
; WRKU:= IF ODD THEN
; Q(0:11) CON 12 EXT 0
; ELSE
; 12 EXT 0 CON Q(12:23)
; DATAOUT:= WRK1 OR WRKU
; LAD:=WRKU:=SB(0:22) CON 1 EXT 0, WRTP
; IF ICS=WRKU THEN
; GOTO LOAD-IR
; TEST IO, TEST INT
; GOTO NEXT INSTR

CONT TSTIO 1
CONT WRTP
NEXT TI0IN 1

```

;EXCHANGE REGISTER AND MEMORY WORD, RX, OPCODE = 31

```

;RXE:
;
; CALL ESCAPE(3106)
;RX:
; IF W-ADDR THEN GOTO RX-REG
; DATAOUT:=W
; WRK1:=DATAIN, TEST IO
; LAD:=WRKU:=SB(0:22) CON 1 EXT 0, WRTP
;

IZQ OR,0,3106
CALL F,F,ESCAP
JMP F,NWADR,RXREG
ZAR OR,W,DATO
XZB OR,DATI,WRK1
CABR CAND,.1,SB,WRKO,LAD
RA SUBO,ICS,WRKU

CONT TSTIO 1
CONT WRTP

```

10015 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 CC417 0631000000C20421      JMP      F,NZ,RXLOA
03
04 C0420 7457431540710100     XZAB    CIOST,OR,WRK1,W
05
06
07 CC421 4657431540510100     RXLOA:  XZAB    CIOST,OR,WRK1,W
08 CC422 4605730000241011     RZR     OR,ICS,LAD
09 C0423 7545230000600000     ZQ      OR
10
11
12 C0424 0657430172000000     RXREG:  XZAB    CDATI,OR,LAX,WRKO
13 C0425 4607430003000000     ZAB     OR,W,LAX
14 C0426 7407431500600000     ZAB     OR,WRKU,W
15
16
17
18 ;*****
19 ;*****
20 ;*****
21
22
23
24
25
26
27
28
29
30
31
32
33
34
35
36

```

;DOUBLEWORD INSTRUCTIONS WITH MEMORY REFERENCE  
;\*\*\*\*\*

;LOAD DOUBLE REGISTER, DL, OPCODE = 66

```

22
23 C0427 0624730000154240     DLE:    IZQ     OR,O,6612
24 C0430 0631000002000031     CALL    F,F,ESCAP
25
26 CC431 0631000000130436     DL:     JMP     F,NWADR,DLREG
27 CC432 4657730000514000     XZB     OR,DATI,W
28 CC433 064711342620011     ABBR    SUNO,.2,SB,LAD
29 CC434 0631000000130032     JMP     F,NWADR,INTU
30 C0435 3457730001714000     XZB     OR,DATI,WPRE
31
32
33 C0436 4657430140000000     DLREG:  XZAB    CDATI,OR,LAX,W
34 C0437 4607113426000001     ABBR    SUNO,.2,SB,LAST
35 C0440 7407430141600000     ZAB     OR,LAX,WPRE
36

```

```

; IF IC<>WRKU THEN
; BEGIN
; W:= WRK1, TEST IO, TEST INT
; GOTO NEXT INSTR
; END
; W:= WRK1, TEST IO
; LAD:= ICS, READ INSTRUCTION
; TEST INT
; GOTO NEXT INSTRUCTION
;RXREG:
; WRKU:=LAX
; LAX:=W
; W:=WRKU, TEST INT
; GOTO NEXT INSTR

```

```

;DLE:
; CALL ESCAPE(6612)
;DL:
; IF W-ADDR THEN GOTO DL-REG
; W:=DATAIN, TEST IO
; LAD:=SB:=SB-2, READP
; IF W-ADDR THEN GOTO INTU
; WPRE:=DATAIN, TEST IO,TEST INT
; GOTO NEXT INSTR
;DLREG:
; W:=LAX
; SB:=LAST:=SB-2
; WPKE:=LAX, TEST INT
; GOTO NEXT INSTR

```

10020 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03
04
05 CC441 4624730000160240
06 CC442 0631000002000031
07
08 CC443 0631000000130453
09 CC444 4657730033514000
10 CC445 0647113426200011
11 CC446 0631000000130032
12 CC447 0657730032514000
13 CC450 4607101540000000
14 CC451 0607105501000000
15 CC452 7406731224644000
16
17
18
19 CC453 4657430173000000
20 CC454 4607113426000001
21 CC455 4607101540000000
22 CC456 4607104141000000
23 CC457 7406731224644000
24
25

```

;ADD INTEGER DOUBLE WORD, AA, OPCODE = 70

A AE:

IZQ OR,0,7012  
CALL F,F,ESCAP

AA:

JMP F,NWADR,AAREG  
XZB OR,DATI,WRK1  
ABBR SUNO,.2,SB,LAD  
JMP F,NWADR,INTU  
XZB OR,DATI,WRKU  
ABB ADD,WRK1,W  
ABB ADDC,WRKO,WPRE  
EZB OR,STOC,STAT,STAT

CONT TSTIO 1  
CONT READP  
CONT TSTIO 1  
NEXT TOVIN

;AAE:

; CALL ESCAPE(7012)  
;AA:  
; IF W=ADDR THEN GOTO AA=REG  
; WRK1:=DATAIN, TEST IO  
; SB:=LAD:=SB-2, READP  
; IF W=ADDR THEN GOTO INTO  
; WRKU:=DATAIN, TEST IO  
; W:=W + WRK1  
; WPRE:=WPRE + WRKO + CARRY  
; EX(22:23):=OVERFLOW CON CARRY  
; TEST OVERFLOW, TEST INT  
; GOTO NEXT INSTR

A AREG:

XZAB CDATI,OR,LAX,WRK1  
ABBR SUNO,.2,SB,LAST  
AEB ADD,WRK1,W  
AEB ADDC,LAX,WPRE  
EZB OR,STOC,STAT,STAT

CONT 0 1  
NEXT TOVIN

;AAREG:

; WRK1:=LAX  
; LAST:=SB:=SB-2  
; W:=W + WRK1  
; WPRE:=WPRE + LAX + CARRY  
; EX(22:23):= OVERFLOW CON CARRY  
; TEST OVERFLOW, TEST INT  
; GOTO NEXT INSTR



10021 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```
01 ;SUBTRACT INTEGER DOUBLE WORD, SS, OPCODE = 71
02
03
04
05 0C460 0624730000162240      IZQ  OR,U,7112
06 0C461 0631000020C0031      CALL F,F,ESCAP
07
08 0C462 0631000000130472      JMP  F,NWADR,SSREG
09 0C463 4657730033514000      XZB  OR,DATI,WRK1
10 0C464 06471134262C0011      ABBR SUNO,=2,SB,LAD
11 0C465 0631000000130032      JMP  F,NWADR,INTU
12 0C466 0657730032514000      XZB  OR,DATI,WRKO
13 0C467 4607113540CC0000      ABB  SUNO,WRK1,W
14 0C470 46071155010C0000      ABB  SUNC,WRKO,WPRE
15 0C471 7406731224644000      EZB  OR,STOC,STAT,STAT
16
17
18
19 0C472 4657430173CC0000      XZAB CDATI,OR,LAX,WRK1
20 0C473 46071134260C0001      ABBR SUNO,=2,SB,LAST
21 0C474 4607113540CC0000      ABB  SUNO,WRK1,W
22 0C475 0607114141000000      ABB  SUNC,LAX,WPRE
23 0C476 7406731224644000      EZB  OR,STOC,STAT,STAT
24
25
;SSE:
;
; CALL ESCAPE(7112)
;SS:
; IF W=ADDR THEN GOTO SS=REG
; WRK1:=DATAIN, TEST IO
; SB:=LAD:=SB-2, READP
; IF W=ADDR THEN GOTO INTU
; WRKO:=DATAIN, TEST IO
; W:=W - WRK1
; WPRE:=WPRE - WRKO - 1 + CARRY
; EX(22:23):=OVERFLOW CON CARRY
; TEST OVERFLOW, TEST INT
; GOTO NEXT INSTRUCTION
;SSREG:
; WRK1:=LAX
; SB:=LAST:=SB-2
; W:=W - WRK1
; WPRE:=WPRE - LAX - 1 + CARRY
; EX(22:23):=OVERFLOW CON CARRY
; TEST OVERFLOW, TEST INT
; GOTO NEXT INSTR

CONT TSTIO 1
CONT READP
CONT TSTIO 1
NEXT TOVIN
CONT 0 1
NEXT TOVIN
```

10022 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 U1
02 U2
03 U3
04 U4
05 U5
06 U6
07 U7
08 CC477 4624730000110020
09 CC500 0631000002000031
10
11 CC501 06310000000410506
12 CC502 0631000000020510
13 CC503 0251523300003704
14 CC504 2637330000520060
15 CC505 3406731224624100
16
17
18
19 CC506 0205403300000004
20
21 CC507 2635330000522060
22
23 CC510 7406731224604200
24
25
26
27
28
29 CC511 0624730000112020
30 CC512 0631000002000031
31
32 CC513 06310000000410524
33 CC514 4631000000020530
34 CC515 4604430000000000
35 CC516 0251523300003704
36
37 CC517 2636330001520060
38 CC520 0606731224004100

;SHIFT INSTRUCTIONS
;*****
;ARITHMETIC SHIFT SINGLE, AS, OPCODE = 44
ASE:
; IZQ OR,0,4401
; CALL F,F,ESCAP
AS:
; JMP T,NEG,ASRGT
; JMP F,NZ,ASN0
; CAR SUB0,,1,SB,LCD PUSH
; SLZBB OR,W,Z,NL,T,MAXLO LRTN
; EZB OR,STSZ,STAT,STAT NEXT TSHIN
ASRGT:
; ZAR ADD0,SB,LCD PUSH
ASN0:
; SRZBB OR,W,FO,NL,T,MAXLO LRTN
; EZB OR,STZZ,STAT,STAT NEXT TSTIN
;ARITHMETIC SHIFT DOUBLE, AD, OPCODE = 45
ADE:
; IZQ OR,0,4501
; CALL F,F,ESCAP
AD:
; JMP T,NEG,ADRGT
; JMP F,NZ,ADNO
; ZAQ OR,W
; CAR SUB0,,1,SB,LCD PUSH
; DLZBB OR,WPRE,Z,NL,T,MAXLO LRTN
; EZB OR,STSZ,STAT,STAT
;ASE:
; CALL ESCAPE(4401)
;AS:
; IF SB<0 THEN GOTO AS=RIGHT
; IF SB=0 THEN GOTO AS=NO
; LCD:= -SB+1 , COUNT:= MIN(SB,48)
; W:= W(COUNT:23+COUNT)
; EX:= SHIFT OFL, ZERU
; TEST SHIFT OFL, TEST INT,
; GOTO NEXT INSTRUCTION
;AS=RIGHT:
; LCD:=SB+1 , PUSH
; COUNT:= MAX(SB,-48)
; W:= SIGN EXTENDED W(U:23+COUNT)
;AS=NO:
; EX:= 0,0, TEST INT
; GOTO NEXT INSTRUCTION
;ADE:
; CALL ESCAPE(4501)
;AD:
; IF SB<0 THEN GOTO AD=RIGHT
; IF SB=0 THEN GOTO AD=NO
; Q:=W
; LCD:= -SB+1 , PUSH
; COUNT:= MIN(48,SB)
; WPRE,Q:= WPRE,Q (COUNT:47+COUNT)
; EX:= SHIFT OFL, ZERU

```

10023 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 00521 0613541232001400 CAB AND,3,STAT,WRKC
03 00522 4651523500003500 CA SUBO,.180,WRKU
04 00523 7407230000640000 ZQB OR,W
05
06
07 00524 4604430000000000 ADRGT: ZAQ OR,W
08 00525 0205403300000004 ZAR ADDO,SB,LCD
09
10 00526 2634330001522060 DRZBB OR,WPRE,FO,NL,T,MAXLO LRTN
11 00527 4607230000000000 ZQB OR,W
12
13 00530 3406731224624200 ADNO: EZB OR,STZZ,STAT,STAT NEXT TSHIN
14
15
16
17
18
19 00531 0624730000114020 LSE: 1ZQ OR,0,4601
20 00532 0631000002000031 CALL F,F,ESCAP
21
22 00533 4631000000410540 LS: JMP T,NEG,LSRGT
23 00534 063100000020537 JMP F,NZ,LSNO
24 00535 0251523300003704 CAR SUBO,.1,SB,LCD
25
26 00536 2637330000520060 SLZBB OR,W,Z,NL,T,MAXLO
27 00537 7407330000600000 LSNO: ZBB OR,W
28
29
30 00540 0205403300000004 LSRGT: ZAR ADDO,SB,LCD
31
32 00541 6635330000520060 SRZBB OR,W,Z,NL,T,MAXLO
33 00542 7407330000600000 ZBB OR,W
34
; WRKO:= SHIFT OFL, ZERO
; IF WRKO<>U THEN SET OVERFLOW
; W:= Q, TEST OVERFLOW
; GOTO NEXT INSTR
;AD-RIGHT:
; Q:=W
; LCD:= SB+1, PUSH
; COUNT:= MAX(SB,-48)
; WPRE,Q := SIGN EXT WPRE,Q(U:47+COUNT)
; W:= Q
;AD-NO:
; EX:= O,U, TEST INT
; GOTO NEXT INSTR

;LSE:
; CALL ESCAPE(4601)
;LS:
; IF SB<0 THEN GOTO LS-RIGHT
; IF SB=0 THEN GOTO LS-NO
; LCD:= -SB+1, PUSH
; COUNT:= MIN(SB,48)
; W:= W(COUNT:23+COUNT)
; TEST INT
; GOTO NEXT INSTR
;LS-RIGHT:
; LCD:= SB+1, PUSH
; COUNT:= MAX(SB,-48)
; W:= W(COUNT:23+COUNT)
; TEST INT
; GOTO NEXT INSTRUCTION

```

10024 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03
04
05
06
07
08
09
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
;LOGICAL SHIFT DOUBLE, LD, OPCODE = 47
;LDE:
05 00543 4624730000116020      IZQ  OR,0,4701
06 00544 0631000002000031      CALL F,F,ESCAP
;LD:
08 00545 4631000000410554      JMP  T,NEG,LDRGT
09 00546 4631000000020553      JMP  F,NZ,LDNO
10 00547 4604430000000000      ZAQ  OR,W
11 00550 0251523300003704      CAR  SUBO,.1,SB,LCD      PUSH
13 00551 2636330001520060      DLZBB OR,WPRE,Z,NL,T,MAXLO  LRTN
14 00552 3407230000600000      ZQB  OR,W      NEXT TSTIN
16 00553 7405230000600000      ZQ   OR      NEXT TSTIN
;LDRGT:
19 00554 4604430000000000      ZAQ  OR,W      PUSH
20 00555 0205403300000004      ZAR  ADDO,SB,LCD
22 00556 6634330001520060      DRZBB OR,WPRE,Z,NL,T,MAXLO  LRTN
23 00557 3407230000600000      ZQB  OR,W      NEXT TSTIN
;
; CALL ESCAPE(4701)
;LD:
; IF SB<0 THEN GOTO LD-RIGHT
; IF SB=0 THEN GOTO LD-NO
; Q:=W
; LCD:= -SB+1 , PUSH
; COUNT:= MIN(SB,48)
; WPRE,Q:= WPRE,Q(COUNT:47+COUNT)
; W:=Q, TEST INT
; GOTO NEXT INSTR
;LD-NO: TEST INT,
; GOTO NEXT INSTR
;LD-RIGHT:
; Q:= W
; LCD:= SB+1 , PUSH
; COUNT:= MAX(SB,-48)
; WPRE,Q:= WPRE,Q(SB:47+SB)
; W:=Q, TEST INT
; GOTO NEXT INSTR

```

10025 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

;NORMALIZE SINGLE, NS, OPCODE = 42

```

01
02
03
04
05 00560 4624730000104100
06 00561 0631000002000031
07
08 00562 0605240000000004
09 00563 0637330000000060
10 00564 0631000000020602
11 00565 4231000000460567
12 00566 6637330000470060
13 00567 0635330000001060
14
15 00570 06477120330003000
16 00571 0662731573000200
17
18
19
20 00572 463100000130371
21 00573 4654730000514000
22 00574 4662641532000300
23
24
25
26 00575 4605131533000012
27 00576 4613551332223711
28 00577 0645523500001000
29 00600 063100000020325
30 00601 3455730000710100
31
32
33 00602 0613730033002300
34 00603 0631000000000572

;NSE:
; CALL ESCAPE(4204)
;NS:
; LCD:=0
; W:= W SHIFT 1
; IF W = U THEN GOTO NS-ZERO
; IF W(0)=W(1) THEN GOTO NSC
; REPEAT
; W:= W SHIFT 1
; UNTIL W(0)<>W(1)
; WRK1:= -LCS
; WRK1:= IF ODD THEN
; 12 EXT U CON WRK1(12:23)
; ELSE
; WRK1(12:23) CON 12 EXT 0
; IF W-ADDR THEN GOTO HS-REG
; Q:= DATAIN, TEST IO
; WRKU:= IF ODD THEN
; Q(U:11) CON 12 EXT 0
; ELSE
; 12 EXT U CON Q(12:23)
; DATAOUT:= WRKU OR WRK1
; LAD:=WRKU:=SB(U:22) CON 1 EXT 0, WRTP
; IF ICS=WRKU THEN
; GOTO LOAD-IR
; TEST IO, TEST INT
; GOTO NEXT INSTR
;NSZER:
; WRK1:= 8'4000
; GOTO NS1

PUSH
LRTN

CONT TSTIO 1
CONT WRTP
CONT WRTN
NEXT TIOIN 1

```

10026 CPU823 MICROPROGRAM LBJ 850E16 VERSION 8

```

01 ;NORMALIZE DOUBLE, ND, OPCODE = 43
02
03
04
05 06604 06247300000106100
06 06605 0631000002000031
07
08 06606 06053300010000000
09 06607 0631000000420612
10 06610 46053300000000000
11 06611 0631000000020602
12 06612 06043300000000000
13 06613 06052400000000004
14 06614 06363300010000060
15 06615 0231000000460617
16 06616 6636330001470060
17 06617 0634330001001060
18
19 06620 46072300000000000
20 06621 46310000000000570

```

NDE: IZQ OR,U,4304  
CALL F,F,ESCAP

ND: ZB OR,WPRE  
JMP T,NZ,ND1  
ZB OR,W  
JMP F,NZ,NSZER

ND1: ZBQ OR,W  
ZQR AND,LCD  
DLZBB OR,WPRE,Z,NL,F,F  
JMP T,NORMU,NDU  
DLZBB OR,WPRE,Z,NL,T,NORM1  
DRZBB OR,WPRE,LNK,NL,F,F

ND0: ZQB OR,W  
JMP F,F,NSU0

PUSH  
LRTN

```

;NDE:
;
; CALL ESCAPE(4304)
;ND:
; IF WPRE=0 AND W=0 THEN
; GOTO NS-ZERO
;
; Q:=W
; LCD:= 0
; IF WPRE(0)=WPRE(1) THEN
; GOTO NDU ELSE PUSH
; REPEAT
; WPRE,Q:= WPRE,Q SHIFT 1
; UNTIL WPRE(0)<>WPRE(1)
; W:=Q
; GOTO NSOU

```

IG027 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 ;JUMP INSTRUCTIONS
03 ;*****
04
05 ;JUMP WITH REGISTER LINK, JL, OPCODE = 15
06 ;NO LINK
07
08
09 C0622 0624730000032400
10 C0623 0631000002000031
11
12 C0624 0631000000130032
13 C0625 0655431300410103
14 C0626 7405230000600000
15
16
17
18
19 C0627 0624730000032400
20 C0630 0631000002000031
21
22 C0631 0631000000130032
23 C0632 0604730000001000
24 C0633 0655431300410103
25
26 C0634 3407230000600000

;JLE:
;
; CALL ESCAPE(1520)
;JL:
; IF W-ADDR THEN GOTO INTO
; TEST LIMITVIOLATION, ICD:=SB
CONT TSTLI 1
NEXT TSTIN

;JLLE:
;
; CALL ESCAPE(1520)
;JLL:
; IF W-ADDR THEN GOTO INTO
; Q:= IC
CONT TSTLI 1
NEXT TSTIN

;WITH LINK
;
;JLE:
;
; CALL ESCAPE(1520)
;JLL:
; IF W-ADDR THEN GOTO INTO
; Q:= IC
CONT TSTLI 1
NEXT TSTIN

```

10028 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

;JUMP WITH INTERRUPT DISABLED, JD, OPCODE = 16  
;NO LINK

```

01
02
03
04
05
06 CC635 46247300000035400
07 CC636 06310000002000031
08
09 CC637 0641501300000600
10 CC640 0631000000010643
11 CC641 06515013000002300
12 CC642 0631000000010651
13
14 CC643 0631000000130032
15 CC644 06554313000410103
16 CC645 0613531224003605
17 CC646 07452300000000000
18 CC647 47052300000000000
19 CC650 74052300000600000
20
21 CC651 0613551471001706
22 CC652 06074113670000000
23 CC653 0653501325002300
24 CC654 4631000000001434
25
26
27
28
29 CC655 46247300000035400
30 CC656 06310000002000031
31
32 CC657 0641501300000600
33 CC660 4631000000010663
34 CC661 06515013000002300
35 CC662 0631000000010651

;JDE:
;
; CALL ESCAPE(1660)
;JD:
; IF SB>= -MONTOP THEN
; GOTO DISABLE
; IF SB >= -8'4000 THEN
; GOTO MONCALL
;DISABLE:
; IF W-ADDR THEN GOTO INTO
; TEST LIMITVIOLATION, ICDI= SB
; STAT:=CPUST:= STAT OR BIT 20 (DISABLE)
; DELAY 495 NSEC FOR CHANGE OF LEVEL
;
; TEST INT, GOTO NEXT INSTR
;MONCALL:
; AUTOLOADING:=U
; INF:=INF-1
; CAUSE:= SB+ 8'4000, MON CALL NO
; GOTO INTERRUPT CALL

;JDLE:
;
; CALL ESCAPE(1660)
;JDL:
; IF SB>= -MONTOP THEN
; GOTO DISABLE-LINK
; IF SB>= -8'4000 THEN
; GOTO MONCALL

```

```

;JDE:
;
; CALL ESCAPE(1660)
;JD:
; IF SB>= -MONTOP THEN
; GOTO DISABLE
; IF SB >= -8'4000 THEN
; GOTO MONCALL
;DISABLE:
; IF W-ADDR THEN GOTO INTO
; TEST LIMITVIOLATION, ICDI= SB
; STAT:=CPUST:= STAT OR BIT 20 (DISABLE)
; DELAY 495 NSEC FOR CHANGE OF LEVEL
;
; TEST INT, GOTO NEXT INSTR
;MONCALL:
; AUTOLOADING:=U
; INF:=INF-1
; CAUSE:= SB+ 8'4000, MON CALL NO
; GOTO INTERRUPT CALL

;JDLE:
;
; CALL ESCAPE(1660)
;JDL:
; IF SB>= -MONTOP THEN
; GOTO DISABLE-LINK
; IF SB>= -8'4000 THEN
; GOTO MONCALL

```



10C29 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 CC663 0631000000130032      ;DISABLE_LINK:
04 CC664 06047300000C1000      ; IF W-ADDR THEN GOTO INTO
05 CC665 0655431300410103      ; Q:= IC
06 CC666 06135312240C3605      ;TEST LIMIT, IC:= SB
07 CC667 07452300000C0000      ; STAT:=CPUST:= STAT OR BIT20 (DISABLE)
08 CC670 47052300000C0000      ; DELAY 495 NSEC
09 CC671 34072300000C0000      ;
10                                ; W:= Q, TEST INT, GOTO NEXT INSTR
11                                ;
12                                ;
13                                ;
14                                ;
15 CC672 06247300000C37400      ;JEE:
16 CC673 06310000002000031      ;
17                                ; CALL ESCAPE(1760)
18 CC674 0631000000130032      ;JE:
19 CC675 0655431300410103      ; IF W-ADDR THEN GOTO INTO
20 CC676 06135512240C3605      ; TESTLIMITVIOLATION, ICD:= SB
21 CC677 06310000000C0647      ; STAT:=CPUST:= STAT CAND BIT 20 (ENABLE)
22                                ; GOTO DSAB1
23                                ;
24                                ;
25                                ;
26                                ;
27 CC700 06247300000C37400      ;JELE:
28 CC701 06310000002000031      ;
29                                ; CALL ESCAPE(1760)
30 CC702 0631000000130032      ;JEL:
31 CC703 06047300000C1000      ; IF W-ADDR THEN GOTO INTO
32 CC704 0655431300410103      ; Q:= IC
33 CC705 06135512240C3605      ; TESTLIMITVIOLATION, ICD:= SB
34 CC706 46310000000C0670      ; STAT:=CPUST:= STAT CAND BIT 20 (ENABLE)
                                ; GOTO DSAL1

```

DSALL:

```

JMP F,NWADR,INTO
RZQ OR,ICS
XZAR CI0ST,OR,SB,ICD
CABR OR,-1B20,STAT,STAT,CPUST
ZG OR
ZG OR
ZGB OR,W

```

```

CONT TSTLI 1
CONT U 3
CONT U 2
NEXT TSTIN

```

;JUMP WITH INTERRUPT ENABLED, JE, OPCODE = 17  
;NO LINK

JEE:

```

IZQ OR,0,1760
CALL F,F,ESCAP

```

JE:

```

JMP F,NWADR,INTO
XZAR CI0ST,OR,SB,ICD
CABR CAND,-1B20,STAT,STAT,CPUST
JMP F,F,DSAB1

```

```

CONT TSTLI 1
CONT U 3
CONT U 2
NEXT TSTIN

```

;JUMP WITH INTERRUPT ENABLED, JE, OPCODE = 17  
;WITH LINK

JELE:

```

IZQ OR,0,1760
CALL F,F,ESCAP

```

JEL:

```

JMP F,NWADR,INTO
RZQ OR,ICS
XZAR CI0ST,OR,SB,ICD
CABR CAND,-1B20,STAT,STAT,CPUST
JMP F,F,DSAL1

```

```

CONT TSTLI 1
CONT U 3
CONT U 2
NEXT TSTIN

```

10030 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 ;FLOATING POINT INSTRUCTIONS
02 ;*****
03 ;*****
04 ;*****
05 ;CONVERT INTEGER TO FLOATING, CI, OPCODE = 40
06
07 GIE:
08 00707 062473000100020 IZQ OR,U,4001
09 00710 063100000200031 CALL F,F,ESCAP
10
11 00711 060743000100000 ZAB OR,W,WPRE
12 00712 4651712000000404 CZR SUNO,23,LCD
13 00713 06455233000003004 RAR SUBO,LCS,SB,LCD
14 00714 060424000000000 ZGQ AND
15
16 NORMA:
17
18 00715 060533000100000 ZB OR,WPRE
19 00716 063100000420723 JMP T,NZ,CINZ
20 00717 060523000000000 ZQ OR
21 00720 063100000420723 JMP T,NZ,CINZ
22 00721 0613730000002300 CIZER: CZB OR,1B12,W
23 00722 7406731224604200 EZB OR,STZZ,STAT,STAT NEXT TSTIN
24
25 00723 0636330001000060 DLZBB OR,WPRE,Z,NL,F,F
26 00724 023100000460726 JMP T,NORMO,CIO
27 00725 6636330001470060 DLZBB OR,WPRE,Z,NL,T,NORM1
28 00726 0634330001001060 DRZBB OR,WPRE,LNK,NL,F,F
29 00727 0650600000002300 CQQ ADD,1B12
30
31 00730 4674304001003060 DRZBB ADDC,WPRE,SGN,NL,F,F
32 00731 0636330001000060 DLZBB OR,WPRE,Z,NL,F,F
33 00732 023100000460734 JMP T,NORMO,CIO
34 00733 6636330001470060 DLZBB OR,WPRE,Z,NL,T,NORM1
35 00734 0634330001001060 DRZBB OR,WPRE,LNK,NL,F,F
36 00735 06106500000000300 CQQ CAND,B12
;CIE:
; CALL ESCAPE(4001)
;CI:
; WPRE:=W
; LCD:= -SB -23
; Q:= 0
;NORMALIZE:
; WPRE,Q= FRACTION
; LC= -EXPONENT
; IF WPRE=0 AND W=U THEN
; BEGIN
; W:=8'4000 , TEST INT
; GOTO NEXT INSTR
; EX(22:23):= 0
; END
;
; IF WPRE(0)=WPRE(1) THEN
; NORMALIZE WPRE,Q
; Q:= Q+ 8'4000
;ROUND:
; WPRE,Q:= (WPRE+CARRY,Q) SHIFT 1
;
; IF WPRE(0)=WPRE(1) THEN
; NORMALIZE WPRE,Q
; Q:= Q(U:11) CON 12 EXT 0

```

10031 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 00736 4653702032002300      CZB   ADDO, 1B12,WRKO
03 00737 464751532003000      RAB   SUNO,LCS,WRKO,WRKO
04 00740 4613561532002300      CAB   EXOR, 1B12,WRKO,WRKO
05 00741 0613541500000300      CAB   AND, B12,WRKU,W
06 00742 0607030000000000      AQB   OR,W,W
07 00743 0606731224004200      EZB   OR,STZZ,STAT,STAT
08 00744 0611551500000300      CA   CAND,B12,WRKO
09 00745 463100000420747      JMP   T,NZ,C11
10 00746 7405230000600000      ZQ   OR
                                NEXT TSTIN
11
12 00747 0631000000410751      JMP   T,NEG,C1UFL
13 00750 4607302024000000      ZBB   ADDO,STAT
14 00751 4607302024000000      ZBB   ADDO,STAT
15 00752 4611541200002600      ZBB   AND, 1B5,STAT
16 00753 063100000420035      JMP   T,NZ,INTF
17 00754 7405230000600000      ZQ   OR
                                NEXT ISTIN
18
19
20
21
22 00755 4624730000152020      IZQ   OR,U,65U1
23 00756 0631000002000031      CALL  F,F,ESCAP
24
25 00757 4662730032000400      HZB   OR,EXT,W,WRKU
26 00760 0610550000000300      CAQ   CAND,B12,W
27 00761 060743000730000000      ZAB   OR,WPRE,WRK1
28 00762 4607101332000000      ABB   ADD,SB,WRKU
29 00763 0631000000411004      JMP   T,NEG,CFZER
30 00764 0653513532000400      CAB   SUNO, 23,WRKU,WRKO
31 00765 0631000000410773      JMP   T,NEG,CFSHI
32 00766 4631000000020775      JMP   F,NZ,CFEXI
33 00767 0605330001000000      ZB   OR,WPRE
34 00770 4631000000021004      JMP   F,NZ,CFZER

;CONVERT FLOATING TO INTEGER, CF, OPCODE = 65
;CFE:
;CALL ESCAPE(6501)
;CF:
;WRKO:= 12 EXT W(12) CON W(12:23)
;Q:= W(U:11) CON 12 EXT U
;WRK1:= WPRE
;WRKU:=WRKU+SB
;IF WRKU<U THEN GOTO CF-ZERO
;WRKO:= WRKU-23
;IF WRKU<U THEN GOTO CF-SHIFT
;IF WRKU=0 THEN GOTO CF-EXIT
;IF WPRE = U THEN
;GOTO CF-ZERO
;CFE:
;CALL ESCAPE(6501)
;CF:
;WRKO:= 12 EXT W(12) CON W(12:23)
;Q:= W(U:11) CON 12 EXT U
;WRK1:= WPRE
;WRKU:=WRKU+SB
;IF WRKU<U THEN GOTO CF-ZERO
;WRKO:= WRKU-23
;IF WRKU<U THEN GOTO CF-SHIFT
;IF WRKU=0 THEN GOTO CF-EXIT
;IF WPRE = U THEN
;GOTO CF-ZERO

```

10032 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 CC771 0664713000000000 CFOFL: IZQ SUNO,4000,0
03 CC772 7406731224644000 EZB OR,STOC,STAT,STAT NEXT TOVIN
04
05
06 CC773 0205403500000004 ZAR ADDO,WRKO,LCD PUSH
07 CC774 2634330033112060 DRZBB OR,WRK1,FU,NL,F,LCN LRTN
08 CC775 0605230000000000 CFEXI: ZQ OR
09 CC776 0631000000411001 JMP T,NEG,CF1
10 CC777 4607431540000000 ZAB OR,WRK1,W
11 01000 7406731224604200 EZB OR,STZZ,STAT,STAT NEXT TSTIN
12 01001 0607403540000000 ZAB ADDO,WRK1,W
13 01002 0631000000430771 JMP T,OVFL,CFOFL
14 01003 7406731224604200 EZB OR,STZZ,STAT,STAT NEXT TSTIN
15
16
17 CFZER: EZB OR,STZZ,STAT,STAT
ZBB AND,W NEXT TSTIN
18 01004 0606731224004200
19 01005 3407340000060000
20
21 ;MULTIPLY FLOATING, FM, OPCODE = 62
22
23 FME:
24 01006 46247300000144240 IZQ OR,O,6212
25 01007 0631000002000031 CALL F,F,ESCAP
26
27 01010 06627300360000400 HZB OR,EXT,W,WRK4
28 01011 0631000000131055 JMP F,NWADR,FMREG
29 01012 4657730033514000 XZB OR,DATAI,WRK1
30 01013 0647113426200011 ABBR SUNO,2,SB,LAD
31 01014 0631000000130032 JMP F,NWADR,INTO
32 01015 46627315740000400 HZB OR,EXT,WRK1,WRK2
33 01016 06135515730000300 CAB CAND,B12,WRK1,WRK1
34 01017 46071037340000000 ABB ADDO,WRK4,WRK2
35 01020 0657730032514000 XZB OR,DATAI,WRKO

```

```

; ELSE SET OVERFLOW
; EX:= 1,U , TEST OVERFLOW
; TEST INT, GOTO NEXT INSTR
;CF-SHIFT:
; LCD:= WRKU + 1
; WRK1,Q:= WRK1(0) CON WRK1,Q(0:22)
; IF Q>=0 THEN
; EX(22:23):= 0,0 , TEST INT
; W:= WRK1
; GOTO NEXT INSTR
; W:= WRK1+1
; IF OVERFLOW THEN
; BEGIN
; EX(22:23):=U,0 , TEST INT
; GOTO NEXT INSTR
;CF-ZERO:
; EX:= 0
; W:= 0 , TEST INT, GOTO NEXT INSTR
;FME:
; CALL ESCAPE(6212)
;FM:
; WRK4:= 12 EXT W(12) CON W(12:23)
; IF W-ADDR THEN GOTO FM-REG
; WRK1:= DATAIN , TEST IO
; LAD:=SB:= SB-2 , READP
; IF W-ADDR THEN GOTO INTO
; WRK2:= 12 EXT WRK1(12) CON WRK1(12:23)
; WRK1:=WRK1(U:11) CON 12 EXT 0
; WRK2:= WRK2+WRK4+1
; WRKU:= DATAIN , TEST IO

```

```

NEXT TOVIN
PUSH
LRTN
NEXT TSTIN
NEXT TSTIN
NEXT TSTIN

```

```

OR,STOC,STAT,STAT
OR,WRK1,FU,NL,F,LCN
OR
T,NEG,CF1
OR,WRK1,W
OR,STZZ,STAT,STAT
ADDO,WRK1,W
T,OVFL,CFOFL
OR,STZZ,STAT,STAT
OR,STZZ,STAT,STAT
AND,W

```

```

IZQ OR,O,6212
CALL F,F,ESCAP
HZB OR,EXT,W,WRK4
JMP F,NWADR,FMREG
XZB OR,DATAI,WRK1
ABBR SUNO,2,SB,LAD
JMP F,NWADR,INTO
HZB OR,EXT,WRK1,WRK2
CAB CAND,B12,WRK1,WRK1
ABB ADDO,WRK4,WRK2
XZB OR,DATAI,WRKO

```

```

0205403500000004
2634330033112060
0605230000000000
0631000000411001
4607431540000000
7406731224604200
0607403540000000
0631000000430771
7406731224604200
0606731224004200
3407340000060000
46247300000144240
0631000002000031
06627300360000400
0631000000131055
4657730033514000
0647113426200011
0631000000130032
46627315740000400
06135515730000300
46071037340000000
0657730032514000

```

10033 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 01021 06135500000000300 FM1: CAB CAND,B12,W,W
03 01022 06625300000000600 HAB OR,LSWAP,W,W
04 01023 46044300000000000 ZAQ OR,W
05 01024 06353300010000060 SRZBB OR,WPRE,Z,NL,F,F
06 01025 063533000000001060 SRZBB OR,W,LNK,NL,F,F
07 01026 466273000000000600 HZB OR,LSWAP,W,W
08 01027 060743007600000000 ZAB OR,WPRE,WRK4
09 01030 46741120410000040 DRABB SUNO,WPRE,WPRE,Z,MC,F,F
10 01031 460443000000000000 ZAQ OR,W
11 01032 42517120000001204 CZR SUNO,.11,LCD PUSH
12 01033 06742015400004260 MULQ ADD,WRK1,NL,F,F
13 01034 2674105501113240 MULD ADDC,WRKO,WPRE,SGN,MC,F,LCN LRTN
14
15
16
17
18
19
20
21 01035 06106500000000300 CQQ CAND,B12
22 01036 46135417350000300 CAB AND,B12,WRK4,WRK3
23 01037 06040316400000000 AQQ OR,WRK3
24 01040 42517120000001204 CZR SUNO,.11,LCD PUSH
25 01041 06742015400004260 MULQ ADD,WRK1,NL,F,F
26 01042 2674105501113240 MULD ADDC,WRKO,WPRE,SGN,MC,F,LCN LRTN
27 01043 06106500000000300 CQQ CAND,B12
28
29 01044 46627317360000600 HZB OR,LSWAP,WRK4,WRK4
30 01045 06040317000000000 AQQ OR,WRK4
31 01046 02517120000001304 CZR SUNO,.10,LCD PUSH
32 01047 06742015400004260 MULQ ADD,WRK1,NL,F,F
33 01050 2674105501113240 MULD ADDC,WRKO,WPRE,SGN,MC,F,LCN LRTN
;
; W:= W(U:11) CON 12 EXT U
; W:= W(U:11) CON W(U:11)
; Q:= W
; WPRE:= WPRE SHIFT (-1)
; W:= SHLINK CON W(U:22)
; W:= 12 EXT U CON W(U:11)
; WRK4:= WPRE
; MC:= Q(23) C. W(12) WPRE:= 0
; Q:= W
; FOR LCD:= -11,LCD+1 WHILE LCD<=0 DO
; BEGIN
; IF ADC=U THEN
; BEGIN
; Q:= Q+WRK1
; WPRE:=WPRE+WRKO+C
; END
; ADC:= Q(23)
; WPRE,Q:= W(0) CON W,Q(U:22)
; END
; Q:= Q(U:11) CON 12 EXT U
; WRK3:= 12 EXT 0 CON WRK4(12:23)
; Q:= Q OR WRK3
; FOR LCD:= -11,LCD+1 WHILE LCD<= 0 DO
; 'MULTIPLY'
; Q:= Q(U:11) CON 12 EXT 0
; WRK4:= 12 EXT 0 CON WRK4(0:11)
; Q:= Q OR WRK4
; FOR LCD:= -10,LCD+1 WHILE LCD<=0 DO
; 'MULTIPLY'
;

```

10034 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 01051 0674213540004260      MULQ  SUN0,WRK1,NL,F,F
03 01052 4674115501003260      MULD  SUNC,WRKO,WPRE,SGN,NL,F,F
04
05
06 01053 0605423600000004      ZAR   SUB0,WRK2,LCD
07 01054 0631000000000715      JMP   F,F,NORMA
08
09
10
11 01055 4657430173000000      FMREG: XZAB  CDATI,OR,LAX,WRK1      CONT 0 1
12 01056 4607113426000001      ABBR   SUN0,.2,SB,LAST
13 01057 4662731574000400      HZB    OR,EXT,WRK1,WRK2
14 01060 0613551573000300      CAB    CAND,B12,WRK1,WRK1
15 01061 4607103734000000      ABB    ADD0,WRK4,WRK2
16 01062 0607430172000000      ZAB    OR,LAX,WRKO
17 01063 4631000000001021      JMP   F,F,FM1
18
19
20
21
22 01064 46247300000144240      FMFPE: IZQ   OR,0,6212
23 01065 0631000002000031      CALL  F,F,ESCAP
24
25 01066 4605430000020071      FMFPU: ZAR   OR,W,FPOP1      CONT FMOP
26 01067 0605430040000070      ZAR   OR,WPRE,FPOPO
27 01070 4631000000131114      JMP   F,NWADR,FPURG
28 01071 0657730033004000      FPU1: XZB   OR,DATI,WRK1      CONT 0 1
29 01072 4617730032004100      XZB   OR,IOSTA,WRKO
30 01073 4645113426200011      ABR   SUN0,.2,SB,LAD      CONT READP
31 01074 0605431540000071      ZAR   OR,WRK1,FPOP1
32 01075 4631000000131117      JMP   F,NWADR,FPURG
33 01076 0655730000004070      XZR   OR,DATI,FPUPU      CONT 0 1
34 01077 4613730035001400      CZB   OR,.3,WRK3

```

;MULTIPLY FLOATING, WHEN THE FPUB01 IS PRESENT

```

;FMFPE:
;
; CALL ESCAPE(6212)
;FMFPU:
; FPUOP1:= W , FMOP
; FPUOPU:= WPRE , START FPU
; IF W-ADDR THEN GOTO FPU-REG
; WRK1:= DAIN , TEST IO
; WRKO:= IOSTA
; LAD:= SB-2 , READP
; FPUOP1:= WRK1
; IF W-ADDR THE GOTO FPRG1
; FPOPU:= DAIN , TEST IO , START FPU
; WRK5:= 3

```

10035 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 01100 0611541500003400      CA      AND,.7,WRKU
03 01101 063100000421424      JMP     T,NZ,INTFP
04 01102 0607113426000000      ABB     SUNO,.2,SB
05 01103 4617730032004100      XZB     OR,IOSTA,WRKU
06 01104 0611541500003400      CA      AND,.7,WRKU
07 01105 063100000421424      JMP     T,NZ,INTFP
08 01106 0615730000007000      XZ      OR,FPURO
09 01107 4657730001000700      XZB     OR,FPURO,WPRE
10 01110 4657730000007100      XZB     OR,FPUR1,W
11 01111 4654541640007200      XAQ     AND,FPUST,WRK3
12 01112 063100000421127      JMP     T,NZ,FPUER
13
14 01113 7406731224604200      EZB     OR,STZZ,STAT,STAT      NEXT TSTIN
15
16
17 01114 0655230000000000      FPURG:  XXZQ
18 01115 0605430140000071      ZAR     OR,LAX,FPOP1
19 01116 4607113426000001      ABBR    SUNO,.2,SB,LAST
20 01117 4605430140000070      FPRG1:  ZAR     OR,LAX,FPOP1
21 01120 4613730035001400      CZB     OR,.3,WRK3
22 01121 0615730000007000      XZ      OR,FPURO
23 01122 4657730001000700      XZB     OR,FPURO,WPRE
24 01123 4657730000007100      XZB     OR,FPUR1,W
25 01124 4654541640007200      XAQ     AND,FPUST,WRK3
26 01125 063100000421127      JMP     T,NZ,FPUER
27
28 01126 7406731224604200      EZB     OR,STZZ,STAT,STAT      NEXT TSTIN
29
30
31 01127 4606631224004200      FPUER:  EQB     OR,STZZ,STAT,STAT
32 01130 4611541200002600      CA      AND,.1B5,STAT
33 01131 063100000420035      JMP     T,NZ,INTF
34 01132 7405230000600000      ZQ      OR

```

```

; IF WRKU(21:23)<> 0 THEN
;   GOTO INT-FPU
; SB:= SB-2
; WRKU:= IOSTA
; IF WRKU(21:23)<>0 THEN
;   GOTO INT-FPU
; MAKE FPURO BUSY
; WPRE:= FPUK0
; W:= FPUR1
; Q:= FPUST AND .3
; IF Q<>0 THEN GOTO FPU-ERROR
; EX:= 0
; TEST INT, GOTO NEXT INSTR

;FPU-REG:
; NO OP
; FPUOP1:= LAX
; LAST:=SB:= SB-2
; FPUOPO:= LAX , START FPU
; WRK3:= 3
; MAKE FPURO BUSY
; WPRE:= FPURO
; W:= FPUR1
; Q:= FPUST AND .3
; IF Q<>0 THEN GOTO FPU-ERROR
; EX:= 0
; TEST INT , GOTO NEXT INSTR

;FPU-ERROR:
; STAT(21:23):= 0
; IF STAT(5) THEN
;   GOTO FLOATING EXCEPTION
; ELSE TEST INT , GOTO NEXT INSTR

```

```

CONT 0 1
CONT 0 1
CONT 0 1
NEXT TSTIN
CONT 0 1
CONT 0 1
CONT 0 1
NEXT TSTIN
CONT 0 1
CONT 0 1
CONT 0 1
NEXT TSTIN
NEXT TSTIN

```

10036 CPU823 MICROPROGRAM LBJ 850016 VERSION 8

;DIVIDE FLOATING, FD, OPCODE = 64

```

01
02
03
04
05 01133 4624730000150240
06 01134 063100002000031
07
08 01135 0662730035000400
09 01136 4631000000131200
10 01137 4657730033514000
11 01140 0647113426200011
12 01141 0631000000130032
13 01142 4662731574000400
14 01143 0613551573000300
15 01144 0657730032514000
16 01145 4631000000021206
17 01146 4605430040000000
18 01147 0631000000020721
19 01150 0613550000000300
20 01151 0607123674000000
21 01152 0637330032000400
22 01153 46373300010004020
23 01154 0604240000000000
24 01155 0251712000000404
25 01156 4676107540002160
26 01157 6677105501111120
27
28
29
30
31
32
33
34
35
36
37
38

;FDE:
; CALL ESCAPE(6412)
;FD:
; WRK3:= 12 EXT W(12) CON W(12:23)
; IF W-ADDR THEN GOTO FD-REG
; WRK1:= DAIN , TEST IO
; LAD:= SB:= SB-2 , READP
; IF W-ADDR THEN GOTO INTU
; WRK2:= 12 EXT WRK1(12) CON WRK1(12:23)
; WRK1:= WRK1(0:11) CON 12 EXT 0
; WRKU:= DAIN , TEST IO
; IF WRKU=0 THEN GOTO FD-ZERO-DIVISOR
; IF WPRE = 0 THEN
; GOTO CI-ZERO
; W:= W(0:11) CON 12 EXT 0
; WRK2:= WRK3 - WRK2
; DVS:= WRKU(0)
; ADC:= WPRE(0) EXOR -,DIVSIGN
; Q:= 0
; FOR LCD:=23,LCD+1 WHILE LCD<=0 DO
; BEGIN C. DIVIDE
; IF ADC = 1 THEN
; BEGIN
; W:= W-WRK1
; WPRE:= WPRE-WRKU-1+C
; END ELSE
; BEGIN
; W:= W+WRK1
; WPRE:= WPRE+WRKU
; END
; SHLNK CON WPRE CON W CON Q:=
; WPRE CON W CON Q CON 1 EXT ADC
; ADC:= WPRE(0) EXOR -,DIVSIGN
; END

```

CONT TSTIO 1

CONT READP

CONT TSTIO 1

PUSH

ADDX,WRK1,W,ADC,NL,F,F

ADDC,WRKU,WPRE,LNK,DC,F,LCN LRTN



10037 CPU823 MICROPROGRAM LBJ 850&16 VERSION 8

```

01
02 01160 061366U0350C3500
03 01161 46310U000U461166
04 01162 4637330U350C2060
05 01163 467710/540C00160
06 01164 06771055010C1120
07 01165 0607310034C00000
08
09 01166 4610730000C0300
10 01167 425171200001204
11 01170 4676107540C02160
12 01171 667710550111120
13 01172 4607230000C0000
14 01173 4662750000C0500
15 01174 4605421600C00004
16 01175 06074316410C0000
17 01176 4604406000C00000
18 01177 46310U0000C00730
19
20
21 01200 46574301730C0000
22 01201 4607113426C00001
23 01202 4662731574C00400
24 01203 0613551573000300
25 01204 0607430172000000
26 01205 46310U0000001145
27
28
29 01206 06647130000000000
30 01207 4606731224C04000
31 01210 06310U00000000752

```

```

; WRK3:= Q EXOR BIT 0
; IF WRK3 NOT NORMALIZED THEN
; BEGIN
; SH LINK CON WRK3:= WRK3 CON ADC
; DIVIDE
; WRK2:= WRK2-1
; END
; Q:= 8'7777
; FOR LCD:= -11, LCD+1 WHILE LCD<=0 DO
; DIVIDE
; W:= Q
; W:= W(12:23) CON W(U:11)
; LCD:= -WRK2 -1
; WPRE:= WRK3
; Q:= W + ADC
; GOTO ROUND
; FD-REG:
; WRK1:= LAX
; SB:=LAST:= SB-2
; WRK2:= 12 EXT WRK1(12) CON WRK1(12:23)
; WRK1:= WRK1(0:11) CON 12 EXT 12
; WRKU:= LAX
; GOTO FDO
; FD-ZERO-DIVISOR:
; SET OVERFLOW
; EX(22:23)I= 2
; GOTO TEST-INTF

```

```

CQB EXOR,180,WRK3
JMP T,NORMU,FD1
SLZBB OR,WRK3,ADC,NL,F,F
DIVS ADDX,WRK1,W,Z,NL,F,F
DIVS ADDC,WRK0,WPRE,LNK,DC,F,F
ZBB SUN,WRK2
FD1:
CZQ OR,B12
CZR SUNO,-11,LCD PUSH
DIVD ADDX,WRK1,W,ADC,NL,F,F
DIVS ADDC,WRK0,WPRE,LNK,DC,F,LCN LRTN
ZQB OR,W
HQB OR,SWAP,W,W
ZAR SUB,WRK2,LCD
ZAB OR,WRK3,WPRE
ZAQ ADDX,W
JMP F,F,ROUND
XZAB CDATI,OR,LAX,WRK1
ABBR SUNO,-2,SB,LAST
HZB OR,EXT,WRK1,WRK2
CAB CAND,B12,WRK1,WRK1
ZAB OR,LAX,WRKU
JMP F,F,FDO
IZQ SUNO,4000,0
EZB OR,STOC,STAT,STAT
JMP F,F,TINTF

```

10038 CPU823 MICROPROGRAM LBJ 850016 VERSION 8

;FLOATING DIVIDE, FD, WHEN THE FPU801 IS PRESENT

```

01
02
03
04
05 01211 4624730000150240
06 01212 0631000002000031
07
08 01213 0605430000030071
09 01214 0605430040000070
10 01215 463100000131114
11 01216 463100000001071
12
13
14
15
16 01217 0624730000140240
17 01220 0631000002000031
18
19 01221 0662730035000400
20 01222 463100000131256
21 01223 4657730033514000
22 01224 0647113426200011
23 01225 063100000130032
24 01226 4662731574000400
25 01227 0613551573000300
26 01230 0657730032514000
27 01231 0613550000000300
28 01232 0607113635000000
29 01233 0631000000021251
30 01234 4631000000411245
31 01235 0607101674000000
32 01236 4651513640000200
33 01237 063100000011264
34 01240 4604431540000000
35 01241 4251523640003704
36 01242 2634330032522060
37 01243 4607230033000000
38 01244 463100000001251

```

FDPE:

```

IZQ OR,0,6412
CALL F,F,ESCAP

```

FDFPU:

```

ZAR OR,W,FPOP1
ZAR OR,WPRE,FPOP0
JMP F,NWADR,FPURG
JMP F,F,FPU1

```

CONT FPOP

;FDPE:

```

;
; CALL ESCAPE(6412)
;FDFPU:
; FPUOP1:= W , FPOP
; FPUOP0:= WPRE , START FPU
; IF W-ADDR THEN GOTO FPU-REG
; GOTO FPU1

```

;FLOATING ADD, FA, OPCODE = 60

FAE:

```

IZQ OR,0,6012
CALL F,F,ESCAP

```

FA:

```

HZB OR,EXT,W,WRK3
JMP F,NWADR,FAREG
XZB OR,DATI,WRK1
ABBR SUNO,-2,SB,LAD
JMP F,NWADR,INTO
HZB OR,EXT,WRK1,WRK2
CAB CAND,B12,WRK1,WRK1
XZB OR,DATI,WRKU
CAB CAND,B12,W,W
ABB SUNO,WRK2,WRK3
JMP F,NZ,FAEQ
JMP T,NEG,FASM
ABB ADD,WRK3,WRK2
CA SUNO,-38,WRK3
JMP F,NEG,FSAEX
ZAG OR,WRK1
CAR SUBO,-1,WRK3,LCD
DRZBB OR,WRKU,FO,NL,I,MAXLO LRTN
ZGB OR,WRK1
JMP F,F,FAEQ

```

FA1:

```

HZB OR,EXT,W,WRK3
JMP F,NWADR,FAREG
XZB OR,DATI,WRK1
ABBR SUNO,-2,SB,LAD
JMP F,NWADR,INTO
HZB OR,EXT,WRK1,WRK2
CAB CAND,B12,WRK1,WRK1
XZB OR,DATI,WRKU
CAB CAND,B12,W,W
ABB SUNO,WRK2,WRK3
JMP F,NZ,FAEQ
JMP T,NEG,FASM
ABB ADD,WRK3,WRK2
CA SUNO,-38,WRK3
JMP F,NEG,FSAEX
ZAG OR,WRK1
CAR SUBO,-1,WRK3,LCD
DRZBB OR,WRKU,FO,NL,I,MAXLO LRTN
ZGB OR,WRK1
JMP F,F,FAEQ

```

;FAE:

```

;
; CALL ESCAPE(6012)
;FA:
; WRK3:= 12 EXT W(12) CON W(12:23)
; IF W-ADDR THEN GOTO FA-REG
; WRK1:= DAIN , TEST IO
; LAD:=SB:= SB-2 , READP
; IF W-ADDR THEN GOTO INTO
; WRK2:= 12 EXT WRK1(12) CON WRK1(12:23)
; WRK1:= WRK1(0:11) CON 12 EXT 0
; WRKU:= DAIN , TEST IO
; W:= W(0:11) CON 12 EXT 0
; IF WRK3>WRK2 THEN
; BEGIN
; WRK2:= WRK3
; IF WRK3>= 38 THEN
; GOTO EXIT
; Q:= WRK1
; LCD:= -(WRK3-WRK2) +1
; WRKU,Q:= SGN EXT WRKU,Q(0:23-COUNT)
; WRK1:= Q
; END ELSE

```

CONT TSTIO 1

CONT READP

CONT TSTIO 1

10039 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 01245 46044300000000000000 FASM: ZAQ OR,W
03 01246 420540364000000004 ZAR ADDO,WRK3,LCD PUSH
04 01247 2634330001522060 DRZBB OR,WPRE,FO,NL,T,MAXLO LRTN
05 01250 460723000000000000 ZQB OR,W
06
07
08
09
10 01251 460443000000000000 FAEQ: ZAQ OR,W
11 01252 060400154000000000 AGQ ADD,WRK1
12 01253 4674105501003060 DRABB ADDC,WRKU,WPRE,SGN,NL,F,F
13 01254 460542160000000004 ZAR SUB,WRK2,LCD
14 01255 06310000000000715 JMP F,F,NORMA
15
16
17 01256 465743017300000000 FAREG: XZAB CDATI,OR,LAX,WRK1 CONT U 1
18 01257 460711342600000001 ABBR SUNO,,2,SB,LAST
19 01260 46627315740000400 HZB OR,EXT,WRK1,WRK2
20 01261 06135515730000300 CAB CAND,B12,WRK1,WRK1
21 01262 06074301720000000 ZAB OR,LAX,WRKU
22 01263 46310000000001231 JMP F,F,FA1
23
24 01264 46135416340000300 FSAEX: CAB AND,B12,WRK2,WRK2
25 01265 74071316000600000 ABB OR,WRK2,W
26
27
28
29
30
31 01266 0624730000140240 FAFPE: IZQ OR,O,6012
32 01267 0631000002000031 CALL F,F,ESCAP
33
34 01270 06054300000000071 FAFPU: ZAR OR,W,FPOP1
35 01271 06054300400000070 ZAR OR,WPRE,FPOP0
36 01272 4631000000131114 JMP F,NWADR,FPURG
37 01273 46310000000001071 JMP F,F,FPU1
; IF WRK2>WRK3 THEN
; BEGIN
; Q:= W
; LCD:= WRK3-WRK2+1
; COUNT:= MIN(-LCD,48)
; WPRE,Q:= SGN EXT WPRE,Q(C:23+COUNT)
; W:= Q
; END
; Q:= W
; Q:= Q+WRK1
; WPRE,Q:= ((WPRE+WRKU+C),Q) SHIFT (-1)
; LCD:= -WRK2 - 1
; GOTO NORMALIZE
;FA-REG:
; WRK1:= LAX
; SB:=LAST:= SB-2
; WRK2:= 12 EXT WRK1(12) CON WRK1(12:23)
; WRK1:= WRK1(0:11) CON 12 EXT 0
; WRK0:= LAX
; GOTO FA1
;EXIT:
; WRK2:= WRK2(12:23)
; W:= W OR WRK2 , TEST INT
; GOTO NEXT INST
;FATPE:
; CALL ESCAPE(6012)
;FAFPU:
; FPUOP1:= W , FAOP
; FPUOP0:= WPRE , START FPU
; IF W-ADDR THEN GOTO FPU-REG
; GOTO FPU1
;FLOATING ADD, FA, WHEN THE FPU801 IS PRESENT
;FATPE:
; IZQ OR,O,6012
;CALL F,F,ESCAP
;FAFPU:
;ZAR OR,W,FPOP1
;ZAR OR,WPRE,FPOP0
;JMP F,NWADR,FPURG
;JMP F,F,FPU1

```

10040 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

;FLOATING SUBTRACT, FS, OPCODE = 61

01  
02  
03  
04  
05 01274 46247300000142240  
06 01275 0631000002000031  
07  
FSE:  
08 01276 0662730035000400  
09 01277 0631000000131333  
10 01300 4657730033514000  
11 01301 064711342620011  
12 01302 0631000000130032  
13 01303 4662731574000400  
14 01304 0613551573000300  
15 01305 0657730032514000  
16 01306 061355000000300  
FS1:  
17 01307 0607113635000000  
18 01310 463100000021326  
19 01311 4631000000411322  
20 01312 0607101674000000  
21 01313 465151364000200  
22 01314 063100000001264  
23 01315 4604431540000000  
24  
25 01316 4251523640003704  
26 01317 2634330032522060  
27 01320 4607230033000000  
28 01321 0631000000001326  
29 01322 4604430000000000  
30 01323 4205403640000004  
31 01324 26343300001522060  
32  
33  
34  
35 01325 4607230000000000  
36  
;FSE:  
; ;  
; CALL ESCAPE(6112)  
;FS:  
; WRK3:= 12 EXT W(12) CON W(12:23)  
; IF W-ADDR THEN GOTO FS-REG  
; WRK1:= DATAIN , TEST IO  
; LAD:=SB:= SB-2 , READP  
; IF W-ADDR THEN GOTO INTU  
; WRK2:= 12 EXT WRK1(12) CON WRK1(12:23)  
; WRK1:= WRK1(0:11) CON 12 EXT 0  
; WRKU:= DATAIN , TEST IO  
; W:= W(0:11) CON 12 EXT 0  
; ;  
; IF WRK3>WRK2 THEN  
; BEGIN  
; WRK2:= WRK3  
; IF WRK3>= 38 THEN  
; GOTO EXIT  
; Q:= WRK1  
; COUNT:= MIN(48,WRK3-WRK2)  
; LCD:= -(WRK3-WRK2)+1  
; WRKU,Q:= SGN EXT WRKU,Q(0:23-COUNT)  
; WRK1:= Q  
; END ELSE  
; IF WRK2>WRK3 THEN  
; BEGIN  
; Q:= W  
; COUNT:=MIN(48,WRK2-WRK3)  
; LCD:=WRK3-WRK2+1  
; WPRE,Q:= SGN EXT WPRE,Q(0:23-COUNT)  
; W:= Q  
; END

CONT TSTIO 1  
CONT READP  
CONT TSTIO 1

IZQ OR,0,6112  
CALL F,F,ESCAP  
HZB OR,EXT,W,WRK3  
JMP F,NWADR,FSREG  
XZB OR,DATI,WRK1  
ABBR SUNO,.2,SB,LAD  
JMP F,NWADR,INTU  
HZB OR,EXT,WRK1,WRK2  
CAB CAND,B12,WRK1,WRK1  
XZB OR,DATI,WRKU  
CAB CAND,B12,W,W  
ABB SUNO,WRK2,WRK3  
JMP F,NZ,FSEQ  
JMP T,NEG,FSSM  
ABB ADD,WRK3,WRK2  
CA SUNO,.38,WRK3  
JMP F,NEG,FSAEX  
ZAQ OR,WRK1  
CAR SUBO,.1,WRK3,LCD  
DRZBB OR,WRKU,FO,NL,T,MAXLO LRTN  
ZQB OR,WRK1  
JMP F,F,FSEQ  
ZAQ OR,W  
ZAR ADDO,WRK3,LCD  
DRZBB OR,WPRE,FO,NL,T,MAXLO LRTN  
ZQB OR,W

PUSH  
LRTN  
PUSH  
LRTN

10041 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 01326 4604430000000000
04 01327 0604013540000000
05 01330 0674115501003060
06 01331 4605421600000004
07 01332 0631000000000715
08
09 01333 4657430173000000
10 01334 4607113426000001
11 01335 4662731574000400
12 01336 0613551573000300
13 01337 0607430172000000
14 01340 4631000000001306
15
16
17
18
19 01341 4624730000142240
20 01342 0631000002000031
21
22 01343 4605430000010071
23 01344 0605430040000070
24 01345 4631000000131114
25 01346 46310000000001071

;
; Q:= W
; Q:= Q - WRK1
; WRKO,Q:= (WPRE - WRKU-1+C,Q) SHIFT (-1)
; LCD:= -WRK2-1
; GOTO NORMALIZE
; FS-REG:
; WRK1:= LAX
; SB:= LAST:= SB-2
; WRK2:= 12 EXT WRK1(12) CON WRK1(12:23)
; WRK1:= WRK1(U:11) CON 12 EXT 0
; WRKU:= LAX
; GOTO FS1

; FSEQ:
; ZAG OR,W
; AGQ SUNO,WRK1
; DRABB SUNC,WRKU,WPRE,SGN,NL,F,F
; ZAR SUB,WRK2,LCD
; JMP F,F,NORMA
; FSREG:
; XZAB CDATI,OR,LAX,WRK1
; ABBR SUNO,.2,SB,LAST
; HZB OR,EXT,WRK1,WRK2
; CAB CAND,B12,WRK1,WRK1
; ZAB OR,LAX,WRKU
; JMP F,F,FS1

; FLOATING SUBTRACT, FS, WHEN THE FPU801 IS PRESENT
; FSFPE:
; IZQ OR,0,6112
; CALL F,F,ESCAP
; FSFPU:
; ZAR OR,W,FPOP1
; ZAR OR,WPRE,FPOPO
; JMP F,NWADR,FPURG
; JMP F,F,FPUP1

```

## 10042 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

; RETURN FROM ESCAPE, RE, OPCODE = 26

```

01 ;
02 ;
03 ;
04 REE:    IZQ   OR,0,2632
05 01347 0624730000054640  CALL  F,F,ESCAP
06 01350 06310000020C0031
07 RE:
08 01351 0631000000130032  JMP   F,NWADR,INTU
09 01352 0657730020514000  XZB  OR,DATI,W0
10 01353 06471014262C0011  ABBR ADD,.2,SB,LAD
11 01354 4657730021514000  XZB  OR,DATI,W1
12 01355 06471014262C0011  ABBR ADD,.2,SB,LAD
13 01356 4657730022514000  XZB  OR,DATI,W2
14 01357 06471014262C0011  ABBR ADD,.2,SB,LAD
15 01360 0657730023514000  XZB  OR,DATI,W3
16 01361 06471014262C0011  ABBR ADD,.2,SB,LAD
17 01362 4657730024514000  XZB  OR,DATI,STAT
18 01363 06471014262C0011  ABBR ADD,.2,SB,LAD
19 01364 4657730033514000  XZB  OR,DATI,WRK1
20 01365 06471014262C0011  ABBR ADD,.2,SB,LAD
21 01366 0657730025514000  XZB  OR,DATI,CAUSE
22 01367 06471014262C0011  ABBR ADD,.2,SB,LAD
23 01370 0657730034514000  XZB  OR,DATI,WRK2
24 01371 4607431626000000  ZAB  OR,WRK2,SB
25 01372 0605431540240011  ZAR  OR,WRK1,LAD
26 01373 0631000000561375  JMP  T,MMODE,RGSET
27 01374 0613551224003500  CAB  CAND,.180,STAT,STAT
28 01375 0613551224000005  RGSET: CABR  CAND,B1214,STAT,STAT,CPUST
29 ;
30 01376 0631000000130032  JMP  F,NWADR,INTU
31 01377 0605431540000003  ZAR  OR,WRK1,ICD
32 01400 7455730000610100  XZ   OR,CIOST
33 ;
;REE:
; ; CALL ESCAPE(2632)
;RE:
; IF W-ADDR THEN GOTO INTU
; W0:= DAT1, TSTIO;
; LAD,SB:= SB+2, READP;
; W1:= DAT1, TSTIO;
; LAD,SB:= SB+2, READP;
; W2:= DAT1, TSTIO;
; LAD,SB:= SB+2, READP;
; W3:= DAT1, TSTIO;
; LAD,SB:= SB+2, READP;
; STAT:= DAT1, TSTIO;
; LAD,SB:= SB+2, READP;
; WRK1:= DAT1, TSTIO;
; LAD,SB:= SB+2, READP;
; CAUSE:= DAT1, TSTIO;
; LAD,SB:= SB+2, READP;
; WRK2:= DAT1, TSTIO;
; LAD,SB:= SB+2, READP;
; LAD:= WRK1, READ INSTRUCTION
; IF NOT MONMODE THEN
; CLEAR MONMODE
; STAT:=CPUST:= STAT CAND 817000
; C. REMOVE DUMPERRCOUNT
; IF W-ADDR THEN GOTO INTU
; ICD:= WRK1
; TEST LIMIT, TEST INT
; GOTO NEXT INSTR

```

10043 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 ;BUS INTERRUPTS
02 ;*****
03 ;*****
04 ;*****
05 ;PFINT: C. PREFETCH INTERRUPT
06 ;FINT:
07 01401 4613730025001300 CAUSE:= 10
08 01402 4617730032004100 WRKO:= IOSTATUS
09 01403 4624730000070000 WRKO:= WRKU(13:15) C. PF-STATUS
10 01404 4607041532000000 WRKO:= 21 EXT 0 CON WRKU(13:15)
11 01405 0251712000003404 CLEAR EX BUT BIT 16,18,20
12 01406 6635330032110060 STAT:= STAT OR WRKO
13 01407 4624731777605200 GOTO TEST-INT
14 01410 4607041224000000
15 01411 0607131524000000
16 01412 463100000001431
17
18
19 01413 4653503626003700 INTB2: C. ERROR IN REGISTER DUMP
20 01414 463100000001423 SB:= WRK2+2 C. DUMPADDRESS
21
22 01415 46115412000003200 INTBB: C. BUS INTERRUPT FROM TEST
23 01416 0631000000021423 IF AFTER AM=1 THEN
24 01417 46455014000001003 IC:= IC+2
25 01420 46310000000001423 C. ERROR IN INDIRECT ADDRESS CALC
26
27
28 01421 0607431366000000 GOTU INIB
29 01422 4653513367001100 INTB3 C. ERROR IN FETCH OF SERVICE
30
31 01423 4617730032004100 ; SB:= INF
; INF:= INF-12
; INTB:
; WRKO:= IOSTA

```

PUSH  
LRTN

OR, 10, CAUSE  
OR, IOSTA, WRKO  
OR, 0, 3400  
AND, WRKO, WRKO  
SUNO, 7, LCD  
OR, WRKO, Z, NL, F, LCN  
OR, 7777, 250  
AND, STAT, STAT  
OR, WRKU, STAT  
F, F, TESTI

AD90, 1, WRK2, SB  
F, F, INTB  
AND, 1B2, STAT  
F, NZ, INTB  
ADD, ICS, 2, ICD  
F, F, INTB  
OR, INF, SB  
SUNO, 12, INF, INF  
OR, IOSTA, WRKO

INTB2:  
INTBB:  
INTB3:  
INTB:

CZB  
XZB  
IZQ  
AGB  
CZR  
SRZBB  
IZQ  
AGB  
ABB  
JMP  
CAB  
JMP  
CA  
JMP  
RAR  
JMP  
ZAB  
CAB  
XZB

10044 CPU823 MICROPROGRAM LBJ 850016 VERSION 8

```

01 ;INTB1:
02 ;
03 ;INTF: C. ENTRY FROM FPU
04 ; CAUSE:= 8
05 01424 06137300250C3600 OR,8,CAUSE
06 01425 4624731777605200 OR,7777,250
07 01426 4607041224000000 AND,STAT,STAT
08 01427 0613541532003400 AND,.7,WRKU,WRKU
09 01430 0607131524000000 OR,WRKU,STAT
10 TESTI:
11 01431 0601730000000400 OR,ILIMC
12 01432 4631000000421434 T,NZ,ICALL
13 01433 4631000000001767 F,F,SYFLO

```

EXTERNAL INTERRUPTS AND MONITOR CALLS  
 \*\*\*\*\*

```

14
15
16
17
18
19 01434 0631000002001533 CALL F,F,LOKM
20 01435 0607131524000000 ABB OR,WRKU,STAT
21 01436 0605431340210011 ZAR OR,INF,LAD
22 01437 4600730000000515 SZQR OR,SIZE,ULIM
23 01440 0641220000000302 ZQS SUB,ULIMC
24 01441 4610730000003614 CZQR OR,.8,LLIM
25 01442 4641220000000202 ZQS SUB,LLIMC
26 01443 4604240000000013 ZQQR AND,BASE
27 01444 4641220000000102 ZQS SUB,BASEC
28 01445 0657730036004000 XZB OR,DATI,WRK4
29 01446 0615541740004100 XA AND,I0STA,WRK5
30 01447 4631000000420034 JMP T,NZ,SYFL1
31 01450 0611541200002100 CA AND,.1B16,STAT
32 01451 4631000000421462 JMP T,NZ,ICAL2
33 01452 4645101427210011 ABR ADD,.2,INF,LAD
34 01453 4653513375001400 CAB SUN0,.3,INF,WRK3
35 01454 4653513367001100 CAB SUN0,.12,INF,INF

```

```

;INTB1:
;
;INTF: C. ENTRY FROM FPU
; CAUSE:= 8
; CLEAR EX BUT BIT 16,18,20
; WRKU:= 21 EXT U CON WRKU(21:23)
; STAT:= STAT OR WRKU
;TEST-INT:
; IF ILIMC<>U THEN
; GOTO INTERRUPTCALL
; ELSE GOTO SYSTEMFAULT0

```

```

;INTERRUPTCALL:
; LOCKMONITOR;
; STAT:= STAT OR WRKU; UNLOCK
; LAD:= INF, READ;
; ULIM:= SIZE
; ULIMC:= SIZE
; LLIM:= 8
; LLIMC:= 8
; BASE:= U
; BASEC:= 0
; WRK4:= DAIN C. NEW STATUS
; IF IOSTATUS(21:23)<>0 THEN
; GOTO SYSTEMFAULT1
; IF STAT(16) = 0 THEN
; BEGIN < * NOT USER MODE * >
; LAD:=INF+2, READ
; WRK3:= INF - 3 C. NEW IC
; INF:= INF-12

```

CONT READ  
 CONT 0 1  
 CONT READ



10045 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 01455 46135313670C3700      CAB      OR,.1,INF,INF      CONT 0 2
03 01456 0717523434CC4000      XAB      SUBO,DAI,.2,WRK2
04 01457 0615541740CC4100      XA       AND,IOSTA,WRK5
05 01460 4631000000421524      JMP      T,NZ,DMPFL
06 01461 0631000000C1471      JMP      F,F,ICAL3
07
08 01462 46405234000C1300      SAQ      SUBO,DMOFS,.2
09 01463 06436000340C0700      SOB      ADD,CUR,WRK2
10 01464 4653513375CC1400      CAB      SUNO,.3,INF,WRK3
11 01465 4653513367CC1100      CAB      SUNO,.12,INF,INF
12 01466 4613531367CC3700      CAB      OR,.1,INF,INF
13 01467 0611541440CC1500      CA       AND,.4,CNTR
14 01470 0631000000421502      JMP      T,NZ,ICAL5
15
16 01471 0624730000034320      IZQ      OR,0,GRTB/3-1
17 01472 0207431032CC0000      ZAB      OR,WU,WRKU      PUSH
18
19 01473 4605431500000012      ZAR      OR,WRKU,DATO
20 01474 0647101434230011      ABBR     ADD,.2,WRK2,LAD      CONT WRT
21 01475 4630202003000000      CXZQQ    ADDO,F,F
22 01476 0655541740000100      XA       AND,CIOST,WRK5      CONT U 1
23 01477 2631000000421524      JMP      T,NZ,DMPFL      LRTN
24
25
26 01500 0655541740000100      XA       AND,CIOST,WRK5      CONT U 1
27 01501 4631000000421524      JMP      T,NZ,DMPFL
28
29 01502 0605431640210011      ZAR      OR,WRK3,LAD      CONT READ
30 01503 4610541700000321      CAQR     AND,B12,WRK4,ILIM
31 01504 4641220000000402      ZQS      SUB,ILIMC
32 01505 0613551733000305      CABR     CAND,B12,WRK4,WRK1,CPUST
33 01506 46105412000001700      CAQ      AND,.1B18,STAT

```

```

; INF:= INF OR 1
; WRK2:= DATAIN - 2 C. DUMP ADDR
; IF IOSTATUS(21:23)<>0 THEN
;   GOTO DUMPFFAULT
; END ELSE
; BEGIN
;
; WRK2:= CUR + DUMP OFFSET - 2;
; WRK3:= INF - 3; NEW IC
; INF:= INF - 12;
; INF:= INF OR 1;
; IF CNTR(21)<>0 THEN <*NOT RUN*>
;   SKIP REGISTER DUMP
; END;
; Q:= ADDR(GRTB-1)
; FOR I:= U TO 8 DO
;   BEGIN
;     DATAOUT:= WRKO
;     WRK2,LAD:= WRK2, WRT;
;     WRKU:=GETREG(Q+1), Q:= Q+1
;     IF CPUIOSTATUS(21:23)<>0 THEN
;       GOTO DUMPFFAULT
;     END
;   ;
; IF IOSTATUS(21:23)<>0 THEN
;   GOTO DUMPFFAULT
;
; LAD:= WRK3 , READ
; ILIM:= WRK4(12:23)
; ILIMC:= WRK4(12:23)
; WRK1:=CPUST:= WRK4(U:11) CON 12 EXT 0
; Q:= STAT(18); SAVE PROCESS ACTIVE

```

10046 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 01507 4655541740004100      XA  AND,I0STA,WRK5      CONT 0 1
03 01510 4631000000421524      JMP  T,NZ,DMPFL
04 01511 0617730032244011      XZBR OR,DATI,WRKO,LAD  CONT READJ
05 01512 0631000000130032      JMP  F,NWADR,INTO
06 01513 4605431500000003      ZAR  OR,WRKU,ICD
07 01514 0607431262000000      ZAB  OR,CAUSE,W2
08 01515 0613541232000000      CAB  AND,B1214,STAT,WRKO
09 01516 0631000000021521      JMP  F,NZ,DMPOK
10 01517 0607131533000000      AEB  OR,WRKO,WRK1
11 01520 0613730022001500      CZB  OR,.4,W2
12
13 01521 0607031564000000      DMPOK: AQB  OR,WRK1,STAT
14 01522 4613551471001506      CABR  CAND,.4,CNTR,CNTR,CNTR
15
16 01523 7407431621000000      ZAB  OR,WRK2,W1      NEXT
17
18
19 01524 4653501224002200      DMPFL: CAB  ADD,.1B14,STAT,STAT
20 01525 4610541700000300      CAQ  AND,B12,WRK4
21 01526 4641220000000402      ZQS  SUB,ILIMC
22 01527 0613551224002100      CAB  CAND,.1B16,STAT,STAT
23 01530 0611541200002200      CA  AND,.1B14,STAT
24 01531 0631000000021773      JMP  F,NZ,SYFL2
25 01532 46310000000001431      JMP  F,F,TESTI

; IF IOSTATUS(21:23)<>0 THEN
; GOTO DUMPFAULT
; LAD:=WRKO:=DATI , READ INSTRUCTION
; IF W-ADDR THEN GOTO INTO
; ICD:= WRKU
; W2:= CAUSE
; IF DUMPERRORCOUNT <> U THEN
; W2:= 4
; WRK1:= WRK1 OR STAT(12..14);
;
;DUMPOK:
; STAT:= WRK1 OR Q;
; CNTR(21),CNTR(21):= U;
; < * SET RUN LAMP AND FLAG * >
; W1:= WRK2 C. TOP DUMP ADDR
; GOTO NEXT INSTR
;DUMPFAULT:
; INCREASE DUMPERRORCOUNT
;
; ILIMC:= WRK4(12:23) NEW ILIM
; STAT(16):= Q; CLEAR USERMODE
; IF DUMPERROR OVERKUN THEN < * > 1 * >
; GOTO SYSTEMFAULT2
; ELSE GOTO TEST-INT

```

10047 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 01533 4613730037CC3400
04
05 01534 0611730000CC1424
06 01535 4653513375213011
07 01536 4607240032CC0000
08 01537 0601730000CC0712
09 01540 0654730000CC04000
10 01541 4631000000421547
11 01542 4611730000CC03724
12 01543 4605431640230011
13 01544 0655541740000100
14 01545 4631000000421556
15 01546 20052400000000024
16
17 01547 46117300000003724
18 01550 4605240000210011
19
20 01551 06416220000000700
21 01552 4631000000421560
22 01553 4613730032002000
23 01554 06557300000004100
24 01555 20052400000000024
25
26
27 01556 46052400000000024
28 01557 46310000000000034

; LOCKMONITOR;
; WRK5:= 7
;
; SE1 BYPASSCAM AND BUSBUSY
; LAD,WRK3:= INF-6, READ;
; CLEAR NOT-UNLOCK CONDITION;
; DATO:= CUR;
; Q:= DATI, TEST BUSERROR;
; IF Q <> 0 THEN GOTO LOKM2;
; CAMBC:= 1; CLEAR BUSBUSY;
; LAD:= WRK3, WRT;
; IF IOSTAT(21:23) <> 0 THEN
;   GOTO ICER1;
; CAMBC:= 0, RETURN;
;
; CAMBC:= 1;
; LAD:= U, HEAD;
;
; IF Q <> CUR THEN
;   GOTO ICWT;
; SET NOT-UNLOCK CONDITION;
; WAIT IO;
; CAMBC:= 0, RETURN;
;
; CAMBC:= 0;
; GOTO SYSTEMFAULT1

; LOCKMONITOR;
; WRK5:= 7
;
; SE1 BYPASSCAM AND BUSBUSY
; LAD,WRK3:= INF-6, READ;
; CLEAR NOT-UNLOCK CONDITION;
; DATO:= CUR;
; Q:= DATI, TEST BUSERROR;
; IF Q <> 0 THEN GOTO LOKM2;
; CAMBC:= 1; CLEAR BUSBUSY;
; LAD:= WRK3, WRT;
; IF IOSTAT(21:23) <> 0 THEN
;   GOTO ICER1;
; CAMBC:= 0, RETURN;
;
; CAMBC:= 1;
; LAD:= U, HEAD;
;
; IF Q <> CUR THEN
;   GOTO ICWT;
; SET NOT-UNLOCK CONDITION;
; WAIT IO;
; CAMBC:= 0, RETURN;
;
; CAMBC:= 0;
; GOTO SYSTEMFAULT1

```

10048 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 01560 06247300002000200
04 01561 0605230000000021
05 01562 0655730000004100
06 01563 4605240000000024
07 01564 0607151.431000006
08 01565 0745230000000000
09 01566 0745230000000000
10
11 01567 0631000002611576
12 01570 0651513340213011
13 01571 0631000002601614
14
15 01572 4655730000004000
16 01573 063100000421567
17 01574 0607131.431000006
18 01575 0631000000001534
19
20
21 01576 0605230000000022
22 01577 4705230000000000
23 01600 4705230000000000
24 01601 4724730000007760
25 01602 4614640000000320
26 01603 06516220000003600
27 01604 603100000021605
28 01605 0605230000000017
29 01606 4705230000000000
30 01607 0615730000000212
31 01610 0600730000001100
32 01611 46406000000001000
33 01612 06516000000231311
34 01613 20557300000000100
35
36
37 01614 46310000000254002
38 01615 60310000000264003
39
; ICWT:
; ILIM:= 8;
; DELAY 900 NSEC BEFORE INTERRUPT TEST
; SYNC IO
; CAMBC:= 0;
; CNTR(22),CNTR0(22):= U; SET WAIT LAMP
;
; DELAY
; REPEAT
; IF EXTINT THEN ICEXT
; LAD:= INF-6, READ;
; IF CPUINTERRUPT THEN CALL ICINT;
; C. ALWAYS ENABLE CPU_INTERRUPT;
; SYNC IO;
; UNTIL DATI=0;
; CNTR,CNTR0(22):= 1; CLEAR WAITLAMP
; GOTO LOCKMONITOR
;
; TEST EXTERNAL INTERRUPT 8
; ILEVR:= Q; REQUEST ILEV
; DELAY;
;
; INTRG,Q:= ILEV AND 8'377
; IF Q<>8 THEN
; RETURN
; RTCR:= Q; REQUEST RTC
; DELAY
; DATO:= RTC;
;
; LAD:= PUTAB+PUIX+10, WRT;
; WAIT IO
;
; ONLY AUTOLOAD INTERRUPTS ARE ALLOWED
; IF OCPAL THEN GOTO OCPAUTOLOAD;
; IF REMAUTO THEN GOTO REMOTEAUTOLOAD
; ELSE RETURN

```

CONT 0 1

CONT 0 3  
CONT 0 3

CONT READ

CONT 0 1

CONT 0 2  
CONT 0 2  
CONT 2

RTN

CONT 0 2

CONT WRT  
RTN 0 1

RTN

ICWT:

ICLCP:

ICEXT:

ICINT:

IZQ OR,8,,8.  
ZQR OR,ILIM  
XZ OR,IOSTA  
ZQR AND,CAMBC  
ABBR CAND,,2,CNTR,CNTR0  
ZQ OR  
ZG OR

CALL T,EXINT,ICEXT  
CAR SUNO,,6,INF,LAD  
CALL T,CPUIN,ICINT  
XZ OR,DATI  
JMP T,NZ,ICLCP  
ABBR OR,,2,CNTR,CNTR0  
JMP F,F,LOKM1

ZQR OR,ILEVR  
ZQ OR  
ZG OR  
IZQ OR,0,377  
XQQR AND,ILEV,INTRG  
CQ SUBO,,8  
JMP F,NZ,,+3  
ZQR OR,RTCR  
ZG OR  
XZR OR,RTC,DATO  
SZQ OR,PUTAB  
SQQ ADU,PUIX  
CQR ADD,,10,LAD  
XZ OR,CIOST

JMP F,NOCPA,OCPAL  
JMP F,NREMA,REMA

10049 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 01616 6007431072000000
04 01617 6007431132000000
05 01620 2007431172000000
06 01621 6007431232000000
07 01622 2007730032001000
08 01623 2007431272000000
09 01624 2007431332000000
10 01625 0655541740000100
11 01626 463100000421524
12 01627 0601730000001012
13 01630 0653501634231111
14 01631 4631000000001500
15

;GRTB:
; 1: WRKO:= W1;
; 2: WRKU:= W2;
; 3: WRKU:= W3;
; 4: WRKU:= STAT;
; 5: WRKU:= IC;
; 6: WRKU:= CAUSE;
; 7: WRKU:= SB;
; 10: IF CIOS1(21:23)<> 0 THEN
;      GOTO DUMPPFAULT;
;      DATO:= PUIX;
;      WRK2,LAD:= WRK2+12, WRT;
;      < * DUMP PU INDEX * >
;      GOTO ICAL3;

RTN
RTN
RTN
RTN
RTN
RTN
RTN
CONT 0 1

OR,W1,WRKO
OR,W2,WRKO
OR,W3,WRKO
OR,STAT,WRKO
OR,ICS,WRKO
OR,CAUSE,WRKU
OR,SB,WRKO
AND,CIOST,WRK5
T,NZ,DMPFL
OR,PUIX,DATO
ADD,.12,WRK2,WRK2,LAD
F,F,ICAL4

ZAB
ZAB
ZAB
ZAB
RZB
ZAB
ZAB
XA
JMP
SZR
CABR
JMP

CONT WRT

;GRTB:
; 1: WRKO:= W1;
; 2: WRKU:= W2;
; 3: WRKU:= W3;
; 4: WRKU:= STAT;
; 5: WRKU:= IC;
; 6: WRKU:= CAUSE;
; 7: WRKU:= SB;
; 10: IF CIOS1(21:23)<> 0 THEN
;      GOTO DUMPPFAULT;
;      DATO:= PUIX;
;      WRK2,LAD:= WRK2+12, WRT;
;      < * DUMP PU INDEX * >
;      GOTO ICAL3;

```

10050 CPU823 MICROPROGRAM LBJ 850016 VERSION 8

```

01
02
03
04
05
06
07 01632 0607230025000000
08 01633 4611541200003100
09 01634 463100000021637
10 01635 4613551224003105
11 01636 6005330026000000
12
13 01637 0662731234000600
14 01640 0607141274000000
15 01641 0624730000001760
16 01642 4607041634000000
17 01643 0631000000421645
18 01644 6005330026000000
19 01645 0655730000004100
20 01646 0611541200002100
21 01647 0631000000021653
22 01650 0640501400001200
23 01651 0641600000210711
24 01652 0631000000001654
25
26 01653 0651501340213011
27
28 01654 4613531224003100
29 01655 4624731000177760
30 01656 4607230036000000
31 01657 4645523400001003
32 01660 0631000000001703

;PROCEDURE ESCAPE
;*****
;ESCAP: C. Q=CAUSE
;SCAP:
ESC1:
ESC2:
ESC21:
ESC22:
ZQB OR,CAUSE
CA AND,.1B3,STAT
JMP F,NZ,ESC1
CABR CAND,.1B3,STAT,STAT,CPUST
ZB OR,SB
HZB OR,LSWAP,STAT,WRK2
ABB AND,CAUSE,WRK2
I2Q OR,U,77
AQB AND,WRK2,WRK2
JMP T,NZ,ESC2
ZB OR,SB
XZ OR,I,OSTA
CA AND,.1B16,STAT
JMP F,NZ,ESC21
SAG ADD,EXOFS,.2
SQR ADD,CUR,LAD
JMP F,F,ESC22
CAR ADD,.6,INF,LAD
CAB OR,.1B3,STAT,STAT
I2Q OR,400U,7777
ZQB OR,WRK4
RAR SUBO,ICS,.2,I2C
JMP F,F,EXC3
;ESCAPE: C. Q=CAUSE
;
; CAUSE:= Q
; IF AFTER ESCAPE THEN
; STAT:=CPUST:= STAT CAND '1B3
; C. CLEAR AFTER ESCAPE
; SET SB CONDITIONS
; RETURN
; WRK2:= 12 EXT 0 CON STAT(0:11)
; WRK2:= WRK2 AND CAUSE
;
; WRK2:= WRK2 AND 8'77
; IF WRK2=0 THEN
; SET SB CONDITIONS, RETURN
; WAIT FOR OPERAND FETCH
; IF STAT(16)<>U THEN
; < * USER MODE * >
;
; LAD:= CUR+EXOFS+2, READ;
; ELSE
;
; LAD:= INF+6 , READ
;
; STAT:= STAT OR 1B3 C. AFTER ESC
;
; WRK4:= 8'400C7777 C. MASK
; ICD:= ICS -2
; GOTO EXC3

```

10051 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 01661 0607340025000000
04 01662 4611541200003200
05 01663 063100000021672
06 01664 4645501400001003
07
08 01665 0607340025000000
09 01666 4631000000001672
10
11
12
13 01667 4613730025001500
14 01670 4631000000001672
15
16
17 01671 0607431425000000
18
19
20 01672 0611541200002100
21 01673 063100000021677
22 01674 0600730000001200
23 01675 0641600000210711
24 01676 063100000001700
25
26 01677 4651501340211511
27
28 01700 4613551224003100
29 01701 0624731477777760
30 01702 4607230036000000
31
32 01703 4613730037003400
33 01704 4655541740004100
34 01705 0631000000421421
35 01706 0617730034004000
36 01707 0631000000421714

;INT00: C. ENTRY FROM TEST
; CAUSE:= 0
; IF AFTER AM=1 THEN
; IC:=IC+2
; C. EXCEPTION IN INDIRECT ADDRESS CALC.
;INT0:
; CAUSE:= 0
; GOTO EXCEPTION
;
;
;FLOATING POINT EXCEPTION:
; CAUSE:= .4;
;
;
;INTEGER EXCEPTION:
; CAUSE:= 2
;
;EXCEPTION:
; IF STAT(16)<>0 THEN
; <★ USER MODE ★>
;
; LAD:= CUR+EXOFS, READ;
; ELSE
;
; LAD:= INF+4 , READ
;
; CLEAR AFTER ESCAPE
; WRK4:= 8'63777777 C. MASK
;
;COMMON EXC AND ESCAPE
; WRK5:= 7
; IF IOSTATUS(21:23)<>0 THEN
; GOTO INTB3
; WRK2:= DATAIN
; IF DUMPADDRESS=0 THEN

```

INT00:

ZBB AND,CAUSE  
CA AND,.1B2,STAT  
JMP F,NZ,EXCEP  
RAR ADD,ICS,.2,ICD

INT0:

ZBB AND,CAUSE  
JMP F,F,EXCEP

INTF:

CZB OR,.4,CAUSE  
JMP F,F,EXCEP

INT1:

ZAB OR,.2,CAUSE

EXCEP:

CA AND,.1B16,STAT  
JMP F,NZ,EXC1  
SZQ OR,EXOFS  
SGR ADD,CUR,LAD  
JMP F,F,EXC2

EXC1:

CAR ADD,.4,INF,LAD

EXC2:

CAB CAND,.1B3,STAT,STAT  
IZQ OR,6377,7777  
ZQB OR,WRK4

EXC3:

CZB OR,.7,WRK5  
XA AND,IOSTA,WRK5  
JMP T,NZ,INTB3  
XZB OR,DATI,WRK2  
JMP T,NZ,EXC4

CONT READ

CONT READ

CONT 0 1

10052 CPU823 MICROPROGRAM LBJ 850016 VERSION 8

```

01
02
03 01710 4651513240003600      CA      SUN0,08,CAUSE
04 01711 4631000000411431      JMP     T,NEG,TESTI
05 01712 0607340025000000      ZBB    AND,CAUSE
06 01713 4631000000001431      JMP     F,F,TESTI
07
08 01714 4613730037000700      CZB    OR,015,WRK5
09 01715 0624730000037340      IZQ    OR,0,GRTB1/3-1
10 01716 4607431032000000      ZAB    OR,W0,WRK0
11 01717 4207113434000000      ABB    SUN0,02,WRK2
12
13
14 01720 4605431500000012      ZAR    OR,WRK0,DATO
15 01721 4647101434220011      ABBR   ADD,02,WRK2,LAD
16 01722 4630202003000000      CXZQQ  ADD0,F,F
17 01723 4657541775000100      XAB    AND,CIOST,WRK5,WRK3
18
19 01724 6631000000421753      JMP     T,NZ,TSTEX
20
21
22 01725 4657541775000100      XAB    AND,CIOST,WRK5,WRK3
23 01726 0631000000421753      JMP     T,NZ,TSTEX
24 01727 0607340020000000      ZBB    AND,W0
25 01730 4607431421000000      ZAB    OR,02,W1
26 01731 0613730022001500      CZB    OR,04,W2
27 01732 0613730023003000      CZB    OR,06,W3
28 01733 4607430035000000      ZAB    OR,W,WRK3
29 01734 0624730000001760      IZQ    OR,0,77
30 01735 4607041263000000      AQB    AND,CAUSE,W3

```

```

; BEGIN
; IF ESCAPE THEN
;   CAUSE:= C C. CAUSE > 8
;   GOTO TEST-INT
; END
; WRK5:= 15
; Q:= ADDR(GRTB-1)
; WRK0:= WU;
; WRK2:= WRK2-2;
; FOR I:= 0 TO 7 DO
; BEGIN
;   DATAOUT:= WRK0
;   WRK2,LAD:= WRK2+2, WRTP;
;   WRKU:= GETREG(Q+1), Q:= Q+1
;   WRK3:= CPUIOSTATUS(20:23)
;   IF IOSTATUS(20:23)<>0 THEN
;     GOTO TEST-EX;
; END
;
; IF CIOST(21:23)<>0 THEN
;   GOTO TEST-EX;
; WU:= 0
; W1:= 2
; W2:= 4
; W3:= 6
; WRK3:= W C. W-FIELD < 1
; W3:= CAUSE AND 8'77

```

```

PUSH
CONT WRTP
CONT 0 1
LRTN

```

```

CONT 0 1

```



10053 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 C1736 0635330025000060
04 C1737 0635330025000060
05 C1740 0635330025000060
06 C1741 4613551265003400
07 C1742 4635131665000060
08 C1743 4607730020001000
09 C1744 0607431261000000
10 C1745 0607431322000000
11 C1746 4647101434240011
12 C1747 0607141724000005
13 C1750 0631000000130032
14 C1751 4605431600000003
15 C1752 7455730000610100
16
17
18 01753 0611541640003600
19 01754 0631000000021413
20
21 01755 0613730025003000
22 01756 4631000000001431

; CAUSE:= CAUSE(0:17) CON 6 EXT 0
;
;
; CAUSE:= CAUSE SHIFT (-4) + W-FIELD
; WU:= ICS
; W1:= CAUSE
; W2:= SB
; LAD,WRK2:= WRK2+2, READ INSTRUCTION;
; STAT:=CPUST:= STAT AND MASK
; IF W-ADDR THEN GOTO INTU
; ICD:= WRK2
; TEST LIMIT, TEST INT
; GOTO NEXT INSTR
;TEST-EX:
; IF IOSTATUS(20)=0 THEN
;   GOTO INTB2
; ELSE
;   CAUSE:= 6
;   GOTO TEST-INT
;

SRZBB OR,CAUSE,Z,NL,F,F
SRZBB OR,CAUSE,Z,NL,F,F
SRZBB OR,CAUSE,Z,NL,F,F
CAB CAND,.7,CAUSE,CAUSE
SRABB OR,WRK3,CAUSE,Z,NL,F,F
RZB OR,ICS,W0
ZAB OR,CAUSE,W1
ZAB OR,SB,W2
ABBR ADD,.2,WRK2,LAD
ABBR AND,WRK4,STAT,CPUST
JMP F,NWADR,INTO
ZAR OR,WRK2,ICD
XZ OR,CIOST

CONT READJ
NEXT TLIIN 1

TSTEX:
CA AND,.1B20,WRK3
JMP F,NZ,INTB2
CZB OR,.6,CAUSE
JMP F,F,TEST1I

```

10054 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```
01
02
03 01757 6007431072000000
04 01760 6007431132000000
05 01761 2007431172000000
06 01762 6007431232000000
07 01763 2007730032001000
08 01764 2007431272000000
09 01765 2007431332000000
10 01766 46310000000001725

      OR,W1,WK0
      OR,W2,WK0
      OR,W3,WK0
      OR,STAT,WRKU
      OR,ICS,WRKU
      OR,CAUSE,WRKU
      OR,SB,WRKU
      F,F,EXC5

      ZAB
      ZAB
      ZAB
      ZAB
      RZB
      ZAB
      ZAB
      JMP

GRTB1:
; 1: WRKU:= W1;
; 2: WRKU:= W2;
; 3: WRKU:= W3;
; 4: WRKU:= STAT;
; 5: WRKU:= IC;
; 6: WRKU:= CAUSE;
; 7: WRKU:= SB;
; GOTO EXC5;

RTN
RTN
RTN
RTN
RTN
RTN
RTN
```

10055 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```
01
02
03 ;SYSTEMFAULT U-2
04 ;*****
05 SYFLO: IZQ OR,0,0 ; SYSTEMFAULT CAUSE 0
06 01767 062473000000000000 ; TEST INTERRUPT, ILIM=0
07 01770 463100000000000000 JMP F,F,SYFLT ;
08
09 ;SYFL1:
10 .YFL1: IZQ OR,0,1 ; SYSTEMFAULT CAUSE 1
11 01771 462473000000000000 ; BUSERROR IN FETCH OF NEW STATUS
12 01772 463100000000000000 JMP F,F,SYFLT ; AT INTERRUPT
13
14 SYFL2: IZQ OR,0,2 ; SYSTEMFAULT CAUSE 2
15 01773 462473000000000000 JMP F,F,SYFLT ; DUMPERROR OVERRUN
16 01774 463100000000000000 ;
```

```

10056 CPU623 MICROPROGRAM LBJ 850816 VERSION 8
01
02
03
04
05
06
07
08
09
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
26
27
28
29
30
31
32
33
; GOTO CHECK CLOCK;
; GOTO DISPATCHING POINT

; FILE: T2, EDITED: 850814/LBJ

CC0001 .NOMA 1
CC6000 .LOC 20CC*3

; STEPPING STONES
      JMP F,F,.HCLK
      JMP F,F,.ISPA

; ADDRESS CALCULATION ROUTINES
;*****
; THE ADDRESS CALCULATIONS ARE CALLED VIA A SEPARATE TABLE
; INPUT TO THE TABLE ARE:
; AFTER ESCAPE = 1, ESCAPE = 1
;*****

AFTES:  ZAR  OR,SB,LAD          EXEC READC          ; LAD:= SB, COND READP
24 C2002 3605431300100011          ; GOTO INSTR EXECUTION

; AFTER ESCAPE = 1, ESCAPE = 0
;*****
AENES:  CABR CAND,.1B3,STAT,STAT,CPUST EXEC READC          ; AFTER ESCAPE:= 0
31 C2003 4613551224003105          ; LAD:= SB, COND READP
32 C2004 3605431300100011          ; GOTO INSTR EXECUTION

```

10057 CPU823 MICROPROGRAM LBJ 850816 VERSION B

```

01
02
03 ;AFTER ESCAPE = 1, AFTER AM = 1
04 ;*****
05 INESC:      ZAR      OR,SB,LAD          CONT READP
06 C2005 06054313002C0011  CABR      CAND,.1B3,STAT,STAT,CPUST
07 C2006 4613551224CC3105  JMP      F,NWADR,REGAD
08 C2007 063100000132027  XZ       OK,IOSTA
09 C2010 4655730000514100  CABR      CAND,.1B2,STAT,STAT,CPUST
10 C2011 4613551224C03205  XZBR     OK,DATI,SB,LAD
11 C2012 76177300261C4011
12
13
14 ;AFTER AM = 0, INDIRECT = 0, EXCAPE = 0
15 ;*****
16
17 DIR:      RZBR     OR,DISP,SB,LAD      EXEC READC
18 C2013 76077300261C0011
19
20
21 REL:      RZBR     OR,DIC,SB,LAD       EXEC READC
22 C2014 36077300261C2011
23
24
25 INDEX:   RABR     ADD,DISP,X,SB,LAD   EXEC READC
26 C2015 36475001261C0011
27
28
29 RELX:    RABR     ADD,DIC,X,SB,LAD    EXEC READC
30 C2016 76475001261C2011
31

```

```

; LAD:= SB , READP
; AFTER ESCAPE:= 0
; IF W-ADDR THEN GOTO REG. ADDR
; TEST IO
; AFTER AM:= 0
; SB:=LAD:=DATAIN, COND READP
; GOTO INSTR EXEC

```

```

;DIRECT:
; SB:=LAD:=D, COND READP
; GOTO INSTR EXEC

```

```

;RELATIVE:
; SB:=LAD:= D+IC, COND READP
; GOTO INSTR EXEC

```

```

;INDEX:
; SB:=LAD:= D+X , COND READP
; GOTO INSTR EXEC

```

```

;RELATIVEINDEX:
; SB:=LAD:= D+IC+X , COND READP
; GOTO INSTR EXEC

```

10058 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03
04
05
06 C2017 46077300262C0011
07 C2020 463100000132025
08 C2021 46135312240C3205
09 C2022 4655730000514100
10 C2023 4613551224003205
11 C2024 7617730026104011
12
13
14
15
16 C2025 46557300000C0100
17 C2026 76074301661C0011
18
19
20 C2027 46557300000C0100
21 C2030 46135512240C3205
22 C2031 76074301661C0011
23
24
25
26 C2032 06077300262C2011
27 C2033 463100000132025
28 C2034 46135312240C3205
29 C2035 4655730000514100
30 C2036 46135512240C3205
31 C2037 7617730026104011
32

;AFTER AM = 0, INDIRECT = 1, ESCAPE = 0
;*****

DIRI:
RZBR OR,DISP,SB,LAD          CONT READP
JMP  F,NWADR,REGA1
CABR OR,.1B2,STAT,STAT,CPUST
XZ   OR,IOSTA              CONT TSTIO 1
CABR CAND,.1B2,STAT,STAT,CPUST
XZBR OR,DATI,SB,LAD        EXEC READC

REGA1:
XZ   OR,CIOST              CONT U 1
ZABR OR,LAX,SB,LAD        EXEC READC

REGAD:
XZ   OR,CIOST              CONT U 1
CABR CAND,.1B2,STAT,STAT,CPUST
ZABR OR,LAX,SB,LAD        EXEC READC

RELI:
RZBR OR,DIC,SB,LAD          CONT READP
JMP  F,NWADR,REGA1
CABR OR,.1B2,STAT,STAT,CPUST
XZ   OR,IOSTA              CONT TSTIO 1
CABR CAND,.1B2,STAT,STAT,CPUST
XZBR OR,DATI,SB,LAD        EXEC READC

; SB:=LAD:= D , READP
; IF W=ADDR THEN GOTO KEG ADDR1
; AFTER AM:= 1
; TEST IO
; AFTER AM:= 0
; SB:=LAD:= D+IC , COND READP
; GOTO INSTR EXEC

; REG ADR1:
; WAIT IO
; SB:=LAD:= LAX , COND READP
; GOTO INSTR EXEC

;REG ADDR:
; WAIT IO
; AFTER AM:= 0
; SB:=LAD:= LAX , COND READP
; GOTO INSTR EXEC

; SB:=LAD:= D+IC , READP
; IF W=ADDR THEN GOTO KEG ADDR1
; AFTEK AM:= 1
; TEST IO
; AFTER AM:= 0
; SB:=LAD:= D+IC , COND READP
; GOTO INSTR EXEC

```

10059 CPU23 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 C2040 06475001262C0011
04 C2041 463100000132025
05 C2042 46135312240C3205
06 C2043 4655730000514100
07 C2044 46135512240C3205
08 C2045 7617730026104011
09
10
11
12 C2046 46475001262C02011
13 C2047 463100000132025
14 C2050 46135312240C3205
15 C2051 4655730000514100
16 C2052 46135512240C3205
17 C2053 7617730026104011
18
19
20
21
22
23
24 C2054 46077300262C0011
25 C2055 46135312240C3205
26 C2056 06310000020C2167
27 C2057 063100000132027
28 C2060 4655730000514100
29 C2061 46135512240C3205
30 C2062 7617730026104011
31

;
; SB:=LAD:= D+X , READP
; IF W-ADDR THEN GOTO REG ADDR1
; AFTER AM:= 1
; TEST IO
; AFTER AM:= 0
; SB:=LAD:= DATAIN , COND READP
; GOTO INSTR EXEC

;
; SB:=LAD:= D+IC+X , READP
; IF W-ADDR THEN GOTO REG ADDR1
; AFTER AM:= 1
; TEST IO
; AFTER AM:= 0
; SB:=LAD:= DATAIN , COND READP
; GOTO INSTR EXEC

;
; SB:=LAD:= D , READP
; AFTER AM:= 1
; CALL ADDR ESCAPE(10002)
; IF W-ADDR THEN GOTO REG ADDR
; TEST IO
; AFTER AM:= 0
; SB:=LAD:= DATAIN , COND READP
; GOTO INSTR EXEC

INDXI:
RABR ADD,DISP,X,SB,LAD          CONT READP
JMP F,NWADR,REGA1
CABR OR,.1B2,STAT,STAT,CPUST
XZ OR,IOSTA                    CONT TSTIO 1
CABR CAND,.1B2,STAT,STAT,CPUST
XZBR OR,DATI,SB,LAD           EXEC HEADC

RELXI:
RABR ADD,DIC,X,SB,LAD          CONT READP
JMP F,NWADR,REGA1
CABR OR,.1B2,STAT,STAT,CPUST
XZ OR,IOSTA                    CONT TSTIO 1
CABR CAND,.1B2,STAT,STAT,CPUST
XZBR OR,DATI,SB,LAD           EXEC READC

;AFTER AM = 0, INDIRECT = 1, ESCAPE = 1
;*****

DIRIE:
RZBR OR,DISP,SB,LAD           CONT READP
CABR OR,.1B2,STAT,STAT,CPUST
CALL F,F,ESCA1
JMP F,NWADR,REGAD
XZ OR,IOSTA                    CONT TSTIO 1
CABR CAND,.1B2,STAT,STAT,CPUST
XZBR OR,DATI,SB,LAD           EXEC READC

```

10060 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 C2063 06077300262C02011          ; SB:=LAD:= D+X , READP
04 C2064 4613531224C03205          ; AFTER AM:= 1
05 C2065 0631000020C02167          ; CALL ADDR ESCAPE(100U2)
06 C2066 063100000132027          ; IF W-ADDR THEN GOTO REG ADDR
07 C2067 4655730000514100          ; TEST IO
08 C2070 4613551224C03205          ; AFTER AM:= 0
09 C2071 76177300261C4011          ; SB:=LAD:= D+X , COND READP
10                                     ; GOTO INSTR EXEC
11
12
13 C2072 06475001262C0011          ; SB:=LAD:= D+X , READP
14 C2073 4613531224C03205          ; AFTER AM:= 1
15 C2074 0631000020C02167          ; CALL ADDR ESCAPE(100U2)
16 C2075 063100000132027          ; IF W-ADDR THEN GOTO REG ADDR
17 C2076 4655730000514100          ; TEST IO
18 C2077 4613551224C03205          ; AFTER AM:= 0
19 C2100 76177300261C4011          ; SB:=LAD:= D+X , COND READP
20                                     ; GOTO INSTR EXEC
21
22
23 C2101 46475001262C02011          ; SB:=LAD:= D+IC+X , READP
24 C2102 4613531224C03205          ; AFTER AM:= 1
25 C2103 0631000020C02167          ; CALL ADDR ESCAPE(100U2)
26 C2104 063100000132027          ; IF W-ADDR THEN GOTO REG ADDR
27 C2105 4655730000514100          ; TEST IO
28 C2106 4613551224C03205          ; AFTER AM:= 0
29 C2107 76177300261C4011          ; SB:=LAD:= D+X , COND READP
30                                     ; GOTO INSTR EXEC

```

RELIE:

```

RZBR OR,DIC,SB,LAD          CONT READP
CABR OR,.1B2,STAT,STAT,CPUST
CALL F,F,ESCA1
JMP F,NWADR,REGAD          CONT TSTIO 1
XZ OR,IUSTA
CABR CAND,.1B2,STAT,STAT,CPUST
XZBR OR,DATI,SB,LAD          EXEC READC

```

IDXIE:

```

RABR ADD,DISP,X,SB,LAD          CONT READP
CABR OR,.1B2,STAT,STAT,CPUST
CALL F,F,ESCA1
JMP F,NWADR,REGAD          CONT TSTIO 1
XZ OR,IUSTA
CABR CAND,.1B2,STAT,STAT,CPUST
XZBR OR,DATI,SB,LAD          EXEC READC

```

RLXIE:

```

RABR ADD,DIC,X,SB,LAD          CONT READP
CABR OR,.1B2,STAT,STAT,CPUST
CALL F,F,ESCA1
JMP F,NWADR,REGAD          CONT TSTIO 1
XZ OR,IUSTA
CABR CAND,.1B2,STAT,STAT,CPUST
XZBR OR,DATI,SB,LAD          EXEC READC

```



10061 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 ;AFTER AM = 1, INDIRECT = 0, ESCAPE = 0
03 ;*****
04
05 AMR:
06 C2110 4613551224003205 CABR CAND,.1B2,STAT,STAT,CPUST
07 C2111 7647501326102011 RABR ADD,DIC,SB,SB,LAD EXEC READC
08
09 AMD:
10 C2112 4613551224003205 CABR CAND,.1B2,STAT,STAT,CPUST
11 C2113 3647501326100011 RABR ADD,DISP,SB,SB,LAD EXEC READC
12
13 AMRX:
14 C2114 4613551224003205 CABR CAND,.1B2,STAT,STAT,CPUST
15 C2115 0647501326002000 RAB ADD,DIC,SB,SB
16 C2116 7647100126100011 ABBR ADD,X,SB,LAD EXEC READC
17
18 AMX:
19 C2117 4613551224003205 CABR CAND,.1B2,STAT,STAT,CPUST
20 C2120 4647501326000000 RAB ADD,DISP,SB,SB
21 C2121 7647100126100011 ABBR ADD,X,SB,LAD EXEC READC

```

```

; AFTER AM:= 0
; SB:= SB+IC+D, COND READP
; GOTO INSTR EXEC
; AFTER AM:= 0
; SB:= LAD:= D+ SB , COND READP
; GOTO INSTR EXEC

; AFTER AM:= 0
; SB:= SB+IC+D
; SB:= SB+X , COND READP
; GOTO INSTR EXEC
; AFTER AM:= 0
; SB:= D+SB
; SB:=LAD:= SB+X , COND READP
; GOTO INSTR EXEC

```

10062 CPU823 MICKOPROGRAM LBJ 850816 VERSION 8

```

01
02
03 ;AFTER AM = 1, INDIRECT = 1, ESCAPE = 0
04 ;*****
05
06 AMRI:
07 RABR ADD,DIC,SB,SB,LAD CONT READP ; SB,LAD:= SB+IC+C, READP;
08 JMP F,NWADR,REGAD ; IF W-ADDR THEN GOTO KEG ADDR;
09 XZ OR,IOSTA CONT TSTIO 1 ; TEST IO
10 CABR CAND,.1B2,STAT,STAT,CPUST EXEC READC ; AFTEK AM:= 0;
11 XZBR OR,DATI,SB,LAD ; SB,LAD:= DATI, COND READP;
12 ; GOTO INSTR EXEC
13
14 AMDI:
15 RABR ADD,DISP,SB,SB,LAD CONT READP ; SB:=LAD:= D+SB , READP
16 JMP F,NWADR,REGAD ; IF W-ADDR THEN GOTO KEG ADDR
17 XZ OR,IOSTA CONT TSTIO 1 ; TEST IO
18 CABR CAND,.1B2,STAT,STAT,CPUST EXEC READC ; AFTER AM:= 0
19 XZBR OR,DATI,SB,LAD ; SB:=LAD:= DATAIN , COND READP
20 ; GOTO INSTR EXEC
21
22 AMRXI:
23 RAB ADD,DIC,SB,SB
24 AEHR ADD,X,SB,LAD CONT READP ; SB:= SB+IC+D;
25 JMP F,NWADR,REGAD ; SB,LAD:= SB+X, READP;
26 XZ OR,IOSTA CONT TSTIO 1 ; IF W-ADDR THEN GOTO KEG ADDR;
27 CABR CAND,.1B2,STAT,STAT,CPUST EXEC READC ; TEST IO;
28 XZBR OR,DATI,SB,LAD ; AFTER AM:= 0;
29 ; SB,LAD:= DATI, COND READP
30 ; GOTO INSTR EXEC
31
32 AMXI:
33 RAG ADD,DISP,X
34 AGBR ADD,SB,SB,LAD CONT READP ; Q:= D+X
35 JMP F,NWADR,REGAD ; SB:=LAD:= Q+SB , READP
36 XZ OR,IOSTA CONT TSTIO 1 ; IF W-ADDR THEN GOTO REGADR
37 CABR CAND,.1B2,STAT,STAT,CPUST EXEC READC ; TEST IO
38 XZBR OR,DATI,SB,LAD ; AFTER AM:= 0
39 ; SB:=LAD:= DATAIN , COND READP
40 ; GOTO INSTR EXEC

```

10063 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 ;AFTER AM = 1, INDIRECT = 1, ESCAPE = 1
04 ;*****
05 AMRIE: RAB ADD,ICS,SB,SB
06 C2150 0647501326001000
07 C2151 0647501326200011
08 C2152 0631000002002167
09 C2153 0631000000132027
10 C2154 4655730000514100
11 C2155 4613551224003205
12 C2156 7617730026104011
13
14
15 ARXIE: RAB ADD,ICS,SB,SB
16 C2157 0647501326001000
17 C2160 0644500100000000
18 C2161 0647001326200011
19 C2162 0631000002002167
20 C2163 0631000000132027
21 C2164 4655730000514100
22 C2165 4613551224003205
23 C2166 7617730026104011
24
25
26 ESCA1: RAR ADD,ICS,.2,ICD
27 C2167 4645501400001003
28 C2170 0624730000200040
29 C2171 0631000002000031
30 C2172 2045523400001003

```

```

;
; SB:= SB+ICS
; SB:=LAD:= SB+D , READP
; CALL ADDR ESCAPE(1000U2)
; IF W-ADDR THEN GOTO REG ADDR
; TEST IO
; AFTER AM:= C
; SB:=LAD:= DATAIN , COND READP
; GOTO INSTR EXEC

```

```

;
; SB:= SB+ICS
; Q:= D+X
; SB:=LAD:= SB+Q
; CALL ADDR ESCAPE(1000U2)
; IF W-ADDR THEN GOTO REG ADDR
; TEST IO
; AFTER AM:= 0
; SB:=LAD:= DATAIN , COND READP
; GOTO INSTR EXEC

```

```

;ESCAPE IN INDIRECT ADDRESS CALC
; IC:= IC+2
; ESCAPE(1000U2)
; IC:= IC-2

```

RTN

10064 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 ;SKIP INSTRUCTIONS
02 ;*****
03 ;*****
04 ;*****
05 02173 74310000002174 SKIP: JMP F,F,+3 NEXT
06 02174 4645501400001003 RAR ADD,ICS,.2,ICD NEXT TSTIN
07 02175 7405230000600000 ZQ OR
08
09 ;SKIP IF REGISTER HIGH, SH, OPCODE = 50
10
11 SHE:
12 02176 0624730000120420 IZQ OR,0,5021
13 02177 0631000020000031 CALL F,F,ESCAP
14
15 SH:
16 02200 4605123300000000 AB SUB0,SB,W
17 02201 0631000000452173 JMP T,SIGN,SKIP
18 02202 7405230000600000 ZQ OR
19
20 ;SKIP IF REGISTER LOW, SL, OPCODE = 51
21
22 SLE:
23 02203 4624730000122420 IZQ OR,0,5121
24 02204 0631000020000031 CALL F,F,ESCAP
25
26 SL:
27 02205 4605113300000000 AB SUN0,SB,W
28 02206 0631000000452173 JMP T,SIGN,SKIP
29 02207 7405230000600000 ZQ OR
30
31 ;SKIP IF REGISTER EQUAL, SE, OPCODE = 52
32
33 SEE:
34 02210 4624730000124420 IZQ OR,0,5221
35 02211 0631000020000031 CALL F,F,ESCAP
36
37 SE:
38 02212 4605123300000000 AB SUB0,SB,W
39 02213 06310000000022173 JMP F,NZ,SKIP
40 02214 7405230000600000 ZQ OR

```

```

; INCREASE PC
; ICD:= ICS+2
; TEST INT, GOTO NEXT INSTR

;SHE:
; CALL ESCAPE(5021)
;SH:
; IF W > SB THEN
; GOTO SKIP
; TEST INT
; GOTO NEXT INSTR

;SLE:
; CALL ESCAPE(5121)
;SL:
; IF W < SB THEN
; GOTO SKIP
; TEST INT
; GOTO NEXT INSTR

;SEE:
; CALL ESCAPE(5221)
;SE:
; IF W = SB THEN
; GOTO SKIP
; TEST INT
; GOTO NEXT INSTR

```

10065 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03
04
05 C2215 0624730000126420
06 C2216 0631000002000031
07
08 C2217 4605123300000000
09 C2220 4631000000422173
10 C2221 7405230000060000
11
12
13
14
15
16 C2222 4624730000130420
17 C2223 0631000002000031
18
19 C2224 0607130026000000
20 C2225 4605123300000000
21 C2226 063100000022173
22 C2227 7405230000060000
23
24
25
26
27
28 C2230 0624730000132420
29 C2231 0631000002000031
30
31 C2232 0605141300000000
32 C2233 063100000022173
33 C2234 7405230000060000
34

;SKIP IF REGISTER NOT EQUAL, SN, OPCODE = 53
;SNE:
; IZQ OR,0,5321
; CALL F,F,ESCAP
;SN:
; IF W <> SB THEN
; GOTO SKIP
; TEST INT
; GOTO NEXT INSTR

;SKIP IF REGISTER BITS ONE, SO, OPCODE = 54
;SOE:
; IZQ OR,U,5421
; CALL F,F,ESCAP
;SO:
; IF W = W OR SB THEN
; GOTO SKIP
; TEST INT
; GOTO NEXT INSTR

;SKIP IF REGISTER BITS ZERO, SZ, OPCODE = 55
;SIZE:
; IZQ OR,0,5521
; CALL F,F,ESCAP
;SZ:
; IF W AND SB = 0 THEN
; GOTO SKIP
; TEST INT
; GOTO NEXT INSTR

```

10066 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03
04
05 C2235 0624730000134420
06 C2236 0631000002000031
07
08 C2237 06105412000003400
09 C2240 46050413000000000
10 C2241 0631000000022173
11 C2242 74052300000600000
12
13
14
15
16
17 C2243 4624730000052420
18 C2244 0631000002000031
19
20 C2245 46310000000412254
21
22 C2246 0643501333000100
23 C2247 0641513540000200
24 C2250 06310000000412253
25 C2251 4641513540000300
26 C2252 46310000000412173
27 C2253 0631000000132173
28 C2254 74052300000600000
29

;SKIP IF NO EXCEPTIONS, SX, OPCODE = 56
;SXE:
;
; CALL ESCAPE(5621)
;SX:
; Q:= STAT(21;23)
; IF SB AND Q = 0 THEN
; GOTO SKIP
; TEST INT
; GOTO NEXT INSTR

NEXT TSTIN

;SKIP IF NO WRITE PROTECTION, SP, OPCODE = 25
;SPE:
;
; CALL ESCAPE(2521)
;SP:
; IF SB < 0 THEN
; TEST INT, GOTO NEXT INSTR
; WRK1:=SB + BASEC
; IF SB + BASEC < LOWER LIMIT THEN
; GOTO SP2
; IF SB + BASEC < UPPER LIMIT THEN
; GOTO SKIP
; IF SB<8 THEN GOTO SKIP
; TEST INT, GOTO NEXT INSTR

NEXT TSTIN

```

10067 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 ;INPUT-OUTPUT INSTRUCTIONS
02 ;*****
03 ;*****
04 ;*****
05 ;DATA IN, DI, OPCODE = 35, PRIVILEGED INSTRUCTION
06
07 ;DIE:
08 IZQ OR,0,3541
09 CALL F,F,ESCAP
10 ;DI:
11 JMP F,MODE,INTO
12 ZAR OR,SB,LAD
13 CZB OR,,7,WRKO
14 ABB CAND,WRKO,STAT
15 JMP T,NGADR,DIDEV
16
17 XAB AND,IOSTA,WRKO,WRKO
18 XZB OR,DATI,W
19 ABB OR,WRKO,STAT
20 ;DIDEV:
21 XAB AND,CIOST,WRKO,WRKO
22 XZB OR,CDATI,W
23
24 ABB OR,WRKO,STAT
25
26
27
28
29 ;DATA OUT, DO, OPCODE = 1, PRIVILEGED INSTRUCTION
30 ;DOE:
31 IZQ OR,0,0141
32 CALL F,F,ESCAP
33 ;DO:
34 JMP F,MODE,INTO
35 ZAR OR,W,DATO
36 ZAR OR,SB,LAD
37 CZB OR,,7,WRKO
38 ABB CAND,WRKO,STAT
39 XAB AND,CIOST,WRKO,WRKO
40 ABB OR,WRKO,STAT
41
42
43
44
45
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```

10068 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```
01
02
03
04
05 C2303 46247300000001000 F00E: IZQ OR,0,0040
06 C2304 4631000000002312 JMP F,F,ESUNA
07 C2305 46247300000147000 F63E: IZQ OR,0,6340
08 C2306 4631000000002312 JMP F,F,ESUNA
09 C2307 06247300000175000 F76E: IZQ OR,0,7640
10 C2310 4631000000002312 JMP F,F,ESUNA
11 C2311 46247300000177000 F77E: IZQ OR,0,7740
12 C2312 06310000002000031 ESUNA: CALL F,F,ESCAP
13 C2313 46310000000000032 JMP F,F,INTU

; ESCAPE ON UNASSIGNED OPERATION CODES
;*****
; OPCODE=0
; GOTO ESUNA
; OPCODE=63
; GOTO ESUNA
; OPCODE=76
; GOTO ESUNA
; OPCODE=77
; CALL ESCAPE
; GOTO INTU
```



10069 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

;LOCK SEMAPHORE, LK, OPCODE = 74, PRIVILEGED INSTRUCTION

01  
02  
03  
04  
05 02314 4624730000171140  
06 02315 0631000002000031  
07  
LKE: IZQ OR,0,7446  
CALL F,F,ESCAP  
;LKE:  
; ; CALL ESCAPE(7446);

08  
09 02316 063100000160032  
10  
11 02317 4613730037000700  
12 02320 0611730000001424  
13 02321 0605451300200011  
14 02322 4631000000132362  
15 02323 0601730000000712  
16 02324 4657730032504000  
17 02325 4631000000422336  
18 02326 4611730000003724  
19 02327 4605431300220011  
20 02330 4654541740000100  
21 02331 4631000000422355  
22  
23 02332 4611531200003605  
24  
25 02333 4605240000000024  
26 02334 4605730000241011  
27 02335 7545230000600000  
LK: JMP F,M,MODE,INTO  
OR,15,WRK5  
OR,3,CAMBC  
OR,SB,LAD  
F,NWADR,LER2  
OR,CUR,DATO  
OR,DATI,WRKO  
T,NZ,WSEM3  
OR,1,CAMBC  
OR,SB,LAD  
AND,CU,ST,WRKS  
T,NZ,LER1  
CAR OR,1B20,STAT,CPUST  
ZQR AND,CAMBC  
RZR OR,ICS,LAD  
ZQ OR  
CONT READP  
CONT TSTBE 1  
CONT WRTP  
CONT U 1  
CONT READJ  
NEXT TSTIN 3  
;LK:  
; IF -,MONMODE THEN GOTO INTO  
; LOCKED:= TRUE;  
; WHILE LOCKED DO  
; SET CAMBYPASS AND BUSBUSY;  
; LAD:= SB, READP  
; IF W-ADDRESS THEN GOTO LER2  
; DATO:= CUR;  
; WRKU:= DATI, TEST BUSERROR;  
; IF Q <> U THEN GOTO WSEM3;  
; CAMBC:= 1; LOCKED:= FALSE;  
; LAD:= SB, WRTP;  
; WAIT IO;  
; IF IOSTAT(20:23)<>0 THEN  
; GOTO LER1;  
; CPUST:= STAT OR 1B20;  
; < \* SELECT DISABLE LIMIT \* >  
; CLEAR BYPASSCAM;  
; LAD:= ICS, START PREFETCH;  
; NEXT INST;

10070 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 C2336 4611730000003724
04 C2337 4605240000210011
05 C2340 4641523500000700
06
07 C2341 063100000022330
08 C2342 0655730000004100
09 C2343 4605240000000024
10
11 C2344 0605431300200011
12 C2345 4645523400001003
13 C2346 0607131431600006
14 C2347 0607151431000006
15
16
17 C2350 4645501400001003
18 C2351 0655730000514000
19 C2352 4631000000422344
20 C2353 0607151431000006
21 C2354 0631000000002320
22
23 C2355 4605240000000024
24 C2356 0611640000003600
25 C2357 4631000000420032
26 C2360 0607230032000000
27 C2361 0631000000000033
28
29 C2362 4655730000000100
30 C2363 4605240000000024
31 C2364 4631000000000032

; WSEM3:
; CLEAR BUSBUSY;
; LAD:= 0, READ;
; IF CUR = WRKO THEN
;   LOCKED:= FALSE;
;   GOTO WSEM2;
; WAIT IO;
; CLEAR CAMBYPASS;
; WHILE WRKU<>0 DO
;   LAD:= SB, READP;
;   IC:= IC-2;
;   CLEAR WAIT LAMP, TEST INT;
;   SET WAIT LAMP;
; INSTRUCTION WILL BE EXECUTED
; IC:=IC+2;
; TESTIO;
; END WHILE WRKU<>0
; CLEAR WAIT LAMP
; END WHILE LOCKED;
; CLEAR BYPASSCAM;
; IF IOSTAT(20)<>0 THEN
;   GOTO INTO ;LIMIT VIOLATION;
; ELSE GOTO INTB1 ;IO ERROR;
;
; CAMBC:= 0;
; GOTO INTU;

CONT READ
CONT 0 1
CONT READP
CONT TSTIN
CONT TSTIO 1
CONT 0 1

```

10071 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 ;UNLOCK SEMAPHORE, UL, OPCODE = 37 , PRIVILEGED INSTRUCTION
03
04 ULE:
05 C2365 0624730000077140 IZQ OR,0,3746
06 C2366 0631000002000031 CALL F,F,ESCAP
07
08 UL:
09 C2367 0631000000160032 JMP F,M,MODE,INTO
10 C2370 06310000000130032 JMP F,NW,ADR,INTU
11 C2371 46547300000514000 XZQ OR,DATI
12 C2372 06416220000000700 SQ SUBO,CUR
13 C2373 06310000000422400 JMP T,N,L,ULEX
14 ; DO NOT OPEN IF THE SEMAPHORE WAS NOT LOCED BY THE PROCESS ITSELF
15 C2374 06054312000000005 ZAR OR,STAT,CPUST
16 ; DELAY .495 NSEC BEFORE TESTINT
17 C2375 46052400000000012 ZQR AND,DATO
18 C2376 4605431300220011 ZAR OR,SB,LAD
19 C2377 34557300000710100 XZ OR,CIOST
20 C2400 74052300000600000 ULEX: ZQ OR
;UL:
; IF --,MONMODE THEN GOTO INTO;
; IF SB < 8 THEN GOTO INTU;
CONT TSTIO 1 ; Q:= WORD(SB);
; IF Q <> CUR THEN
; GOTO ULEXIT;
; CPUST:= STAT; RESTORE CPUST
; DATO:= U;
CONT WKTP ; LAD:= SB, WRTP;
NEXT IIOIN 1 ; TEST IO, TEST INT, NEXT;
NEXT TSTIN ;

```

10072 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03
04
05 02401 0624730000075640
06 02402 0631000002000031
07
08 02403 0631000000160032
09 02404 4603730026211111
10 02405 0643501436001100
11 02406 46247300000000640
12 02407 46406000000001300
13 02410 0657730033504000
14 02411 4631000000022444
15 02412 0647000026210011
16 02413 0607431432000000
17 02414 4613730037003400
18 02415 0654730000504000
19 02416 0631000000422421
20 02417 4651513540003700
21 02420 0631000000422435
22 02421 0647001726210011
23 02422 4655730000504000
24 02423 0631000000422435
25
26 02424 0605430000000012
27 02425 0605431300230011
28 02426 4655730000500100

;ACTIVATE PROCESS, AP, OPCODE = 36, PRIVILEGED INSTRUCTION
;APE:
; CALL ESCAPE(3672);
;AP:
; IF -,MONMODE THEN GOTO INTC
; SB,LAD:= PUTAB, READ
; PUSTART:= PUTAB+2;
; Q:= DUMP OFFSET + 26;
; <* OFFSET OF PU-INDEX *>
; FREE:= PU-TABLE(U), TESTBE;
; IF FREE=0 THEN GOTO EXIT;
; SB,LAD:= W + Q, READ
; WKKU:= 2;
; WKK5:= 7;
; IX:= DATI, TESTBE;
; IF IX<>0 OR
; FREE=1 THEN
; BEGIN
; LAD,SB:=PUSTART+IX*2, READ;
; TESTBE;
; IF DATI=0 THEN
; BEGIN
; DATO:= W;
; LAD:= SB, WRT;
; TESTBE;
CONT READ
CONT TSTBE 1
CONT READ
CONT TSTBE 1
CONT READ
CONT TSTBE 1
CONT WRT
CONT TSTBE 1 ;
F,M,MODE,INTO
OR,PUTAB,SB,LAD
ADD,PUTAB,,2,WKK4
OR,0,26.
ADD,DMOFS
OR,DATI,WKK1
FNZ,APEX
ADD,W,SB,LAD
OR,,2,WKK0
OR,,7,WKK5
OR,DATI
TNZ,AP1
SUNO,,1,WKK1
TNZ,AP3
ADD,WKK4,SB,LAD
OR,DATI
TNZ,AP3
OR,W,DATO
OR,SB,LAD
OR,CIOST

```



10074 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03
04 C2445 06247300000165640 DPE: IZQ OR,0,7272
05 C2446 06310000002000031 CALL F,F,ESCAP
06
07 C2447 06310000001600032 DP: F,M,MODE,INTO
08 C2450 06111541200001700 AND,.1B18,STAT
09 C2451 4631000000422467 JMP T,NZ,DPEX
10 C2452 4603730026211111 SZBR OR,PUTAB,SB,LAD
11 C2453 0654730000504000 XZQ OR,DATI
12 C2454 0645202000000012 ZGR ADDO,DATO
13 C2455 0603730026231111 SZBR OR,PUTAB,SB,LAD
14 C2456 4643501432001100 SAB ADD,PUTAB,.2,WRKO
15 C2457 4655730000500100 XZ OR,CIOST
16 C2460 4605240000000012 ZQR AND,DATO
17 C2461 0643501526231011 SABR ADD,PUIX,WRKO,SB,LAD
18 C2462 0613531224001700 CAB OR,.1B18,STAT,STAT
19 C2463 4655730000500100 XZ OR,CIOST
20 C2464 0641702000001012 SZR ADDO,PUIX,DATO
21 C2465 4653501366233011 CABR ADD,.6,INF,SB,LAD
22 C2466 3455730000710100 XZ OR,CIOST
23
24
25
26 C2467 7405230000600000 DPEX: ZG OR
;DEACTIVATE PROCESS, DP, OPCODE = 72, PRIVILEGED INSTRUCTION
;DPE:
;CALL ESCAPE(7272);
;DP:
; IF -,MONMODE THEN GOTO INTO;
; IF STAT(18)<>0 THEN
; GOTO DP_EXIT; < * NO PROCESS ACTIVE * >
; SB,LAD:= PUTAB, READ;
CONT READ
CONT TSTBE 1
; Q:= DATI, IESTBE;
; DATO:= Q+1; < *NOT IN THE SAME INST* >
; SB,LAD:= PUTAB, WRT;
; WRKU:= PUTAB + 2;
CONT TSTBE 1
; TESTBE;
; DATO:= 0;
; SB,LAD:= Q + PUIX, WRT;
; STAT(18):= 1; < *NO PROCESS ACTIVE* >
CONT TSTBE 1
; TESTBE;
; DATO:= PUIX+1;
CONT WRT
; LAD,SB:= INF+6, WRT;
NEXT TIOIN 1
; NEXT INST, TESTIO, TESTINT;
; THE MONITOR LOCK IS CHANGED AS THE PROCESS IS NO LONGER ACTIVE
NEXT TSTIN
; NEXT INST, TEST INT;

```

10075 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

;EXIT, XI, OPCODE = 14, PRIVILEGED INSTRUCTION
;SB SIGNALS NEXT STATE:
;
; U = MONITOR LEVEL
; 1 = USER LEVEL

```

```

06 XIE:
07 02470 0624730000031640 IZQ OR,U,1472
08 02471 0631000002000031 CALL F,F,ESCAP
09
10 02472 0631000000160032 JMP F,M,MODE,INTO
11 02473 0651523300003700 CA SUBO,,1,SB
12 02474 0631000000022514 JMP F,NZ,U,LEVL
13

```

MLEVL:

```

14
15 02475 4651501340211011 CAR ADD,,14,INF,LAD
16 02476 0655730000504100 XZ OR,,10,STA
17 02477 0617730026214011 XZBR OR,DATI,SB,LAD
18 02500 4631000002002554 CALL F,F,L,DRG
19 02501 4605240000000012 ZGR AND,DATO
20 02502 4653501366233011 CABR ADD,,6,INF,SB,LAD
21 02503 4624731777601360 IZQ OR,7777,0057
22 02504 0607041524000000 AGB AND,WRKU,STAT
23 02505 4655730000500100 XZ OR,C,IOST
24 02506 0605431540240011 ZAR OR,WRK1,LAD
25 02507 4607431626000000 ZAB OR,WRK2,SB
26 02510 4653501367001100 CAB ADD,,12,INF,INF
27 02511 0631000000130032 JMP F,N,W,ADR,INTO
28 02512 0605431540000003 ZAR OR,WRK1,ICD
29 02513 74557300000610100 XZ OR,C,IOST

```

;MONITOR CASE:

```

; LAD:= INF+14, READ;
; TSTBE 1 ; TSIBL: <* DUMP ADDRESS * >
; LAD,SB:= DATI, READ;
; LOAD REGISTERS;
; DATO:= U;
; SB,LAD:= INF+6, WRT;
; STAT:= Q AND -(12..17,19);
; TSTBE; OPEN MONITOR LOCK
; LAD:= WRK1, READ INSTRUCTION;
; SB:= WRK2;
; INF:= INF+12;
; IF W=ADDRESS THEN GOTO INTC
; ICD:= WRK1;
NEXT TLIIN 1; NEXT INSTRUCTION

```

10076 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03
04
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06
07
08
09
10
11
12
13
14
15
16
17
18
19
20
;USER CASE:
; IF STAT(18)=C THEN
; BEGIN <* PROCESS ACTIVE * >
; U:= DUMPOFFSET;
; LAD,SB:= CUR+Q, READ;
; LOAD REGISTERS;
; IF WRKU<>1B17 THEN
; BEGIN <* OPEN MONITOR LOCK * >
; DATO:= U;
; SB,LAD:= INF+6, WRT;
; STAT:= WKKU AND -(12..19);
; STAT:= STAT OR 1B16;
; INF:= INF + 12;
; TSTBE; OPEN MONITOR LOCK
; LAD:= WKK1, READ INSTRUCTION;
; SB:= WKK2;
; IF W-ADDRESS THEN GOTO INTO;
; ICD:= WKK1;
; NEXT INST, TEST LIMITS;

CONT READ
CONT WRT
CONT TSTBE 1
CONT READJ
NEXT TLIIN 1

AND,.1B18,STAT
T,NZ,ULEV2
OR,DMOFS
ADD,CUR,SB,LAD
F,F,LDREG
AND,.1B17,WRKO
T,NZ,ULEV1
AND,DATO
ADD,.6,INF,SB,LAD
CAND,B1219,WRKO,STAT
OR,.1B16,STAT,STAT
ADD,.12,INF,INF
OR,CIOST
OR,WRK1,LAD
OR,WRK2,SB
F,NWADR,INTO
OR,WRK1,ICD
OR,CIOST

CA
JMP
SZQ
SGBR
CALL
CA
JMP
ZGR
CABR
CAB
CAB
CAB
XZ
ZAR
ZAB
JMP
ZAR
XZ
06115412000C1700
4631000000422547
46007300000C1300
0643600026210711
4631000002002554
06115415000C2000
4631000000422536
46052400000C0012
4653501366233011
0613551524000100
0613551224002100
4653501367001100
4655730000500100
0605431540240011
4607431626000000
0631000000130032
0605431540000003
7455730000610100

```



1UG77 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 ULEV1:
04 C2536 0605431540240011 ZAR OR,WRK1,LAD          CONT READJ
05 C2537 061153150003605  CAR OR,.1B20,WRKU,CPUST
06 C2540 061355152400100  CAB CAND,B1219,WRKU,STAT
07 C2541 0613531224002100 CAB OR,.1B16,STAT,STAT
08 C2542 4607431626000000 ZAB OR,WRK2,SB
09 C2543 4653501367001100 CAB ADD,.12,INF,INF
10 C2544 063100000130032  JMP F,NWADR,INTO
11 C2545 0605431540000003 ZAR OR,WRK1,ICD
12 C2546 7455730000610100 XZ OR,C10ST
13
14
15 ULEV2:
16 C2547 4605240000000012 ;<★ NO PROCESS ACTIVE, OPEN MONITOR LOCK ★>
17 C2550 4653501366233011 ZGR AND,DATO
18 C2551 4655730000500100 CABR ADD,.6,INF,SB,LAD  CONT WRT
19 C2552 4653501367001100 CAB OR,C10ST          CONT TSTBE 1
20 C2553 0631000000002001 JMP ADD,.12,INF,INF
21
; END ELSE
; BEGIN
; LAD:= WRK1, READ INSTRUCTION
; CPUST:= WRKU OR 1B20; SET OISABLE LIM;
; STAT:= WKU AND -(12..19);
; STAT:= STAT OR 1B16;
; SB:= WRK2;
; INF:= INF+12;
; IF W-ADDRESS THEN GOTO INTO
; ICD:= WRK1;
; NEXT INSTRUCTION
; END;
; END ELSE BEGIN
; DATO:= 0;
; SB,LAD:= INF+6, WRT;
; TEST BUSERROR;
; INF:= INF+12;
; GOTO DISPATCHING POINT;
; END;
NEXT TLIIN 1
NEXT TSTBE 1

```

1007E CPU823 MICROPROGRAM LBJ 850&16 VERSION 8

```

01
02
03 C2554 46577300205C4000
04 C2555 4647101426210011
05 C2556 06577300215C4000
06 C2557 4647101426210011
07 C2558 06577300225C4000
08 C2559 4647101426210011
09 C2560 46577300235C4000
10 C2561 4647101426210011
11 C2562 46577300325C4005
12 C2563 4647101426210011
13 C2564 06577300335C4000
14 C2565 4647101426210011
15 C2566 46577300255C4000
16 C2567 4647101426210011
17 C2568 46577300255C4000
18 C2569 4647101426210011
19 C2570 46547300050C4016
20 C2571 4647101426210011
21 C2572 0641220000000002
22 C2573 46577300355C4013
23 C2574 4647101426210011
24 C2575 4641421640000102
25 C2576 06547300050C4000
26 C2577 4647101426210011
27 C2578 4641220000000202
28 C2579 0645013640000014
29 C2580 06547300050C4000
30 C2581 4647101426210011
31 C2582 0641220000000302
32 C2583 4645013640000015
33 C2584 06547300050C4021
34 C2585 2041220000000402

```

LDREG:

```

XZB OR,DATI,W0
ABBR ADD,.2,SB,LAD
XZB OR,DATI,W1
ABBR ADD,.2,SB,LAD
XZB OR,DATI,W2
ABBR ADD,.2,SB,LAD
XZB OR,DATI,W3
ABBR ADD,.2,SB,LAD
XZBR OR,DATI,WRK0,CPUST
ABBR ADD,.2,SB,LAD
XZB OR,DATI,WRK1
ABBR ADD,.2,SB,LAD
XZB OR,DATI,CAUSE
ABBR ADD,.2,SB,LAD
XZB OR,DATI,WRK2
ABBR ADD,.2,SB,LAD
XZQR OR,DATI,CPA
AEBR ADD,.2,SB,LAD
ZQS SUB,CPAC
XZBR OR,DATI,WRK3,BASE
ABBR ADD,.2,SB,LAD
ZAS SUB,WRK3,BASEC
XZQ OR,DATI
ABBR ADD,.2,SB,LAD
ZUS SUB,LLIMC
AQR SUNO,WRK3,LLIM
XZQ OR,DATI
AEBR ADD,.2,SB,LAD
ZQS SUB,ULIMC
AQR SUNO,WRK3,ULIM
XZQR OR,DATI,ILIM
ZQS SUB,ILIMC

```

```

;LOAD REGISTERS
; WU:= DATI;
; SB,LAD:= SB+2, READ;
; W1:= DATI; TSTBE;
; SB,LAD:= SB+2, READ;
; W2:= DATI; TSTBE;
; SB,LAD:= SB+2, READ;
; W3:= DATI; TSTBE;
; SB,LAD:= SB+2, READ;
; WRK0,CPUST:= DATI, TSTBE; LATER STAT
; SB,LAD:= SB+2, READ;
; WRK1:= DATI, TSTBE; LATER IC;
; SB,LAD:= SB+2, READ;
; CAUSE:= DATI, TSTBE;
; SB,LAD:= SB+2, READ;
; WRK2:= DATI, TSTBE; LATER SB;
; SB,LAD:= SB+2, READ;
; CPA,Q:= DATI, TSTBE;
; SB,LAD:= SB+2, READ;
; CPAC:= Q;
; WRK3,BASE:= DATI, TSTBE;
; SB,LAD:= SB+2, READ;
; BASEC:= WRK3;
; Q:= DATI, TSTBE;
; SB,LAD:= SB+2, READ;
; LLIMC:= Q;
; LLIM:= Q - BASE;
; Q:= DATI, TSTBE;
; SB,LAD:= SB+2, READ;
; ULIMC:= Q;
; ULIM:= Q - BASE;
; Q,ILIM:= DATI, TSTBE;
; ILLIMC:= Q, RETURN;

```

10079 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03
04 02614 0605230000000022      ;CHCLK:
05 02615 4705230000000000      -HCLK:
06 02616 4705230000000000
07 02617 47247300000007760
08 02620 0617640033000300      OR,ILEVR
09 02621 4605431540000020      OR
10 02622 0745230000000000      OR
11 02623 0745230000000000      OR
12 02624 0745230000000000      OR
13 02625 0745230000000000      OR
14 02626 0651523540003600      SUB0,.8,WRK1
15 02627 60310000000022630      F,NZ,PUINIT
16
;CHECK CLOCK INTERRUPT:
; ILEVR:= Q;
; DELAY(675NSEC);
;
; WRK1:= ILEV(16:23);
; INTRG:= WRK1; CLEAR INTERRUPT
;
; DELAY(900NSEC);
;
; IF LEVEL=8 THEN
;   GOTO PUINIT
; ELSE RETURN;
CONT 0 2
CONT 0 2
CONT 2
CONT 0 3
CONT 0 3
CONT 0 3
CONT 0 3
RTN

```

10080 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 C2630 46137300370C3400
04 C2631 0611730000213611
05 C2632 4655541740004100
06 C2633 4631000000423004
07 C2634 46247300760C0000
08 C2635 06176400320C0100
09 C2636 46147300000C0400
10 C2637 07226015320C0600
11 C2640 4605431500210011
12 C2641 4655541740004100
13 C2642 4631000000423004
14 C2643 46177300330C4000
15 C2644 0605431540210011
16 C2645 4655541740004100
17 C2646 4631000000423004
18 C2647 06177300340C4000
19 C2650 46414216000C1102
20 C2651 0647101433210011
21 C2652 4655541740004100
22 C2653 4631000000423004
23 C2654 46177300270C4000
24 C2655 0647101433210011
25 C2656 4655541740004100
26 C2657 4631000000423004
27 C2660 46147300000C4000
28 C2661 06412200000C0602
29 C2662 0647101433210011
30 C2663 4655541740004100
31 C2664 4631000000423004
32 C2665 46147300000C4000
33 C2666 06412200000C0502
34 C2667 0647101433210011
35 C2670 4655541740004100

```

```

; MASK:= 7;
; LAD:= 8, READ;
; IF IOSTA AND MASK <> 0 THEN
;   GOTO SYSTEMFAULT7;
;
; WRKU:= CIOST AND 8'3700000;
; QI:= DATI;
; WRKU:= Q + WRKO(0:11);
; LAD:= WRKU, READ;
; IF IOSTA AND MASK <> 0 THEN
;   GOTO SYSTEMFAULT7;
;
; WRK1:= DATI;
; LAD:= WRK1, READ;
; IF IOSTA AND MASK <> 0 THEN
;   GOTO SYSTEMFAULT7;
;
; WRK2:= DATI;
; PUTAB:= WRK2;
; WRK1,LAD:= WRK1+2, READ;
; IF IOSTA AND MASK <> 0 THEN
;   GOTO SYSTEMFAULT7;
;
; INF:= DATI;
; WRK1,LAD:= WRK1+2, READ;
; IF IOSTA AND MASK <> 0 THEN
;   GOTO SYSTEMFAULT7;
;
; MONT:= DATI;
; WRK1,LAD:= WRK1+2, READ;
; IF IOSTA AND MASK <> 0 THEN
;   GOTO SYSTEMFAULT7;
;
; SIZE:= DATI;
; WRK1,LAD:= WRK1+2, READ;
; IF IOSTA AND MASK <> 0 THEN

```

```

CONT READ
CONT U 1

CONT READ
CONT U 1

CONT READ
CONT O 1

CONT READ
CONT U 1

CONT READ
CONT U 1

CONT READ
CONT U 1

CONT READ
CONT O 1

```

```

OR,07,WRK5
OR,08,LAD
AND,IOSTA,WRK5
T,NZ,SYFL7
OR,0370,0
AND,CIUST,WRKO
OR,DATI
ADD,LSWAP,WRKO,WRKO
OR,WRKO,LAD
AND,IOSTA,WRK5
T,NZ,SYFL7
OR,DATI,WRK1
OR,WRK1,LAD
AND,IOSTA,WRK5
T,NZ,SYFL7
OR,DATI,WRK2
SUB,WRK2,PUTAB
ADD,02,WRK1,LAD
AND,IOSTA,WRK5
T,NZ,SYFL7
OR,DATI,INF
ADD,02,WRK1,LAD
AND,IOSTA,WRK5
T,NZ,SYFL7
OR,DATI
SUB,MONT
ADD,02,WRK1,LAD
AND,IOSTA,WRK5
T,NZ,SYFL7
OR,DATI
SUB,SIZE
ADD,02,WRK1,LAD
AND,IOSTA,WRK5

```

```

PUINIT:  CZB
          CZR
          XA
          JMP
          IZQ
          XQB
          XZQ
          HGB
          ZAR
          XA
          JMP
          XZB
          ZAR
          XA
          JMP
          XZB
          ZAS
          ABBR
          XA
          JMP
          XZB
          ABBR
          XA
          JMP
          XZQ
          ZQS
          ABBR
          XA
          JMP
          XZQ
          ZQS
          ABBR
          XA

```

10081 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 C2671 4631000000423004      JMP      T,NZ,SYFL7
03 C2672 4614730000004000      XZQ     OR,DATI
04 C2673 0641220000001202      ZQS     SUB,EXOFS
05 C2674 0647101433210011      ABBR    ADD,.2,WRK1,LAD
06 C2675 46555417400004100      XA      AND,IOSTA,WRK5
07 C2676 4631000000423004      JMP      T,NZ,SYFL7
08 C2677 4614730000004000      XZQ     OR,DATI
09 C2700 4641220000001302      ZQS     SUB,DMOFS
10 C2701 0611730000001424      CZR     OR,.3,CAMBC
11 C2702 0651712000001404      CZR     SUNO,.3,LCD
12 C2703 4607240032000000      ZQB     AND,WRKO
13 C2704 4207101434000000      ABB     ADD,.2,WRK2
14
15
16 C2705 4645101534210011      ABR     ADD,WRKO,WRK2,LAD
17 C2706 46555417400004100      XA      AND,IOSTA,WRK5
18 C2707 0631000000422777      JMP     T,NZ,ERR
19 C2710 06557020000004000      XZ      ADDO,DATI
20 C2711 4631000000022714      JMP     F,NZ,FOUND
21 C2712 2637101432114060      NSABB   ADD,.2,WRKO,NL,F,LCN  LRTN
22
23 C2713 0631000000002777      JMP     F,F,ERR
24
25 C2714 0641421500001002      ZAS     SUB,WRKO,PUIX
26 C2715 4613531471002006      CABR    OR,-1B17,CNTR,CNTR,CNTR
27
;
;
; GOTO SYSTEMFAULT7;
;
; EXOFS:= DATI;
; WRK1,LAD:= WRK1+2, READ
; IF IOSTA AND MASK <> 0 THEN
; GOTO SYSTEMFAULT7;
;
; DMOFS:= DATI;
; CAMBC:= 3; SET BYPASSCAM+BUSBUSY
;
; WRKO:= 0;
; WRK2:= WRK2+2;
; FOR LC:= -5 STEP 1 UNTIL 0 D
; BEGIN
; LAD:= WRK2+WRKO, READ;
; IF IOSTA AND MASK <> 0 THEN
; GOTO ERROR;
; IF DATI=-1 THEN
; GOTO FOUND;
; WRKU:= WRKU+2;
; END;
; GOTO ERROR;
; FOUND:
; PUIX:= WRKU;
; CNTR,CNTR:= CNTR OR 1B17;
; TURN RUNLAMP ON OCP ON;

```

FOUND:

10082 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 C2716 4603730032211111
03 C2717 4655541740004100
04 C2720 0631000000422777
05 C2721 4614730000004000
06 C2722 0645202000000012
07 C2723 0605431500230011
08 C2724 0607101432000000
09 C2725 0655541740000100
10 C2726 0631000000422777
11 C2727 4611730000003724
12 C2730 4605240000000012
13 C2731 0641501500231011
14 C2732 0655541740000100
15 C2733 4631000000423002
16 C2734 4605240000000024
17 C2735 4653501367001100
18
19 C2736 0624730017601760
20 C2737 0605230000000021
21 C2740 4643501432001100
22 C2741 4613531471001506
23 C2742 0624730000000500
24 C2743 4607230024000000
25 C2744 4600730000001000
26 C2745 4601230000000702
27
28
29
30 C2746 4643501532211011
31 C2747 0657730035004000
32 C2750 4631000000422754

; LAD,WRKU:= PUTAB, READ;
; IF IOSTA AND MASK <> 0 THEN
; GOTO ERROR;
; Q:= DATT;
; DATO:= Q+1;
; LAD:= WRKU, WRT;
; WRKU:= WRKU + 2;
; IF CIOST AND MASK <> 0 THEN
; GOTO ERROR;
; CAMBC:= 1;
; DATO:= 0;
; LAD:= WRKU + PUIX, WRT;
; IF CIOST AND MASK <> 0 THEN
; GOTO ERROR1;
; CAMBC:= 0;
; INF:= INF+12;
;DISPATCHING POINT:
;
; ILIM:= 63<12+63; ENABLE ALL INT
; WRKU:= PUTAB + 2;
; CNTR,CNTRO:= CNTR OR 4; CLEAR RUN LAMP
; STAT:= 1B16+1B18;
; CUR:= -PUIX-1;

; WHILE IDLE, THE PU IS ON USER LEVEL
; WRKU,LAD:= WRKU+PUIX, READ;
; WRK3:= DATT, WAIT;
; IF WRK3 = 0 THEN

```

.ISPA:

; WHILE IDLE, THE PU IS ON USER LEVEL

10083 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

01 ; DO NOT TEST FOR INTERRUPT THE FIRST TIME (SINGLE STEP)

02  
 03  
 04 ; REPEAT  
 05 C2751 4605431500210011 ZAR OR,WRKO,LAD CONT READ ; LAD:= WRKU, READ;  
 06 C2752 0657730035604000 XZB OR,DAT1,WRK3 CNT TSTIN 1 ; WRK3:= DAT1, TSTINT;  
 07 C2753 063100000022751 JMP F,NZ,LOP1 ; UNTIL WRK3 <> 0;  
 08 C2754 4641421640000702 DISP1: ZAS SUB,WRK3,CUR ; CUR:= WRK3;  
 09 C2755 0643501666211311 SABR ADD,DMOFS,WRK3,SB,LAD CONT READ ; LAD,SB:= WRK3+DMOFS, READ;

10 ; WHILE LOADING THE REGISTERS, THE PU IS ON MONITOR LEVEL

11  
 12  
 13 C2756 4653513367001100 CAB SUNO,.12,INF,INF ; INF:= INF - 12;  
 14 C2757 0607240024000000 ZGB AND,STAT ; STAT:= 0;  
 15 C2760 4613551471001506 CABR CAND,.4,CNTR,CNTR,CNTR ; CNTR,CNTR:= CNTR CAND 4; SET RUN LAMP  
 16 C2761 4631000020002554 CALL F,F,LDREG ; LOAD REGISTERS;  
 17 C2762 4653501367001100 CAB ADD,.12,INF,INF ; INF:= INF + 12;  
 18 ; CHANGE TO USER LEVEL;  
 19 C2763 0613551524000100 CAB CAND,B1219,WRKG,STAT ; STAT:= WRKU AND -, (12,.19);  
 20 C2764 0611541500002000 CA AND,.1B17,WRKU ; IF LOCK CONDITION THEN

10084 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 C2765 4631000000022771      JMP      F,NZ,DISP2
03 C2766 0631000002000037      CALL    F,F,LOCKM
04 C2767 0601730000000421      SZR     OR,ILIMC,ILIM
05 C2770 46115512000003605      CAR     OR,.1B20,STAT,CPUST
06
07 C2771 0605431540240011      DISP2: ZAR   OR,WRK1,LAD
08 C2772 4607431626000000      ZAB     OR,WRK2,SB
09 C2773 0613531224002100      CAB     OR,.1B16,STAT,STAT
10 C2774 0605431540000003      ZAR     OR,WRK1,ICD
11 C2775 0631000000130032      JMP     F,NWADR,INTU
12 C2776 74557300000610100      XZ      OR,CIOST
13
14
15 C2777 46117300000003724      ERR:    CZR   OR,.1,CAMBC
16 C3000 46052400000210011      ZGR     AND,LAD
17 C3001 4655730000000100      XZ      OR,CIOST
18
19 C3002 46052400000000024      ERR1:   ZGR   AND,CAMBC
20 C3003 46310000000003004      JMP     F,F,SYFL7
21
22 SYFL7: IZQ   OR,0,7
23 C3004 46247300000000160      CALL    F,F,SYFLT
24 C3005 06310000020004021
; BEGIN
; LOCKMONITOR;
; RESTORE INTERRUPTLIMITS;
; SET DISABLE LIMITS;
; END;
; LAD:= WRK1, READ INSTRUCTION;
; SB:= WRK2;
; STAT:= STAT OR 1B16;
; ICD:= WRK1;
; IF W-ADDRESS THEN GOTO INTC
; GOTO NEXT INSTRUCTION;
NEXT TLIIN 1
; ERROR:
; CAMBC:= 1; SET BYPASSCAM
; LAD:= 0, READ;
; SYNC IO;
; ERROR1:
; CAMBC:= 0;
; GOTO SYSTEMFAULT7;
; SYSTEMFAULT7:
; ERROR IN INITIALIZING PU;
; SYSTEMFAULT(7);

```



## 10085 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
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05 C3006 0624730000166340
06 C3007 0631000002000031
07
08
09 C3010 4645523400001003
10 C3011 4613531224003105
11
12 C3012 4613551326003700
13 C3013 063100000023037
14 C3014 4605122001000000
15 C3015 4231000000413030
16 C3016 4655730000710100
17
18
19 C3017 4605430000200011
20 C3020 0655730000514012
21 C3021 46054300040220011
22 C3022 4607101400000000
23 C3023 0607113426000000
24 C3024 6637101401024060
25 C3025 4613551224003105
26 C3026 4645501400001003
27 C3027 3455730000710100

;MOVE HALFWORDS, MH, OPCODE = 73
;MHE:
; IZQ OR,0,7316
; CALL F,F,ESCAPE
; CALL ESCAPE(7316);

;MH:
; ICD:= ICS - 2;
; STAT,CPUST:= STAT OR 1B3;
; CALCULATION *
; SB:= SB AND -1;<*MAKE SB EVEN*
; IF SB=0 THEN GOTO MHFIN;
; IF W >= WPRE THEN
; BEGIN <* MOVE FORWARD *
; FOR SB:= SB TO 0 BY -2 DO
; BEGIN
; TESTIO, TESTINTERRUPT;
; LAD:= W, READP;
; DATO:= DATI, TESTIO;
; LAD:= WPRE, WRTP;
; W:= W+2;
; WPRE:= WPRE+2;
; END;
; STAT,CPUST:= STAT OR 1B3;
; ICD:= ICS+2;
; TESTIO,TESTINT, NEXT INST;

; <* SET AFTER ESCAPE IN ORDER TO INHIBIT ADDRESS
;CAB CAND,.1,SB,SB
;F,NZ,MHFIN
;SUBO,W,WPRE
;T,NEG,MHBAK
;OR,CPUST
PUSH
CONT TIOIN 1

;OR,W,LAD
;OR,DATI,DATO
;OR,WPRE,LAD
;ADD,.2,W
;SUNO,.2,SB
;NSABB ADD,.2,WPRE,NL,F,NZ LRTN
;CABR CAND,.1B3,STAT,STAT,CPUST
;RAR ADD,ICS,.2,ICD
;XZ OR,CIOST
CONT READP
CONT TESTIO 1
CONT WRTP
;NEXT TIOIN 1

```

10086 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

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04 C3030 42535113330C3700
05
06
07 C3031 4655730000710100
08 C3032 06451015402C0011
09 C3033 0655730000514012
10 C3034 0645101541220011
11 C3035 06071134260C0000
12 C3036 26371134330C24060
13
14 C3037 46135512240C3105
15 C3040 46455014000C1003
16 C3041 34557300000710100

```

MHBAK:

CAB SUN, .1, SB, WRK1 PUSH

XZ OR, CIOST  
 ABR ADD, WRK1, W, LAD  
 XZR OR, DAT1, DATO  
 ABR ADD, WRK1, WPRE, LAD  
 ABB SUNO, .2, SB  
 NSABB SUNO, .2, WRK1, NL, F, NZ LRTN

CONT TIOIN 1  
 CONT READP  
 CONT TSTIO 1  
 CONT WRTP  
 NEXT TIOIN 1

MHFIN:

CABR CAND, .1B3, STAT, STAT, CPUST  
 RAR ADD, ICS, .2, ICD  
 XZ OR, CIOST

```

; END FORWARD;
; ELSE BEGIN
; I:= SB-2;
; FOR J:=I TO 0 BY -2 DO
; BEGIN
; TESTIO, TESTINT;
; LAD:= W+I, READP;
; DATO:= DAT1, TESTIO;
; LAD:= WPRE+I, WRTP;
; SB:= SB-2;
; END;
; END BACKWARD;
; STAT, CPUST:= STAT AND -.1B3;
; IC:= IC+2;
; TESTIO TESTINT;

```

1U087 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

;INDEX CHECK AND CALCULATION, IX, OPCODE = 75  
;THE INSTRUCTION HAS THE FOLLOWING FORMAT  
; IX W <BASE>  
; <DOPEREL>,<TYPE>

```

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08 C3042 0624730000172640
09 C3043 0631000002000U31
10
11 C3044 4645501400C01003
12 C3045 063100000130032
13 C3046 4654730000514000
14 C3047 0645523400201011
15 C3050 0607430037CC0000
16 C3051 4657730033514000
17 C3052 46627315720C0100
18 C3053 46471015262C0011
19 C3054 0662731573CC0400
20
21 C3055 0605423540CC0000
22 C3056 0631000000C13064
23 C3057 4251523540C03704
24 C3060 6637330037520060
25 C3061 46577300335514000
26 C3062 06471134262C0011
27 C3063 0631000000CC03066

;IXE:
;
; CALL ESCAPE(/532);
;IX:
; ICD:= ICS+2;
; IF SB < 8 THEN GOTO INTO;
; Q:= DATI, TESTIO;
; LAD:= ICS-2, HEADP; READ DOPEREL,TYPE
; IX:= W;
; WRK1:= DATI, TSTIO;
; DOPEHEL:=WRK1(0)EXTEND12 CON WRK1(0:11);
; SB,LAD:= BASE+DOPEHEL, READP; (* LB *)
; TYPE:=WRK1(12)EXT 12 CON WRK1(12:23);
; NOTE!! HZB DOES NOT WRITE THE RESULT ON DBUS, SO NO CONDITION IS GENERATED
; IF TYPE > U THEN
; BEGIN
; LCD:= 1-TYPE;
; IX:= IX SHIFT TYPE;
; LB:= DATI, TSTIO;
; SB,LAD:= SB-2, READP; (* UB *)
; END ELSE

IXZ OR,0,7532
CALL F,F,ESCAP
RAR ADD,ICS,.2,ICD
JMP F,NWADK,INTU
XZQ OR,DATI
RAR SUBO,ICS,.2,LAD
ZAB OR,W,WRK5
XZB OR,DATI,WRK1
HZB OR,EXLOA,WRK1,WRKO
ABBR ADD,WRKO,SB,LAD
HZB OR,EX1,WRK1,WRK1
; NOTE!! HZB DOES NOT WRITE THE RESULT ON DBUS,
ZA SUBO,WRK1
JMP F,NEG,IXNEG
CAR SUBO,.1,WRK1,LCD
SLZBB OR,WRK5,Z,NL,T,MAXLO
XZB OR,DATI,WRK3
ABBR SUNO,.2,SB,LAD
JMP F,F,IXJOIN

CONT TSTIO 1
CONT READP
CONT TSTIO 1
CONT READP
CONT TSTIO 1
CONT READP
CONT TSTIO 1
CONT READP
PUSH
LRTN
CONT TSTIO 1
CONT READP
END ELSE

```



1U089 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

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FILL 40CC  
.EOT

;FILE: T3, EDITED: 850402/LBJ

\*\*\*\*\*

;

; CPU 823 TCP ROUTINE

;

\*\*\*\*\*

00001  
014000

.NOMAC'1  
-LOC 4000\*3

;JUMP TABLE STARTING AT LOCATION 8'4000

04000 4631000000004617  
04001 4631000000004022  
04002 4631000000005073  
04003 4631000000005111  
04004 4631000000004511  
04005 4631000000004207  
04006 4631000000004137  
04007 4631000000004162  
04010 0631000000004075  
04011 4631000000004024  
04012 0631000000005035  
04013 4631000000004164  
04014 4631000000004116  
04015 4631000000005106  
04016 4631000000005037  
04017 0631000000005366  
04020 4631000000005357  
04021 4631000000005247

JMP F,F,.OWUP  
JMP F,F,.CPIN  
JMP F,F,.CPAL  
JMP F,F,.EMAL  
JMP F,F,.INGL  
JMP F,F,.CPEK  
JMP F,F,.POCT  
JMP F,F,.CRLF  
JMP F,F,.CHAR  
JMP F,F,.CPI2  
JMP F,F,.ELAY  
JMP F,F,.PTXT  
JMP F,F,.TNUM  
JMP F,F,.AUTO  
JMP F,F,.INTR  
JMP F,F,.AMTEST  
JMP F,F,.AMER  
JMP F,F,.YFLT

; GOTO POWERUP  
; GOTO TCPIN  
; GOTO OCPAUTOLOAD  
; GOTO REMOTE AUTOLOAD  
; GOTO SINGLE INSTR. INTERRUPT  
; GOTO TCPEKOR  
; GOTO TYPE OCIAL  
; GOTO TYPE 'CR'LF'  
; GOTO GETCHAR  
; GOTO TCPIN2  
; GOTO DELAY  
; GOTO TYPE TEXT  
; GOTO GETNUMBER  
; GOTO RAUTU  
; GOTO CLEAK INTERRUPT  
; GOTO CAMTEST  
; GOTO CAM ERROR  
; GOTO SYFLT

10090 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

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01
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04 C4022 4641421500001402
05 C4023 4631000002004010
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08 C4024 0611541440002000
09 C4025 063100000024033
10 C4026 0665622000000160
11 C4027 0631000000024033
12 C4030 0611541440001500
13 C4031 0631000000422001
14 C4032 4631000000004500
15
16
17 C4033 0665622000002060
18 C4034 063100000024473
19 C4035 0665622000002300
20 C4036 4631000000424047
21 C4037 4631000002004010
22 C4040 4665622000002320
23 C4041 463100000024421
24 C4042 4665622000002340
25 C4043 063100000024425
26 C4044 0665622000002440
27 C4045 063100000024407
28 C4046 4631000000004005

;
;
; SAVE WRKU IN SWRKO
; Q:= GETCHAR
;
;
; IF CNTR.RUN AND Q= 'BELL'
; BEGIN
;
; IF CNTR.21 <> 0 THEN
; GOTO DISPATCHING POINT < * NOT RUN * >
; ELSE GOTO CONTINUE?
; END?
;TCPI3:
; IF Q='C' THEN GOTO C
;
; IF Q<>'L' THEN GOTO TEST-R
; Q:=GETCHAR
; IF Q='M' THEN GOTO LM
;
; IF Q='N' THEN GOTO LN
; IF Q='R' THEN GOTO LR
; GOTO TCP ERROR

```

```

;TCPIN:
;CPIN: ZAS SUB,WRKU,SWRKO
CALL F,F,GCHAR
;CPI2:
;TCPI2: CA AND,,1B17,CNTR
JMP F,NZ,TCPI3
IQ SUB0,0,7
JMP F,NZ,TCPI3
CA AND,,4,CNTR
JMP T,NZ,DISPA
JMP F,F,C1

```

```

TCPI3:
IQ SUB0,0,"C
JMP F,NZ,C
IQ SUB0,0,"L
JMP T,NZ,TESTR
CALL F,F,GCHAR
IQ SUB0,0,"M
JMP F,NZ,LM
IQ SUB0,0,"N
JMP F,NZ,LN
IQ SUB0,0,"R
JMP F,NZ,LR
JMP F,F,TCPER

```

10091 CPUB23 MICROPROGRAM LBJ 850816 VERSION 8

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01
02 C4047 06656220000C2440 TESTR: IQ SUB0,0,"R
03 C4050 06310000000C24416 JMP F,NZ,R
04 C4051 46656220000C2460 IQ SUB0,0,"S
05 C4052 06310000000C24464 JMP F,NZ,S
06 C4053 06656220000C2500 IQ SUB0,0,"T
07 C4054 46310000000C26000 JMP F,NZ,TX
08 C4055 06656220000C2600 IQ SUB0,0,"X
09 C4056 46310000000C24005 JMP T,NZ,TCPER
10 C4057 46310000000C2004010 CALL F,F,GCHAR
11 C4060 46656220000C2100 IQ SUB0,0,"D
12 C4061 06310000000C24503 JMP F,NZ,XD
13 C4062 46656220000C2320 IQ SUB0,0,"M
14 C4063 06310000000C24440 JMP F,NZ,XMM
15 C4064 46656220000C2340 IQ SUB0,0,"N
16 C4065 46310000000C24444 JMP F,NZ,XN
17 C4066 06656220000C2440 IQ SUB0,0,"R
18 C4067 46310000000C24400 JMP F,NZ,XR
19 C4070 46656220000C2460 IQ SUB0,0,"S
20 C4071 06310000000C24506 JMP F,NZ,XST
21 C4072 06656220000C2560 IQ SUB0,0,"W
22 C4073 06310000000C24577 JMP F,NZ,XW
23 C4074 46310000000C24005 JMP F,F,TCPER

; IF Q=R' THEN GOTO R
;
; IF Q=S' THEN GOTO S
;
; IF Q=T' THEN GOTO TX
;
; IF Q>X' THEN GOTO TCP ERROR
;
; Q:=GETCHAR
; IF Q=D' THEN GOTO XD
;
; IF Q=M' THEN GOTO XMM
;
; IF Q=N' THEN GOTO XN
;
; IF Q=R' THEN GOTO XR
;
; IF Q=S' THEN GOTO XST
;
; IF Q=W' THEN GOTO XW
;
; GOTO TCP ERROR

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10092 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

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; CHARACTER INPUT PROCEDURE
;THE PROCEDURE READS A 8 BITS CHARACTER FROM TCP AND MASK OUT THE 7 LEAST
;SIGNIFICANT BITS. THE RESULT IS LOADED IN THE Q-REGISTER AND THE WRKU-REGISTER
;AND ECHGED ON TPOUT
; REGISTERS USED: WRKU
;CHAR:
;GCHAR:
;GETCHAR:
; IF TCPST(22)<>0 THEN WAIT
; C. DATA AVAILABLE
; Q:=TCPIN
; CLEAR TC INTERRUPT
; Q:=Q(17:25)
; WRKU:=Q
; TYPE(WKKO)
AND,TCPST,.2
F,NZ,GCHAR
OR,IPIN
OR,TCCLR
AND,O,177
OR,WRKU
F,F,TYPE
;PROCEDURE TYPE
;THE CHARACTER IN THE WRKU-REGISTER IS TYPED ON THE TCP
;REGISTER USED: Q, RETURN: Q=CHAR
TYPE:
; Q:=1
; IF TCPST(23) THEN WAIT
; C. TCOUTFULL
; Q:=TPOUT:= WRKU(16:23)
; IF Q<>CR THEN RETURN
;
; WRKU:= LF
; GOTO TYPE

```

RTN



10093 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

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; OCTAL NUMBER INPUT PROCEDURE
;
; THE PROCEDURE READS AN OCTAL NUMBER FROM TCP TO WRK2.
; THE NUMBER IS TERMINATED BY 'LF' OR ':'.
; REGISTER USED: Q,WRKO

      .TNUM:
      ;GTNUM:
      ZAB      AND,WRK2,WRK2
      GTNU1:   CALL  F,F,GCHAR
      IQ       SUBO,0,12
      JMP      TNZ,,+3
      IQ       SUBO,0,"":
      JMP      TNZ,,+3
      IQ       SUBO,0,40
      JMP      FNZ,GTNU1
      IQ       SUBO,0,57
      JMP      F,NEG,TCPER
      IQ       SUBO,0,67
      JMP      T,NEG,TCPER
      IQQ      AND,0,7
      SLABB   ADD,WRK2,WRK2,Z,NL,F,F
      SLZBB   OR,WRK2,Z,NL,F,F
      AQB     OR,WRK2,WRK2
      JMP     F,F,GTNU1

      04116 0607441634000000
      04117 4631000002004010
      04120 4665622000000240
      04121 6031000000424122
      04122 46656220000001640
      04123 6031000000424124
      04124 06656220000001000
      04125 4631000000024117
      04126 06656220000001360
      04127 0631000000014005
      04130 06656220000001560
      04131 4631000000414005
      04132 46246400000000160
      04133 06771016340000060
      04134 46373300340000060
      04135 0607031634000000
      04136 06310000000004117

      ;GETNUM:
      ; WRK2:=0
      ; Q:= GETCHAR
      ; IF Q='LF' THEN RETURN
      ;
      ; IF Q=':' THEN RETURN
      ;
      ; IF Q='SP' THEN GOTO GTNU1
      ; IF Q<= 8'57 THEN
      ;   GOTO TCP ERROR
      ; IF Q> 8'67 THEN
      ;   GOTO TCP ERROR
      ; Q:= 21 EXT C CON Q(21:23)
      ; WRK2:=WRK2 SHIFT 2
      ; WRK2:=WRK2 SHIFT 1
      ; WRK2:=WRK2 OR Q
      ; GOTO GTNU1

      RTN
      RTN

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10094 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

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;TYPE OCTAL NUMBER PROCEDURE
;
;THE PROCEDURE TYPES THE CONTENT OF THE Q-REGISTER AS 8 OCTAL DIGITS
; REGISTERS USED: WRKU,WRK1,WRK2
      -POCT:
      ;TPOCT:
      ZQB OR,WRK1
      IZQ OR,U,10
      ZQB OR,WRK2
      TPOCT1:
      ZEB AND,WRKU
      ZAQ OR,WRK1
      DLZBB OR,WRKU,Z,NL,F,F
      DLZBB OR,WRKU,Z,NL,F,F
      DLZBB OR,WRKU,Z,NL,F,F
      ZQB OR,WRK1
      IZQ OR,U,60
      AQB OR,WRKU,WRKU
      CALL F,F,TYPE
      ZBB SUN,WRK2
      JMP T,NZ,TPOCT1
      RTN
;TYPE OCTAL:
; WRK1:= Q
; WRK2:= 8 / COUNT
; REPEAT
; WRKU:=0
; Q:=WRK1
; WRKU,Q:= WRKU,Q SHIFT 1
; WRKU,Q:= WRKU,Q SHIFT 1
; WRKU,Q:= WRKU,Q SHIFT 1
; WRK1:=Q
; Q:=8'60
; WRKU:= WRKU OR Q
; TYPE(WRKU)
; COUNT:=COUNT-1
; UNTIL COUNT = 0
; RETURN

```

```

;TYPE ADDRESS
;THE PROCEDURE TYPES THE BINARY NUMBER IN THE Q-REGISTER AS
;8 OCTAL DIGITS, TERMINATED BY: SP SP
; REGISTERS USED: WRKU,WRK1,WRK2
      TYPAD:
      CALL F,F,TPOCT
      IZQ OR,1642,0040
      JMP F,F,TPTXT
;TYPEADDRESS:
; TYPE OCTAL(Q)
; TYPETEXT(:<SP><<SP>>)

```

```

04137 4607230033CC0000
04140 46247300CC00200
04141 0607230034CC0000
04142 0607340032CC0000
04143 4604431540CC0000
04144 0636330032CC0060
04145 0636330032CC0060
04146 0636330032CC0060
04147 4607230033CC0000
04150 0624730000CC1400
04151 0607031532CC0000
04152 06310000CC2CC4104
04153 0607310034CC0000
04154 6031000000424142
04155 06310000002CC4006
04156 0624730350401000
04157 063100000000CC4013

```

10095 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

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;TYPE BINARY NUMBER
;THE PROCEDURE TYPE THE BINARY NUMBER IN THE Q-REGISTER AS
;8 OCTAL DIGITS, TERMINATED BY NL
; REGISTERS USED: WRKO,WRK1,WRK2
TPNUM: CALL F,F,TPOCT
      JMP F,F,CRLF
;TYPE 'CR' 'LF' ON TCP
      .CRLF:
      ;CRLF: IZQ OR,0,13.
      JMP F,F,TPTXT
;TYPE TEXT
;THE PROCEDURE TYPES THE THREE CHARACTERS IN THE Q-REGISTER
; REGISTER USED: WRKO,WRK1
      .PTXT:
      ;TPTXT: ZBB AND,WRKU
              DLZBB OR,WRKU,Z,NL,F,F
              DLZBB OR,WRKU,Z,NL,F,F
              DLZBB OR,WRKU,Z,NL,F,F
              DLZBB OR,WRKU,Z,NL,F,F
              DLZBB OR,WRKU,Z,NL,F,F
              DLZBB OR,WRKU,Z,NL,F,F
              DLZBB OR,WRKU,Z,NL,F,F
              DLZBB OR,WRKU,Z,NL,F,F
              ZQB OR,WRK1
              CALL F,F,TYPE
              ZAQ OR,WRK1
              JMP T,NZ,TPTXT
;TYPE NUMBER:
; TYPE OCTAL(Q)
; TYPE 'CR' 'LF'
;
;TYPE 'CR' 'LF'
; Q:= 'CR'
; GOTO TYPETEXT
;
;TYPETEXT:
; WRKU:=0
; WRKU,Q:= WRKO,Q SHIFT 8
;
; WRK1:=Q
; TYPE(WRK0)
; Q:= WRK1
; IF Q<>0 THEN GOTO TYPETEXT
; ELSE RETURN
RTN

```

10096 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

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25
;PROCEDURE SKIP SPACES UNTIL NL
;
;THE PROCEDURE READS A LINE OF SPACES UNTIL 'NL'
; REGISTER USED: WRKO,Q

SKPNL:      CALL  F,F,GCHAR
            IQ   SUBO,0,12
            JMP  T,NZ,,+3
            IQ   SUBO,0,40
            JMP  T,NZ,TCPER
            JMP  F,F,SKPNL

;CPER:
;TCPER:     IZQ  OR,"?#16.,13.
            CALL F,F,TPTXT
            JMP  F,F,IDLE

;TYPE <SP><SP>

SPSP:      IZQ  OR,1002,U
            JMP  F,F,TPTXT

;SKIPTONL:
; Q:=GETCHAR
;
; IF Q='LF' THEN RETURN
;
; IF Q<>'SP' THEN GOTO TCPERROR
; GOTO SKIPTONL

;TCPERROR:
;
; TYPETEXT(?<CK>)
; GOTO IDLE

;SPSP:
; Q:=<SP><SP>
; GOTO TYPETEXT

```

10097 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 ;GET REGISTER PROCEDURE
02
03 ;THE PROCEDURE LOADS WRKO WITH THE CONTENTS OF THE REGISTER ADDRESSED
04 ;BY WRK3, FOR NON EXISTING REGISTERS WRKU:=-1
05 ;REGISTERS USED: Q
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08	04214	4604431640CC0000	GETRG:	ZAQ	OR,WRK3	;	GETGEGISTER:
09	04215	0665622000001440		IQ	SUB0,0,62	;	Q:=WRK3
10	04216	4631000000024227		JMP	F,NZ,GTRTC	;	IF Q=8'62 THEN GOTO GTRTC
11	04217	4665622000001400		IQ	SUB0,0,60	;	;
12	04220	0631000000024232		JMP	F,NZ,GDSWT	;	IF Q=8'60 THEN GOTO GET DATA SWITCH
13	04221	0665622000001420		IQ	SUB0,0,61	;	;
14	04222	4631000000024233		JMP	F,NZ,GRSWT	;	IF Q=8'61 THEN GOTO GET REG SWITCH
15	04223	4665610000000640		IQ	SUN,0,32	;	;
16	04224	0631000000014234		JMP	F,NEG,DEFLT	;	IF Q>8'32 THEN GOTO DEFLT
17	04225	0624730000105040		IZQ	OR,0,GRTAB/3	;	Q:= ADDRESS(GRTAB);
18	04226	4631001641000000		JXAQ	ADD,WRK3,F,F	;	GOTO (Q+WRK3)

  

22	04227	4605431500000017	GTRTC:	ZAR	OR,WRKU,RTCR	;	GETRTC:
23	04230	4705230000000000		ZQ	OR	;	ADDR=62, RTCREAD:=DUMMY
24	04231	6017730032000200		XZB	OR,RTC,WRKO	;	DELAY MIN 200 NSEC
25	04232	6003730032001600		SZB	OR,DATSW,WRKO	;	WRKC:=KTC
26	04233	2003730032001700		SZB	OR,REGSW,WRKO	;	ADDR=60, WRKU:= DATSW
27	04234	6007121532000000		DEFLT:	ABB	;	ADDR=61, WRKC:= REGSW
28	04235	0624730000002020		VERSION:	IZQ	;	UNKNOWN ADDR, WRKU:= -1
29	04236	06055414000005000		RA	AND,SWTCH,.2	;	;
30	04237	4631000000024241		JMP	F,NZ,VERS1	;	WRKU:= IF CAM AVAILABLE THEN 65.
31	04240	0624730000001700		IZQ	OR,0,60.	;	;
32	04241	6007230032000000		ZQB	OR,WRKU	;	ELSE 60.



10099 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

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27
;PUT REGISTER PROCEDURE
;THE PROCEDURE LOADS THE REGISTER ADDRESSED BY WRK3 WITH THE CONTENTS
;OF THE WRK0-REGISTER
PUTRG:      ZAQ      OR,WRK3
            IQ       SUBO,0,57
            JMP      F,NZ,PUT57
            IQ       SUBO,0,60
            JMP      F,NZ,PUT60
            IQ       SUN,0,32
            JMP      T,NEG,+,3
            SLZBB   OR,WRK3,Z,NL,F,F
            IQ       OR,0,PRTAB/3
            JXAG    ADD,WRK3,F,F
PUT57:      ZAR      OR,WRK0,INTRG
PUT60:      ZAQ      OR,WRK0
            JMP      F,F,TPNUM
PRTAB:      ZAB      OR,WRKU,W0
            ZQ       OR
            ZAB      OR,WRKU,W1
;PUTREGISTER:
; Q:=WRK3
; IF Q= 8'57 THEN GOTO PUT57
;
;
; IF Q= 8'60 THEN GOTO PUT60
; IF Q> 8'32 THEN RETURN
;
; WRK3:= WRK3 SHIFT 1
; Q:= ADDRESS(PRTAB);
; GOTO (Q+WRK3)
;CLEAR INTERRUPT;
;ADDR=57, INTRG:=WRK0
;DISPLAY NUMBER
;
; TYPE WRKU ON TCP
;PRTAB:
;ADDR= 0, WU:=WRKU
;
;      1, W1:=WRKU
RTN
RTN
RTN
RTN

```





!0101 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 04360 2041421500CC1702      ZAS SUB,WRKU,REGSW      RTN      ;
02 04361 0605230000CC0000      ZQ  OR                  ;
03 04362 6041421500CC0702      ZAS SUB,WRKU,CUR       RTN      ;
04 04363 0605230000CC0000      ZQ  OR                  ;
05 04364 6041421500CC1002      ZAS SUB,WRKU,PUIX     RTN      ;
06 04365 0605230000CC0000      ZQ  OR                  ;
07 04366 2041421500CC1102      ZAS SUB,WRKU,PUTAB    RTN      ;
08 04367 0605230000CC0000      ZQ  OR                  ;
09 04370 2041421500CC1202      ZAS SUB,WRKU,EXOFS    RTN      ;
10 04371 0605230000CC0000      ZQ  OR                  ;
11 04372 6041421500CC1302      ZAS SUB,WRKU,DMOFS    RTN      ;
12 04373 0605230000CC0000      ZQ  OR                  ;
13 04374 2041421500CC1402      ZAS SUB,WRKU,SWRKO    RTN      ;
14 04375 0605230000CC0000      ZQ  OR                  ;
15 04376 6041421500CC1502      ZAS SUB,WRKU,SWRK1    RTN      ;
16 04377 0605230000CC0000      ZQ  OR                  ;

```

;EXAMINE REGISTER

;DISPLAYS THE CONTENTS OF THE ADDRESSED REGISTER AS 8  
;SOCIAL DIGITS ON THE TCP DISPLAY

XR:

```

25 04400 0631000002004014      CALL F,F,GTNUM        ;XR:
26 04401 46135516350C3500      CAB CAND,180,WRK2,WRK3 ; WRK2:=GETOCTAL
27 04402 46310000020C4212      CALL F,F,SPSP        ; WRK3:= U EXT 1 CON WRK2(1:23)
28 04403 46310000020C4214      CALL F,F,GETRG       ; TYPE(<SP><SP>)
29 04404 0604431500CC0000      ZAQ OR,WRKU          ; WRKU:=GETREGISTER(WRK3)
30 04405 46310000020C4160      CALL F,F,TPNUM       ; Q:= WRKU
31 04406 463100000000000020      JMP F,F,IDLE        ; TYPE NUMBER(Q)
                               ; GOTO IDLE

```

101C2 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

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;LOAD REGISTER
;THE ADDRESSED REGISTER IS LOADED WITH THE OCTAL NUMBER
;TYPED ON THE TCP
LR:
      0631000002CC4014      CALL F,F,GTNUM
      4631000002CC4212      CALL F,F,SPSP
      06074316335CC0000      ZAB OR,WRK2,WRK3
      0631000002CC4014      CALL F,F,GTNUM
      46074316332CC0000      ZAB OR,WRK2,WRK0
      0631000002CC4275      CALL F,F,PUTRG
      4631000000CC0020      JMP F,F,IDLE
;RUN
;STARTS MICROINSTRUCTION EXECUTION FROM ADDRESSED CONTROL
;STORE LOCATION
R:
      0631000002CC4014      CALL F,F,GTNUM
      0631431603CC0000      CXZA OR,WRK2,F,F
      4631000000CC0020      JMP F,F,IDLE
;LR:
; WRK2:=GETOCTAL
; TYPE(<SP><SP>)
; WRK3:=WRK2
; WRK2:=GETOCTAL
; WRK0:=WRK2
; PUTREGISTER(Q,WRK3)
; GOTO IDLE
;RUN:
; WRK2:=GETOCTAL
; CALL (WRK2)
; GOTO IDLE

```

101C3 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

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;LOAD MEMORY
;LOADS THE ADDRESSED MEMORY LOCATION WITH THE OCTAL NUMBER
;TYPED ON THE TCP
;THE ADDRESS IS SAVED IN WRK5

LM:
04421 0631000002004014 CALL F,F,GTNUM
04422 4607431637000000 ZAB OR,WRK2,WRK5
04423 4631000002004212 CALL F,F,SPSP
04424 4631000000004431 JMP F,F,LDMEM

;LOAD NEXT MEMORY
;LOADS THE MEMORY LOCATION ADDRESSED BY WRK5+2 WITH THE OCTAL
;NUMBER TYPED ON THE TCP
;THE ADDRESS IS SAVED IN WRK5

LN:
04425 0631000002004201 CALL F,F,SKPNL
04426 4653503777003700 CAB ADD0,,1,WRK5,WRK5
04427 0604431740000000 ZAQ OR,WRK5
04430 4631000002004155 CALL F,F,TYPAD

LDMEM:
04431 0631000002004014 CALL F,F,GTNUM
04432 4605431600000012 ZAN OR,WRK2,DATO
04433 0605431740230011 ZAR OR,WRK5,LAD
04434 0654730000000100 XZQ OR,CIOST
04435 4624640000000160 IQQ AND,0,7
04436 46310000000424005 JMP T,NZ,TCPER
04437 46310000000000020 JMP F,F,IDLE

;LN:
; SKIP TO NL
; WRK5:= WRK5+2
; Q:=WRK5
; TYPE ADDRESS(Q)
;LOAD MEMI
; WRK2:=GETOCTAL
; DATAOUT:=WRK2
; LAD:=WRK5, WRT
; Q:=CPUIOSTATUS
; IF Q(21:25)<>0 THEN
; GOTO TCP ERROR
; GOTO IDLE

CONT WRT
CONT U 1

```

10104 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

01 ;EXAMINE MEMORY

02 ;DISPLAYS THE CONTENTS OF THE ADDRESSED MEMORY LOCATION

03 ;THE ADDRESS IS SAVED IN WRK5

04 XMM:

05 ; WRK2:=GETOCTAL

06 CALL F,F,GTNUM

07 ZAB OR,WRK2,WRK5

08 CALL F,F,XMEM

09 JMP F,F,IDLE

10 ;

11 ;EXAMINE NEXT

12 ;DISPLAYS THE CONTENTS OF THE NUMBER OF CONSECUTIVE MEMORY LOCATIONS

13 ;TYPED ON THE TCP, STARTING AT THE LOCATION ADDRESSED BY WRK5+2

14 ;THE LAST ADDRESS IS SAVED IN WRK5

15 XN:

16 ; WRK2:=GETOCTAL

17 CALL F,F,GTNUM

18 ZAB OR,WRK2,WRK4

19 JMP F,NZ,IDLE

20 ADD,-2,WRK5

21 F,F,XMEM

22 SUN,WRK4

23 JMP F,NZ,IDLE

24 JMP T,CPUIN,INTIN

25 JMP F,F,XNEXT

26 ;XN:

27 ; WRK2:=GETOCTAL

28 ; WRK4:=WRK2

29 ; IF WRK4=0 THEN GOTO IDLE

30 ; WRK5:=WRK5+2

31 ; EXAMINE(WRK5)

32 ; WRK4:=WRK4-1

33 ; IF WRK4=0 THEN GOTO IDLE

34 ; IF CPUINTEERRUPT THEN GOTO INTIN

35 ; GOTO XNEXT

08 06310000020004014

09 0607431637000000

10 06310000020004455

11 06310000000000020

20 06310000020004014

21 0607431637000000

22 063100000000020020

23 0607101437000000

24 06310000020004455

25 0607310036000000

26 063100000000020020

27 0631000000000600023

28 06310000000004447

10105 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

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01
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07 04455 0604431740210011
08 04456 4631000002004155
09 04457 46547300000004100
10 04460 46246400000000160
11 04461 46310000000424005
12 04462 46147300000004000
13 04463 06310000000004160

;DISPLAY MEMORY ADDRESS AND THE CONTENTS OF THE MEMORY LOCATION
;ADDRESSED BY WKKS
XMEM:
04455 0604431740210011 04455 0604431740210011
04456 4631000002004155 04456 4631000002004155
04457 46547300000004100 04457 46547300000004100
04460 46246400000000160 04460 46246400000000160
04461 46310000000424005 04461 46310000000424005
04462 46147300000004000 04462 46147300000004000
04463 06310000000004160 04463 06310000000004160

;DISP:
;LAD:=Q:=WKKS, READ
;TYPE ADDRESS(Q)
;Q:=IOSTATUS
;IF Q(21:23)<>0 THEN
;GOTO TCP EKROR
;Q:=DATAIN
;TYPE NUMBER(Q)
```

10106 CPU823 MICROPROGRAM LBJ 850E16 VERSION 8

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;SINGLE INSTRUCTION
;EXECUTES THE INSTRUCTION ADDRESSED BY IC AND RETURNS TO IDLE LOOP
;THE CONTENTS OF W0, W1, W2, W3, STAT, IC, CAUSE, AND SB IS DISPLAYED

S:
08 C4464 0631000002004201      CALL F,F,SKPNL
09 C4465 4613551471001600      CAB CAND,.1B19,CNTR,CNTR
10 C4466 4613531471002006      CABR OR,.1B17,CNTR,CNTR,CNTR0
11 C4467 4604431200000000      ZAQ OR,STAT
12 C4470 4665622000005000      IQ SUB0,0,0240
13 C4471 463100000022001      JMP F,NZ,DISPA
14 C4472 4631000000004500      JMP F,F,C1

;CONTINUE
;STARTS INSTRUCTION EXECUTION IN THE LOCATION ADDRESSED BY IC,
; OR FROM THE DISPATCHING POINT

C:
22 C4473 0631000002004201      CALL F,F,SKPNL
23 C4474 4613531471002006      CABR OR,.1B17,CNTR,CNTR,CNTR0
24 C4475 4604431200000000      ZAQ OR,STAT
25 C4476 4665622000005000      IQ SUB0,0,0240
26 C4477 463100000022001      JMP F,NZ,DISPA

C1:
28 C4500 460573000241011      RZR OR,ICS,LAD          CONT READJ
29 C4501 4613551471001506      CABR CAND,.4,CNTR,CNTR,CNTR0
30 C4502 3455730000410100      XZ OR,CIOST          NEXT TSTLI 1 ; GOTO NEXT INSTRUCTION

;S:
; SKIP TO NL
; CNTR:= CNTR AND -,BIT19 (SINGLE INST)
; CNTR,CNTR0:= CNTR OR 1B17 (RUN);
; IF STAT = 8'240 THEN
;
; GOTO DISPATCHING POINT;
; ELSE GOTO C1;

;C:
; SKIP TO NL
; CNTR,CNTR0:= CNTR OR 1B17 (RUN)
; IF STAT = 8'240 THEN
;
; GOTO DISPATCHING POINT;
;C1:
; LAD:=ICS, READJ
; CNTR,CNTR0:= CNTR CAND 1B21;

```

101C7 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 ;EXAMINE DYNAMIC REGISTERS
02 ;DISPLAYS THE CONTENTS OF THE DYNAMIC REGISTERS
03
04
05
06 ;XD:
07 04503 063100000C20C04201 CALL F,F,SKPNL ; SKIP TO NL
08 04504 463100000C20C04515 CALL F,F,TPDYN ; TYPE DYNAMIC REGISTERS
09 04505 463100000C00C00020 JMP F,F,IDLE ; GOTO IDLE
10
11 ;EXAMINE STATIC REGISTERS
12 ;DISPLAYS THE CONTENTS OF THE STATIC REGISTERS
13
14
15 ;XST:
16 04506 063100000C20C04201 CALL F,F,SKPNL ; SKIP TO NL
17 04507 463100000C20C04540 CALL F,F,TPSTA ; TYPE STAT. REGISTERS
18 04510 463100000C00C00020 JMP F,F,IDLE ; GOTO IDLE
19
20
21 ;SINGLE INSTRUCTION INTERRUPT
22
23 ;INGL:
24 ;SINGL: CAB CAND,1B17,CNTR,CNTR ; SINGLE INSTR. INT:
25 04511 46135514710C2000 CABR OR,1B19,CNTR,CNTR,CNTR ; CNTR:=CNTR CAND BIT17 (RUN:=0)
26 04512 46135514710C1606 CALL F,F,TPDYN ; CNIR:=CNIRU:=CNTR OR BIT19
27 04513 463100000C20C04515 JMP F,F,IDLE ; (-)SINGLE INSTR INT:=1)
28 04514 463100000C00C00020 ; TYPE DYNAMIC REGISTERS
29

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10108 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

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07 04515 4607340035000000
08 04516 4624730000112600
09 04517 0631001643000000
10 04520 463100002004013
11 04521 463100002004212
12 04522 463100002004214
13 04523 060443150000000
14
15 04524 463100002004160
16 04525 4607302035000000
17 04526 4651523640003400
18 04527 203100000014516
19
20
21 04530 2024730534601640
22 04531 6024730534611640
23 04532 6024730534621640
24 04533 2024730534631640
25 04534 2024730515241640
26 04535 6024730445031640
27 04536 2024730415231640
28 04537 6024730515021640

;TYPE DYNAMIC REGISTERS
;THE PROCEDURE TYPES THE CONTENTS OF THE 8 DYNAMIC REGISTERS
;TYPE DYN:
; FOR WRK3:=U STEP 1 UNTIL 7 DO
; BEGIN
; Q:=ADDRESS(DYTXT)
; Q:=REGTXT(Q+WRK3)
; TYPE TEXT(Q)
; TYPE(<SP><SP>)
; WRKU:=GETREGISTER(WRK3)
; Q:= WRKU
;
; TYPE NUMBER(Q)
; END;
;
;REGISTER TEXT
; Q:=W0:
; Q:=W1:
; Q:=W2:
; Q:=W3:
; Q:=ST:
; Q:=IC:
; Q:=CS:
; Q:=SB:

```

```

TPDYN: ZBB AND,WRK3
TPDY1: IZQ OR,U,DYTXT/3
CXAQ ADD,WRK3,F,F
CALL F,F,TPXT
CALL F,F,SPSP
CALL F,F,GETRG
ZAQ OR,WRKO
CALL F,F,TPNUM
ZBB ADD,WRK3
CA SUB0,7,WRK3
JMP F,NEG,TPDY1
RTN

DYTXT: IZQ OR,2563,0072
IZQ OR,2563,0472
IZQ OR,2563,1072
IZQ OR,2563,1472
IZQ OR,2465,2072
IZQ OR,2224,1472
IZQ OR,2065,1472
IZQ OR,2464,1072
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN

```



101C9 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 ;TYPE STATIC REGISTERS
02 ;
03 ;THE PROCEDURE TYPES THE CONTENTS OF THE STATIC REGISTERS
04 ;
05 ;
06 ;
07 ; FOR WRK4:=0 STEP 1 UNTIL 8 DO
08 ; BEGIN
09 ;   Q:=ADDRESS(STTXT);
10 ;   Q:=REGTXI(WRK4+Q)
11 ;   TYPE TEXT(Q)
12 ;   TYPE(<SP><SP>)
13 ;   WRKU:=GETREG(WRK4+8)
14 ;   Q:=WRKU
15 ;   TYPE NUMBER(Q)
16 ;
17 ;
18 ;
19 ; END

```

```

20 ;REGISTER TEXT
21 ; 10: Q:=CP:
22 ; 11: Q:=BA:
23 ; 12: Q:=LL:
24 ; 13: Q:=UL:
25 ; 14: Q:=IL:
26 ; 15: Q:=IN:
27 ; 16: DUMMY
28 ; 17: Q:=SI:
29 ; 20: Q:=MO:
30 ; 21: DUMMY
31 ; 22: DUMMY
32 ; 23: DUMMY
33 ; 24: Q:=CU:
34 ; 25: Q:=PX:
35 ; 26: Q:=PT:
36 ;
37 ;
38 ;
39 ;
40 ;

```

TPSTA: ZBB AND,WRK4  
 CZB OR,.8,WRK3  
 TPST1: IZQ OR,0,STTXT/3  
 CXAQ ADD,WRK4,F,F  
 CALL F,F,TPTXT  
 CALL F,F,SPSP  
 CALL F,F,GETRG  
 ZAQ OR,WRKO  
 CALL F,F,TPNUM  
 TPST2: ZBB ADDO,WRK3  
 ZBB ADDO,WRK4  
 JMP F,F,TPST1

STTXT:

```

21 04554 2024700415201640 OR,"C*16.,+("P/16.),"P*256.+": RTN
22 04555 6024700411011640 OR,"B*16.,+("A/16.),"A*256.+": RTN
23 04556 6024700461141640 OR,"L*16.,+("L/16.),"L*256.+": RTN
24 04557 2024700525141640 OR,"U*16.,+("L/16.),"L*256.+": RTN
25 04560 6024700445141640 OR,"I*16.,+("L/16.),"L*256.+": RTN
26 04561 2024700445161640 OR,"I*16.,+("N/16.),"N*256.+": RTN
27 04562 0631000000004575 F,F,TPSKP
28 04563 2024700515111640 OR,"S*16.,+("I/16.),"I*256.+": RTN
29 04564 2024700465171640 OR,"M*16.,+("O/16.),"O*256.+": RTN
30 04565 0631000000004575 F,F,TPSKP
31 04566 0631000000004575 F,F,TPSKP
32 04567 0631000000004575 F,F,TPSKP
33 04570 2024700415251640 OR,"C*16.,+("U/16.),"U*256.+": RTN
34 04571 2024700501301640 OR,"P*16.,+("X/16.),"X*256.+": RTN
35 04572 2024700501241640 OR,"P*16.,+("T/16.),"T*256.+": RTN
36 04573 0005230000000000 ZQ
37 04574 6005230000000000 OR
38
39 04575 0005230000000000 TPST2: ZQ
40 04576 063100000000004551 JMP F,F,TPST2

```

10110 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01      ;EXAMINE WORK REGISTERS
02
03      ;DISPLAYS THE CONTENTS OF THE WORKING REGISTERS
04      XW:
05          CALL F,F,SKPNL
06          ZAS SUB,WRK1,SWRK1
07          ZAS SUB,WRK2,DATSW
08          ZAS SUB,WRK3,REGSW
09          ZBB AND,WRK3
10          IZQ OR,U,WRKREG/J
11          CXAQ ADD,WRK3,F,F
12          CALL F,F,TPNUM
13          NSZBB ADDO,WRK3,NL,T,F
14
15          ;XW:
16          ; SKIP TO NL
17          ; SAVE WRK1 IN SWRK1
18          ; SAVE WRK2 IN DATSW
19          ; SAVE WRK3 IN REGSW
20          ; FOR WRK3:= 0,WRK3+1 WHILE TRUE DO
21          ; BEGIN
22          ;   Q:= GETWORK(Q,WRK3)
23          ;   TYPENUMBER(Q)
24          ; END
25
26      WRKREG:
27          SZQ OR,SWRKU
28          SZQ OR,SWRK1
29          SZQ OR,DATSW
30          SZQ OR,REGSW
31          ZAQ OR,WRK4
32          ZAQ OR,WRK5
33          JMP F,F,IDLE
34
35          RTN
36          RTN
37          RTN
38          RTN
39          RTN
40          RTN
41          RTN
42
43      ;WRKREG:
44      ; Q:= SWRKU
45      ; Q:= SWRK1
46      ; Q:= DATSW
47      ; Q:= REGSW
48      ; Q:= WRK4
49      ; Q:= WRK5
50      ;

```



10112 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 04666 06247300000000740
04 04667 06052300000000006
05 04650 06310000000255002
06 04651 06310000000046640
07
08 04652 06247300000000720
09 04653 06052300000000006
10 04654 06310000000255002
11 04655 06310000000046662
12
13 04656 46247300000000700
14 04657 06052300000000006
15 04660 06310000000255002
16 04661 0631000000004727
17
18
19 04662 06647037777777760
20 04663 0631000000424652
21 04664 066471177777760
22 04665 0631000000424652
23 04666 4624730000000020
24 04667 4664622000000020
25 04670 0631000000424652
26 04671 0624730000000000
27 04672 0631000000424652
28 04673 062473177777760
29 04674 4605202000000000
30 04675 0631000000424652
31 04676 4624630000000000
32 04677 4605202000000000
33 04700 0631000000424652
34 04701 462463177777760
35 04702 4605202000000000
36 04703 0631000000424652

;IMOPER:
; TURN ERRORLAMP 3 ON
;
; IF OCPAUTOLOAD THEN GOTO POWU1
; GOTO IMLUP
;ARITHMETIC-LOGIC ERROR:
; TURN ERRORLAMP 2 ON
;
; IF OCPAUTOLOAD THEN GOTO POWU1
;
;ALUE:
; TURN ERRORLAMPS 2,3 ON
;
; IF OCPAUTOLOAD THEN GOTO POWU1
; GOTO ALUTS
;ARITHMETIC-LOGIC TEST:
; TEST (-1)+0 +1
; TEST -(-1)+0 -1
; TEST 1-(1)
; TEST 0 OK 0
; TEST -1 OR 0
; TEST 0 OR -1
; TEST -1 OR -1
;

IMOER: IZQ OK,0,36
      ZQR OR,CNTRO
      JMP F,NOCPA,POWU1
      JMP F,F,IMLUP

ARLOER: IZQ OR,0,35
      ZQR OR,CNTRO
      JMP F,NOCPA,POWU1
      JMP F,F,ARLOT

ALUER: IZQ OR,0,34
      ZQR OR,CNTRO
      JMP F,NOCPA,POWU1
      JMP F,F,ALUTS

ARLOT: IZQ ADD0,7777,7777
      JMP T,NZ,ARLOE
      IZQ SUN,7777,7777
      JMP T,NZ,ARLOE
      IZQ OR,U,1
      IQQ SUB0,U,1
      JMP T,NZ,ARLOE
      IZQ OR,U,0
      JMP T,NZ,ARLOE
      IZQ OK,7777,7777
      ZQ ADD0
      JMP T,NZ,ARLOE
      IQQ OR,0,0
      ZQ ADD0
      IQQ T,NZ,ARLOE
      ZQ OK,7777,7777
      JMP ADD0
      ZQ T,NZ,ARLOE
      JMP

```

10113 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 04704 4624740000000000 AND,U,U
02 04705 0631000000424652 T,NZ,ARLOE
03 04706 462474177777760 AND,7777,7777
04 04707 0631000000424652 T,NZ,ARLOE
05 04710 062473177777760 OR,7777,7777
06 04711 0624640000000000 AND,O,U
07 04712 0631000000424652 T,NZ,ARLOE
08 04713 062473177777760 OR,7777,7777
09 04714 062464177777760 AND,7777,7777
10 04715 4605202000000000 ADDO
11 04716 0631000000424652 T,NZ,ARLOE
12 04717 0624750000000000 CAND,U,U
13 04720 0631000000424652 T,NZ,ARLOE
14 04721 062475177777760 CAND,7777,7777
15 04722 0631000000424652 T,NZ,ARLOE
16 04723 062473177777760 OR,7777,7777
17 04724 4624650000000000 CAND,O,O
18 04725 4605202000000000 ADDO
19 04726 0631000000424652 T,NZ,ARLOE
20
21
22 04727 4624730000000020 OR,U,1
23
24 04730 0607230032000000 OR,WRKO
25 04731 0607023532000000 SUBO,WRKO,WRKO
26 04732 4631000000424656 T,NZ,ALUER
27 04733 4607230033000000 OR,WRK1
28 04734 0607023573000000 SUBO,WRK1,WRK1
29 04735 4631000000424656 T,NZ,ALUER
30 04736 0607230034000000 OR,WRK2
31 04737 0607023634000000 SUBO,WRK2,WRK2
32 04740 4631000000424656 T,NZ,ALUER
33 04741 4607230035000000 OR,WRK3
34 04742 0607023675000000 SUBO,WRK3,WRK3
35 04743 4631000000424656 T,NZ,ALUER
36 04744 4607230036000000 OR,WRK4
37 04745 0607023736000000 SUBO,WRK4,WRK4
38 04746 4631000000424656 T,NZ,ALUER
;
```

```

; TEST 0 AND 0
; TEST -1 AND 0
; TEST 0 AND -1
; TEST -1 AND -1
; TEST -,U AND 0
; TEST -, -1 AND 0
; TEST -, U AND -1
; ALU TEST:
; Q:= 1
; ALU-REGISTER TEST:
; WHILE Q<>0 DO
; BEGIN
; FOR REG:= WRKO,,WRK5 DO
; BEGIN
; REG:= Q
; IF Q=REG<>0 THEN
; ERROR RETURN
; END
;
```

```

ALUTS:
ALURG:
ZQB
AGB
JMP
AGB
JMP
ZQB
AGB
JMP
ZQB
AGB
JMP
ZQB
AGB
JMP
ZQB
AGB
JMP
ZQB
AGB
JMP
ZQB
AGB
JMP
ZQB
AGB
JMP
```



10115 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 C5002 4613730024003505
04 C5003 4631000000675003
05 C5004 0624730140000000
06 C5005 0631000002004012
07 C5006 4631000002004016
08 C5007 4631000002005347
09 C5010 463100000255030
10 C5011 4604730000005000
11 C5012 4624640000004000
12 C5013 463100000025030
13 C5014 4614730000000100
14 C5015 0625640076000000
15 C5016 0631000000425030
16 C5017 0611730000201311
17 C5020 4654730000004100
18 C5021 4624640000000160
19 C5022 0631000000425237
20 C5023 0615730000004003
21 C5024 0615730000244011
22 C5025 4613531471002006
23 C5026 4613551471001506
24 C5027 7405230000000000
25
26
27
28 C5030 4613531471002006
29 C5031 0631000000255031
30 C5032 06247300002000200
31 C5033 06052300000000021
32 C5034 46310000000000020

; END C. CONTINUE WITH POWER UP
; STAT:=CPUS1:= 180 , MONMODE
; WAIT UNTIL NOT POWER LOW
;
; DELAY(600 MSEC) FOR MEMORY POWER ON
; CLEAR ALL INTERRUPTS
; INIT CAM
; IF -,OCPAUTOLOAD OR SWITCH(16) THEN
; BEGIN C. RESTART
;
;
; IF DEVICENUMBER=0 THEN
; BEGIN
; LAD:= 10 , READP
;
; IF IOSTATUS(21:23)<>0 THEN
; GOTO SYSTEMFAULT3
; ICD:= DAIN
; LAD:= DAIN , READ INSTRUCTION
; RUN:= 1
;
; GOTO NEXT INSTR
; END; END;
; NOT RESTART;
; C. OCP AUTOLOAD OR SEM COND MEMORY
; RUN:=1
; WAIT UNTIL AUTOLOAD PB IS RELEASED
;
; ILIM:= 8.; ENABLE START
; GOTO IDLE

```

POWU1:

```

CZBR OR,,180,STAT,CPUST
JMP T,PLOW,,
IZQ OR,600,0
CALL F,F,DELAY
CALL F,F,CINTR
CALL F,F,CAMIN
JMP F,NOCPA,NREST
RZQ OR,SWTCH
IQQ AND,0,200
JMP F,NZ,NREST
XZQ OR,CIOST
IQ AND,U37U,0
JMP T,NZ,NREST
CZR OR,,10,LAD
XZQ OR,IOSTA
IQQ AND,U,7
JMP T,NZ,SYFL3
XZR OR,DATI,ICD
XZR OR,DATI,LAD
CABR OR,,1B17,CNTR,CNTR,CNTR
CABR CAND,,4,CNTR,CNTR,CNTR
ZQ OR

```

```

CONT READP
CONT U 1

CONT READJ
CONT U 1

```

NREST:

```

CABR OR,,1B17,CNTR,CNTR,CNTR
JMP F,NOCPA,,
IZQ OR,8,,8.
ZQR OR,ILIM
JMP F,F,IDLE

```





10117 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```
01
02
03 C5065 0613531471002106
04 C5066 0624730000000560
05 C5067 0631000002004012
06 C5070 0613551471002106
07 C5071 46247300000007660
08 C5072 4631000000004012

SYSRS: CABR OR, .1B16, CNTR, CNTR, CNTR, CNTR, CNTR
IZQ OR, 0, 27
CALL F, F, DELAY
CABR CAND, .1B16, CNTR, CNTR, CNTR, CNTR, CNTR, CNTR, CNTR
IZQ OR, 0, 373
JMP F, F, DELAY

;SYSTEM RESET:
; CPU SYSTEM RESET:= 1
;
; DELAY(9 MICROSEC)
; CPU SYSTEM RESET:= 0
;
; DELAY(100 MICROSEC)
```

10118 CPU823 MICROPROGRAM LBJ 850E16 VERSION 8

```

01
02
03 ;AUTOLOAD SEQUENCES
04 ;*****
05 ;OCP AUTOLOAD
06
07
08
09 C5073 4653720031002006
10 C5074 463100000255074
11 C5075 461473000000100
12 C5076 062564007600000
13 C5077 463100000424000
14 C5100 4631000002005065
15 C5101 462473000000200
16 C5102 0605640000005000
17 C5103 4631000000024015
18 C5104 4631000002005347
19 C5105 4631000000006001
20
21
22 C5106 4613531471003406
23 C5107 462473177777300
24 C5110 0631000000005115
25
26
27
28
29
30 C5111 0613551471003606
31 C5112 0613531471003606
32 C5113 4631000002005065
33 C5114 062473177777700

;OCP AUTOLOAD:
; CNTR:=CNTR0:= 8,77
; WAIT UNTIL AUTOLOAD PB IS RELEASED
; IF DEVICENO <> 0 THEN
;   GOTO POWUP;
; < * FIRST PU DOES THE AUTOLOAD * >
; SYSTEM RESET
; IF FRONT SWITCH=1 THEN
;   INIT CAM
;   GOTO AUTOTEST
;
;
;
; RETURN FROM AUTOLOADTEST
; CLEAR ERROR LAMPS
; Q:= 14-4*10 , DEV BASE FOR DISC
; GOTO COMMON AUTOLOAD SEQUENCE

;REMOTE AUTOLOAD:
; RESET REMOTE AUTOLOAD
;
; SYSTEM RESET
; Q:=14 - 2*10 , DEV BASE FOR FPA

```

10119 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 C5115 U607230034000000
04 C5116 4631000002005044
05 C5117 4631000002005347
06 C5120 4613730024001705
07 C5121 0624730000020540
08 C5122 4605230000000016
09 C5123 0641220000000002
10 C5124 4605230000000015
11 C5125 06412200000000302
12 C5126 0605230000000014
13 C5127 46412200000000202
14 C5130 46050134000000003
15 C5131 06247300000020320
16 C5132 4607230027000000
17 C5133 06247300777777740
18 C5134 06412200000000502
19 C5135 46247300000077760
20 C5136 06412200000000602
21
22 C5137 4613730037003400
23 C5140 4613730033003600
24 C5141 0607340032000000
25 C5142 4251712000003004
26 C5143 4624730000124300
27 C5144 4631001503000000
28 C5145 0605230000000012
29 C5146 4647101433230011
30 C5147 0655541740000100
31 C5150 4631000000425241
32 C5151 2637302032114060
33
34

```

AUTOL:

```

ZQB OR,WRK2
CALL F,F,INITR
CALL F,F,CAMIN
CZBR OR,,1B18,STAT,CPUST
IZQ OR,0,1026
ZQR OR,CPA
ZQS SUB,CPAC
ZQR OR,ULIM
ZQS SUB,ULIMC
ZQR OR,LLIM
ZQS SUB,LLIMC
AGR SUNO,-2,ICD
IZQ OR,0,1015
ZQB OR,INF
IZQ OR,3777,7776
ZQS SUB,SIZE
IZQ OR,0,3777
ZQS SUB,MONT

```

```

CZB OR,,7,WRK5
CZB OR,,8,WRK1
ZBB AND,WRKU
CZR SUNO,-6,LCD
IZQ OR,0,BOOTL/3
CXAQ ADD,WRKU,F,F
ZQR OR,DATO
ABBR ADD,-2,WRK1,LAD
XA AND,CPUST,WRK5
JMP T,NZ,SYFL4
NSZBB ADD,WRKU,NL,F,LCN

```

```

PUSH
CONT WRT
CONT 0 1
LRIN

```

COMMON AUTOLOAD SEQUENCE:

```

; WRK2:=Q, DEV BASE
; INIT REGISTERS
; INIT CAM
; STAT,CPUST:= 1B18; NO PROCESS ACTIVE
; CPA:=CPAC:= 512.+22.
; ULIM:=ULIMC:= 521.+22.
; LLIM:=LLIMC:= 512.+22.
; ICD:= 521.+22.-2
; INF:= 512.+22.-9.
; SIZE:= 8'37777776
; MONTOP:= 8'3777
; WRK5:= 7
; WRK1:= 8;
; WRKU:= 0
; FOR LCD:= -6 STEP 1 UNTIL 0 DO
; BEGIN
; Q:=ADDR(BOOTL+WRKU)
; DATAOUT:= Q
; LAD:=WRK1:= WRK1+2 , WRT
; IF CPUIOSTATUS(21:23)<0 THEN
; GOTO SYSTEMFAULT4
; WRKU:= WRKU+1
; END

```

10120 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```
01
02 05152 06247303220C0000      IZQ   OR,151U,0
03 05153 0605230000C0012     ZGR   OK,DATO
04 05154 0605730000231011     RZR   OR,ICS,LAD
05 05155 463100000020C4016     CALL  F,F,CINTR
06 05156 0655541740C00100     XA    AND,CIOST,WRK5
07 05157 4631000000425241     JMP   T,NZ,SYFL4
08 05160 4624731000C0300     IZQ   OR,40UU,14
09 05161 4604013600C0000     AGQ   SUNO,WRK2
10 05162 463100000020C5207     CALL  F,F,WAUTO
11 05163 0655541740C00100     XA    AND,CIOST,WRK5
12 05164 4631000000425171     JMP   T,NZ,AUTO1
13
14
15 05165 4605730000241011     RZR   OR,ICS,LAD
16 05166 46135314710C2006     CABR  OR,1B17,CNTR,CNTR,CNTR0
17 05167 46135514710C1506     CABR  CAND,4,CNTR,CNTR,CNTR0
18 05170 7405230000C00000     ZQ    OR
19
20
21 05171 46656230000C1000     IQ    SUBO,4000,40
22 05172 0631000000425243     JMP   T,NZ,SYFL5
23
24
25 05173 0624731000C0200     IZQ   OR,400U,10
26 05174 46137300340C1500     CZB   OR,4,WRK2
27 05175 463100000020C5207     CALL  F,F,WAUTO
28 05176 0655541740C00100     XA    AND,CIOST,WRK5
29 05177 0631000000C25165     JMP   F,NZ,AUTO0

; DATA:
; Q:= 8'15100000 ( JL. 0)
; DATAOUT:= Q
; LAD:= ICS , WRT
; CLEAR ALL INTERRUPTS
; IF CPU10STATUS(21:23)<>0 THEN
;   GOTO SYSTEMFAULT4
; Q:= 8'40000014
; Q:= 4000000U+DEV NO*10;
; SEND AUTO(BASE,ADDRESS);
; IF CPU10STATUS(21:23)=0 THEN
;   BEGIN
;     LAD:= ICS , READ INSTRUCTION
;     RUN:= 1
;     GOTO NEXT INSTR
;   END ELSE
;   IF Q<>DISC ADDR THEN
;     GOTO SYSTEMFAULT5
; Q:= 40000010;
; WRK2:= 4;
; SEND AUTO(WRK2,Q);
; IF CPU10STATUS(21:23)=0 THEN
;   GOTO AUTO0;

CONT WRT
CONT U 1
CONT U 1
CONT U 1

AUTO0:
AUTO01:
;TRY THE SECOND IDA801 CONTROLLER
```

10121 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 ;TRY THE FPA TRANSMITTER
04 IZQ OR,7777,7764
05 ZQB OR,WRK2
06 IZQ OR,4000,30
07 CALL F,F,WAUTO
08 XA AND,CIOST,WRK5
09 JMP T,NZ,SYFL6
10 JMP F,F,IDLE
11
12 WAUTO:
13
14 ZAR OR,WRK2,DATO
15 CZR OR,.8,LAD
16 XA AND,CIOST,WRK5
17 JMP T,NZ,SYFL4
18 ZGR OR,LAD
19
20 ;BOOTSTRAPLOADER
21
22 BOOTL:
23 IZQ OR,7777,3000
24 IZQ OR,0,12
25 IZQ OR,0,24
26 IZQ OR,0,400
27 IZQ OR,0,26
28 IZQ OR,0,1400
29 IZQ OR,0,7400
30
31 Q:= 14- 3*10 , DEV BASE FOR FPA XM
32 WRK2:= Q;
33 Q:= 8'40000030, ADDR OF FPA XT;
34 SEND AUTO;
35 IF CPUIOSTATUS(21:23)<>0 THEN
36 GOTO SYSTEMFAULT 6;
37 GOTO IDLE;
38
39 SEND AUTOLOAD;
40 WRK2:=DEVICE BASE, Q=ADDRESS;
41 DATO:= DEVICE BASE;
42 LAD:= 8 , WRT
43 IF CPUIOSTATUS(21:23)<>0 THEN
44 GOTO SYSTEMFAULT4
45 LAD:= ADDRESS , WRT
46
47 ;ADDR DEV DESCRIPTION CHAN PROGRAM
48 ; 12 START OF CH PROG INITIALIZE
49 ; 14 STATUS ADDR
50 ; 16 INTR DESTINATION READ
51 ; 20 INTR LEVEL FINST ADDR
52 ; 22 INTR LEVEL CHAR COUNT
53 ; 24 STOP

```

CONT U 1

CONT WRT  
CONT U 1

RTN WRT

RTN  
RTN  
RTN  
RTN  
RTN  
RTN  
RTN

10122 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

01 ;GET AND PUT GENERAL REGISTER INSTRUCTIONS  
 02 ;\*\*\*\*\*  
 03 ;\*\*\*\*\*  
 04 ;\*\*\*\*\*

05 ;GET GENERAL REGISTER, GG, OPCODE = 34

06 ;GGE:  
 07 ;  
 08 ; CALL ESCAPE(3401)  
 09 ;GG:  
 10 ; WRK3:=SB/2  
 11 ; WRKU:= GETREG(WRK3)  
 12 ; W:= WRKU, TEST INT  
 13 ; GOTO NEXT INSTR  
 14  
 15  
 16  
 17  
 18  
 19  
 20  
 21  
 22  
 23  
 24  
 25  
 26  
 27  
 28  
 29  
 30  
 31  
 32  
 33

05 ;PUT GENERAL REGISTER, GP, OPCODE = 57, PRIVILEGED INSTRUCTION

06 ;GPE:  
 07 ;  
 08 ; CALL ESCAPE(5741)  
 09 ;GP:  
 10 ; IF -,MONMODE THEN GOTO INTG  
 11 ; WRKU:= W  
 12 ; WRK3:= SB/2  
 13 ; PUTREG(WRKU,WRK3)  
 14 ; TEST INT, GOTO NEXT INSTR  
 15  
 16  
 17  
 18  
 19  
 20  
 21  
 22  
 23  
 24  
 25  
 26  
 27  
 28  
 29  
 30  
 31  
 32  
 33

05 ;SYSTEMFAULT 3=6  
 06 ;\*\*\*\*\*  
 07  
 08  
 09  
 10  
 11  
 12  
 13  
 14  
 15  
 16  
 17  
 18  
 19  
 20  
 21  
 22  
 23  
 24  
 25  
 26  
 27  
 28  
 29  
 30  
 31  
 32  
 33

05 ; SYSTEMFAULT CAUSE 3  
 06 ; ERROR IN FETCH OF FIRST INSTRUCTION  
 07 ; AT POWERRESTART

10123 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 C5241 46247300000000100
04 C5242 463100000000004021
05
06
07 C5243 06247300000000120
08 C5244 463100000000004021
09
10
11 C5245 06247300000000140
12 C5246 463100000000004021
13
14
15
16
17 C5247 0607230037000000
18 C5250 0624700515312460
19 C5251 46310000020004013
20 C5252 0624700521052320
21 C5253 46310000020004013
22 C5254 4624700431012520
23 C5255 46310000020004013
24 C5256 4624700461241000
25 C5257 46310000020004013
26 C5260 0604431740000000
27 C5261 46310000020004160
28 C5262 4613551471002006
29 C5263 4613531471001506
30
31 C5264 06247300500000000
32 C5265 06310000020004012
33 C5266 4613551471003706
34 C5267 06247300500000000
35 C5270 06310000020004012
36 C5271 4613531471003706
37 C5272 46310000000000023
38 C5273 063100000000005264

; SYFL4: IZQ OR,0,4
; JMP F,F,SYFLT

; SYFL5: IZQ OR,0,5
; JMP F,F,SYFLT

; SYFL6: IZQ OR,0,6
; JMP F,F,SYFLT

; SYFLT: ZQB OR,WRK5
; .YFLT: IZQ OR,"S*16.+"("Y/16.),"Y*256.+"S
; CALL F,F,TPTXT
; IZQ OK,"T*16.+"("E/16.),"E*256.+"M
; CALL F,F,TPTXT
; IZQ OR,"F*16.+"("A/16.),"A*256.+"U
; CALL F,F,TPTXT
; IZQ OR,"L*16.+"("T/16.),"T*256.+"32.
; CALL F,F,TPTXT
; ZAQ OR,WRK5
; CALL F,F,TPNUM
; CABR CAND,.1B17,CNTR,CNTR,CNTR0
; CABR OR,.04,CNTR,CNTR,CNTR0

; SYFLR: IZQ OR,240,0
; CALL F,F,DELAY
; CABR CAND,.01,CNTR,CNTR,CNTR0
; IZQ OR,240,0
; CALL F,F,DELAY
; CABK OR,.01,CNTR,CNTR,CNTR0
; JMP T,CPUIN,INTIN
; JMP F,F,SYFLR

; SYSTEMFAULT CAUSE 4
; ERROR IN WRITING THE AUTOLOAD PROGRAM
;
; SYSTEMFAULT CAUSE 5
; BUSERROR FROM AUTOLOADDEVICE, FPAR
;
; SYSTEMFAULT CAUSE 6
; BUSEKRROR FROM AUTOLOAD, FPAX AND DISC
;
; SYSTEMFAULT:
;
; WRK5:= Q, CAUSE
; TYPE 'SYSTEMFAULT<32>' ON TCP
;
; Q:= WRK5
; TYPE NUMBER(CAUSE)
; CLEAR RUN LAMPS
;
; WHILE NOT CPUINT DO
; BEGIN
; DELAY(8'240000000);
; SET FAULT LAMP;
; DELAY(8'240000000);
; CLEAR FAULT LAMP;
; END;
; GOTO CPUINTERKRUPT;

```

10124 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 *****
02 ;
03 ;
04 ; CPU823 CACHE TEST
05 ;
06 *****
07 ;
08 ;
09 ;
10 ;
11 ;

```

```

;PROCEDURE TO COUNT THE NUMBER OF CACHE MODULES
;EXIT: WRK5= 1B22*IF SET 0 + 1B23*IF SET 1

```

```

12 CAMCO: ZGR AND,CAMBC
13 ZBB AND,WRK5
14 CZB ADDO,-1,WRK4
15 IZQ OR,U,5U
16 CMCO1: ZGR OR,CAMCN
17
18 ZGR AND,LAD
19 XZ OR,CIOST
20 ZGR OR,CAMTD
21 ZGR AND,LAD
22 XZ OR,IOSTA
23 CGR OR,-3,CAMCN
24 ZGR AND,LAD
25 XZB OR,IOSTA,WRK1
26 ZBB SUN,WRK4
27 JMP F,NZ,CMCO2
28 IZQ OR,U,3U
29 CA AND,-1B17,WRK1
30 JMP T,NZ,CMCO1
31 CZB ADDO,-1,WRK5
32 JMP F,F,CMCO1
33 CMCO2: CA AND,-1B19,WRK1
34 JMP F,NZ,-+3
35 CAB OR,-1,WRK5,WRK5

```

```

;COUNT CAMS
; CAMBYPASSCONTR:=0
; WRK5:= 0
; FOR N:=0,1 DO
; BEGIN
; CAMCONTROLREG:= IF N=0 THEN 8'50
; ELSE 8'3C
; LAD:= 0 , WRT
; C. SET VALID BIT
; CAMTESTDATA:= CAMCONTROLREG
;
; LAD:= 0 , READ
; CAMCONTR:=CAMCONTR OR 3
; LAD:= 0 , HEAD
; WRK1:= IOSTATUS
; IF HIT(N) THEN
; WRK5(22+N):= 1

```

```

CONT WRT
CONT 0 1
CONT READ
CONT 0 1
CONT READ
CONT 0 1

```

```

RTN
RTN
END

```



10125 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03
04 C5323 0653702036003700 CAMCL: CZB ADD,0,1,WRK4
05 C5324 4624730000001000 IZQ OR,0,40
06 C5325 0605240000000040 ZGR AND,CAMTD
07 C5326 0605141437000000 AB AND,0,2,WRK5
08 C5327 063100000025337 JMP F,NZ,CAMC2
09 C5330 0605230000000041 CAMC1: ZGR OR,CAMCN
10 C5331 4605240000000024 ZGR AND,CAMBC
11 C5332 4607340035000000 ZBB AND,WRK3
12 C5333 4251712000000304 CZR SUN0,B12,LCD
13 C5334 0605431640210011 ZAR OR,WRK3,LAD
14 C5335 06557300000004100 XZ OR,I0STA
15 C5336 6637101435114060 NSABB ADD,0,2,WRK3,NL,F,LCN
16 C5337 4607310036000000 ZBB SUN,WRK4
17 C5340 463100000025344 JMP F,NZ,CAMC3
18 C5341 46247300000000400 IZQ OR,0,20
19 C5342 0611541740003700 CA AND,0,1,WRK5
20 C5343 0631000000425330 JMP T,NZ,CAMC1
21
22 C5344 0611730000001241 CAMC3: CZR OR,0,11,CAMCN
23 C5345 4605240000210011 ZGR AND,LAD
24 C5346 6055730000004100 XZ OR,I0STA
25
26
27
28 C5347 46047300000005000 CAMIN: RZQ OR,SWTCH
29 C5350 0625640000000040 IQ AND,0,2
30 C5351 20310000002025274 CALL F,NZ,CAMCO
31 C5352 06310000002005323 CALL F,F,CAMCL
32 C5353 4605240000000024 ZGR AND,CAMBC
33 C5354 46117300000000741 CZR OR,0,15,CAMCN
34 C5355 0605240000230011 ZGR AND,LAD
35 C5356 20557300000000100 XZ OR,C10ST
36
37
;PROCEDURE TO CLEAR THE CACHE MEMORY MODULES
;
; FOR N:= 0,1 00
; BEGIN
; CAMTD:= U
; IF SET N PRESENT THEN
; BEGIN
; CAMCN:= IF N=0 THEN 8'40 ELSE 8'20
; CAMBC:=0
; FOR WRK3:= 0 STEP 2 UNTIL 8'20000 5(
; LAD:= WRK3 , READ
;
; PUSH
; CONT READ
; CONT U 1
; LRTN
;
; END
;
; CAMC3: C. INIT 'READY' AFTER TESTWRITE
; CAMCN:= 8'13
; LAD:= 0 , READ
;
; CONT READ
; RTN 0 1
;
; IF -,SWTCH(-,CAM AVAILABLE) THEN
; BEGIN
; COUNT CAM
; CLEAR CAM
; CAM BYPASS CONT:= 0
; CAMCONTROLREG:= 8'17
; LAD:= 0 , WRT
; C. SET VALID BIT
; END

```

10126 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03
04 C5357 4631000020C5347    ;CAMER: CALL  F,F,CAMIN
05 C5360 06115414000C2000  ;CAMER: CA   AND,,1B17,CNTR
06 C5361 0631000000C20020  ;CAMER: JMP  F,NZ,IDLE
07 C5362 0611541440001500  ;CAMER: CA   AND,,4,CNTR
08 C5363 0631000000422001  ;CAMER: JMP  T,NZ,DISPA
09 C5364 4605730000241011  ;CAMER: RZR  OR,ICS,LAD
10 C5365 7545230000000000  ;CAMER: ZG  OR

11
12
13
14 C5366 4624730000000040    ;CAMTEST: IZQ OR,0,2
15 C5367 060564000000C5000  ;CAMTEST: RQ  AND,SWTCH
16 C5370 0631000000426005    ;CAMTEST: JMP  T,NZ,TO,4
17 C5371 46135314710C3406    ;CAMTEST: CABR OR,,7,CNTR,CNTR,CNTR
18 C5372 46135514710C3706    ;CAMTEST: CABR CAND,,1,CNTR,CNTR,CNTR
19 C5373 4624730000000160    ;CAMTEST: IZQ OR,0,7
20 C5374 06310000002164013    ;CAMTEST: CALL F,M,MODE,TPTXT
21 C5375 06310000002005274    ;CAMTEST: CALL F,F,CAMCO
22 C5376 06310000002006004    ;CAMTEST: CALL F,F,SIZEM
23 C5377 06537020210C3700    ;CAMTEST: CZB ADDU,,1,W1
24 C5400 061373002200C2000    ;CAMTEST: CZB OR,,1B17,W2
25 C5401 0624730000001200    ;CAMTEST: IZQ OR,0,50
26 C5402 0605141437000000    ;CAMTEST: AB  AND,,2,WK5
27 C5403 06310000000025471    ;CAMTEST: JMP  F,NZ,CEX3

28
29 C5404 4607230036000041    C04: ZQBR OR,WK4,CAMCN
30 C5405 0607340020000000    C04: ZEB  AND,W0

31
32 C5406 0607431035000003    C05: ZABR OR,W0,WK3,ICD
33 C5407 06054317000000041    C05: ZAR  OR,WK4,CAMCN
34 C5410 4251712000000304    C05: CZR  SUNO,B12,LCD
35 C5411 46451130350000040    C05: ABR  SUNO,W0,WK3,CAMTD
36 C5412 0605431640210011    C05: ZAR  OR,WK3,LAD
37 C5413 06557300000004100    C05: XZ   OR,IOSTA
38 C5414 6637101435114060    C05: NSABB ADD,,2,WK3,NL,F,LCN
39 C5415 4611531700001441    C05: CAR  OR,,3,WK4,CAMCN

;CAMERORR
;
; INIT CAM
; IF CNTR.1/ =0 THEN
; GOTO IDLE
; IF CNTR.21 =1 THEN
; GOTO DISPATCHING POINT
; LAD:= ICS , HEAD INSTRUCTION
; GOTO NEXT INSTRUCTION

;CAMTEST START
;
; IF CAM NOT AVAILABLE THEN
; GOTO TEST CONTINUE

; TURN ERRORLAMP 3 ON
; IF TCP THEN
; IYPE BELL
; COUNT CAM
; SIZE AND CLEAR MEM
; FOR SET:= U,1 DO
; BEGIN
; MASK:= IF SET=0 THEN 1B17 ELSE 1B19
; IF SET PKSENT THEN
; BEGIN

; CAMCONTROL:=IF SET=0 THEN 8'50
; ELSE 8'30
; FOR STARTWINDOW:= 0 STEP 8'1000U UNT
; BEGIN
; FOR WRK:= 0 STEP 2 UNTIL 8'1000U UN
; BEGIN
; CAMTD:= WRK
; LAD:= STARTW+WRK , READ
; C. WRITE TESTDATA
; END FOR WRK
; CAMCONTROL:= CAMCONTROL OR 3

```

CONT READJ  
NEXT 0 3

PUSH

CONT READ  
CONT U 1  
LRTN

10127 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

01 05416 42135512240003305  
02 05417 46445230000001000  
03 05420 063100000000015427  
04 05421 4607730034211011  
05 05422 4657541133004100  
06 05423 46310000002025526  
07 05424 46555236000004000  
08 05425 0631000002425531  
09 05426 46310000000005460  
10 05427 46646120004000000  
11 05430 0631000000015450  
12 05431 4607730034211011  
13 05432 4657541133004100  
14 05433 0631000002425526  
15 05434 4605730000001012  
16 05435 0605730000231011  
17 05436 06475230340001000  
18 05437 46555236000004000  
19 05440 0631000002425531  
20 05441 46557300000001000  
21 05442 46117300000003724  
22 05443 4607730034211011  
23 05444 47155236000004000  
24 05445 0631000002425534  
25 05446 4605240000000024  
26 05447 46310000000005460  
27 05450 0645513140001000  
28 05451 06310000000415524  
29 05452 4607730034211011  
30 05453 4657541133004100  
31 05454 4631000002025526  
32 05455 06073400340000000  
33 05456 06157300000004000  
34 05457 0631000002425531  
C10:  
CABR CAND,01B1,STAT,CPUST PUSH ;  
RAQ SUBO,ICS,WU ;  
JMP F,NEG,C11 ;  
RZBR OR,ICS,WRK2,LAD ; CONT READ ;  
XAB AND,IOSTA,W2,WRK1 ; CONT 0 1 ;  
CALL F,NZ,CMHER ;  
XA SUBO,DATI,WRK2 ;  
CALL T,NZ,CMRER ;  
JMP F,F,CEX1 ;  
IQQ SUNO,2,U ;  
JMP F,NEG,C12 ;  
RZBR OR,ICS,WRK2,LAD ; CONT READ ;  
XAB AND,IOSTA,W2,WRK1 ; CONT U 1 ;  
CALL T,NZ,CMHER ;  
RZR OR,ICS,DATO ; CONT WRT ;  
RZR OR,ICS,LAD ;  
RAB SUBO,ICS,WU,WRK2 ;  
XA SUBO,DATI,WRK2 ;  
CALL T,NZ,CMRER ;  
XZ OR,CIOST ; CONT 0 1 ;  
CZR OR,01,CAMBC ;  
RZBR OR,ICS,WRK2,LAD ; CONT READ ;  
XA SUBO,DATI,WRK2 ; CONT U 2 ;  
CALL T,NZ,CMHER ;  
ZQR AND,CAMBC ;  
JMP F,F,CEX1 ;  
RA SUNO,ICS,W3 ;  
JMP T,NEG,C50 ;  
RZBR OR,ICS,WRK2,LAD ; CONT READ ;  
XAB AND,IOSTA,W2,WRK1 ; CONT U 1 ;  
CALL F,NZ,CMHER ;  
ZBB AND,WRK2 ;  
XZ OR,DATI ;  
CALL T,NZ,CMRER ;

FOR IC:= W0,WU+2,.....,LAST,0,.....,WU-  
DO BEGIN  
CLEAR ERROR FLAG, SETUP LOOP  
IF IC< STARTWINDOW THEN  
BEGIN  
LAD:=WRK2:=IC, READ  
STATUS:= MASK AND IOSTATUS  
IF STATUS=U THEN CAMHITEKOR  
IF DATAI<> IC THEN  
CAMREADEROR  
END ELSE  
IF IC< STARTWINDOW + 8'20000 THE  
BEGIN  
LAD:=WRK2:= IC, READ  
STATUS:= MASK AND IOSTATUS  
IF STATUS<U THEN CAMHITEKOR  
DATAOUT:= IC  
LAD:= IC, WRT  
IF DATAI<> IC-STARTWINDOW THEN  
CAMREADEROR  
CAMBYPASSREG:= 1  
LAD:=WRK2:= IC, READ  
IF DATAI<>IC THEN  
CAMWRITEERROR  
CAMBYPASSREG:= 0  
END ELSE  
BEGIN  
LAD:=WRK2:= IC, READ  
STATUS:= MASK AND IOSTATUS  
IF STATUS=U THEN CAMHITEKOR  
WRK2:= U  
IF DATAI<>U THEN  
CAMREADEROR



10129 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 C5522 00052300000000000000    ZQ      OR
03
04 C5523 06310000000000000000    JMP     F,F,TU.4
05
06
07 C5524 40052400000000000000    ZQR     AND,ICD
08 C5525 06310000000000000000    JMP     F,F,C13
09
10
11 C5526 06074314320000000000    ZAB     OR,.2,WRKO
12 C5527 06074311340000000000    ZAB     OR,W2,WRK2
13 C5530 46310000000000000000    JMP     F,F,TERROR
14
15
16 C5531 0613730032001400    CZB     OR,.3,WRKO
17 C5532 4617730033004000    XZB     OR,DATI,WRK1
18 C5533 46310000000000000000    JMP     F,F,TERROR
19
20
21 C5534 4613730032001500    CZB     OR,.4,WRKO
22 C5535 4617730033004000    XZB     OR,DATI,WRK1
23 C5536 46310000000000000000    JMP     F,F,TERROR

```

```

; END IF SET 0 AND 1
; CAMTEST FINISHED
; GOTO TEST REPEATTEST

;C50:
;
;
;CAMHITERROR:
; WRKO:= 2
; WRK2:= MASK
; GOTO TERROR

;CAMREADERKOR:
; WRKU:= 3
; WRK1:= DAT1
; GOTO TERROR

;CAMWRITEERKOR:
; WRKU:= 4
; WRK1:= DAT1
;

```

10130 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

FILL 60CC ;FILL SEGMENT WITH ONES  
-E01

;FILE: 14, EDITED: 850124/LBJ

\*\*\*\*\*

; CPU 823 MICRO TEST

\*\*\*\*\*

```
CC0001 .NOMAC 1  
C22000 .LOC 60CC*3  
C60C0 063100000000006006  
C6001 463100000000006062  
C6002 463100000000006111  
C6003 063100000000006104  
C6004 463100000000006156  
C6005 063100000000006077
```

```
; GOTO TX  
; GOTO ATEST  
; GOTO TERROR  
; GOTO LOOP  
; GOTO SIZEMEM  
; GOTO TU.4
```

;TX:  
.TX:

```
C6006 0631000002004014  
C6007 4624730000140260  
C6010 4651513600003600  
C6011 0631001601410000  
C6012 4631000000004005  
CALL F,F,GINUM  
IZQ OR,U,TCASE/3  
CA SUNU,.8,WRK2  
JXAQ ADD,WRK2,T,NEG  
JMP F,F,TCPER
```

```
;TX:  
; WRK2:= TEST NO  
; Q:= ADDR(ICASE);  
; IF WRK2<8 THEN  
; GOTO U+WRK2  
; ELSE  
; GOTO TCP ERROR
```

TCASE:

```
C6013 463100000000006023  
C6014 463100000000006026  
C6015 463100000000006031  
C6016 463100000000006034  
C6017 463100000000006037  
C6020 063100000000006042  
C6021 463100000000006045  
C6022 463100000000006051
```

```
F,F,T0  
F,F,T1  
F,F,T2  
F,F,T3  
F,F,T4  
F,F,T5  
F,F,T6  
F,F,T7  
JMP F,F,T0  
JMP F,F,T1  
JMP F,F,T2  
JMP F,F,T3  
JMP F,F,T4  
JMP F,F,T5  
JMP F,F,T6  
JMP F,F,T7
```

```
; CPU TEST, MEMTEST NORMAL MODE, CAMTEST  
; CPUTEST, MEMTEST SHORT MODE, CAMTEST  
; CPUTEST  
; MEMTEST NORMAL MODE  
; MEMTEST SHORT MODE  
; CAMTEST  
; SIZE AND CLEAR MEMORY  
; LOOP TO ADJUST BUSINTERFACE
```

10131 CPU823 MICROPROGRAM LBJ 85U816 VERSION 8

```

01
02
03 G6023 0624730170CC0000
04 C6024 4607230024CC0005
05 C6025 0631000000CC6071
06
07
08 C6026 4624730070CC0000
09 C6027 4607230024CC0005
10 C6030 0631000000CC6071
11
12
13 C6031 4624730040CC0000
14 C6032 4607230024CC0005
15 C6033 0631000000CC6071
16
17
18 C6034 0624730120CC0000
19 C6035 4607230024CC0005
20 C6036 4631000000CC6073
21
22
23 C6037 4624730020CC0000
24 C6040 4607230024CC0005
25 C6041 4631000000CC6073
26
27
28 C6042 4624730010CC0000
29 C6043 4607230024CC0005
30 C6044 4631000000CC6075
31
32
33 C6045 4631000002CC5044
34 C6046 0631000002CC6004
35 C6047 0631000000CC14005
36 C6050 0631000000CC4004

;T0: C. CPUTEST, MEMTEST NORMAL MODE
;
;
;
;T1: C. CPUTEST, MEMTEST SHORT MODE
;
;
;
;T2: C. CPUTEST
;
;
;
;T3: C. MEMTEST NORMAL MODE
;
;
;
;T4: C. MEMTEST SHORT MODE
;
;
;
;T5: C. CAMTEST
;
;
;
;T6: C. SIZEMEM
; INIT REGISTERS
; SIZE AND CLEAR MEM
; IF IOERROR THEN ICPER
; GOTO SINGLE INSTRUCTION

```

10132 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 C6051 4614730000000100      OR,CIOSTA
04 C6052 4624640076000000      AND,U370,0
05 C6053 0613630032002300      OR,.1B12,WRKU
06 C6054 4662731532000500      OR,SWAP,WRKU,WRKU
07 C6055 4605240000000012      AND,DATO
08 C6056 4213531224003305      OR,.1B1,STAT,STAT,CPUST  PUSH
09 C6057 0605431500230011      OR,WRKU,LAD             CONT WRT
10 C6060 0654730000000100      OR,CIOST                CONT U 1
11 C6061 4631000002006003      CALL F,F,LOOP
12
13
14
15 C6062 4624731170000000      OR,4740,0
16 C6063 4607230024000005      OR,STAT,CPUST
17 C6064 4604730000005000      OR,SWTCH
18 C6065 0625640000001000      AND,U,UU40
19 C6066 0631000000426071      T,NZ,IU.1
20 C6067 0624731070000000      IZQ OR,4340,0
21 C6070 4607230024000005      ZQBR OR,STAT,CPUST
22
23
24 C6071 0611541200002700      CA AND,.1B4,STAT
25 C6072 0631000000426173      JMP T,NZ,CPUSTEST
26
27 C6073 4611541200002600      CA AND,.1B5,STAT
28 C6074 4631000000427475      JMP T,NZ,MENTEST
29
30 C6075 4611541200002500      CA AND,.1B6,STAT
31 C6076 4631000000424017      JMP T,NZ,CAMTEST
32
33
34 C6077 4631000000600023      JMP T,CPUIN,INTIN
35 C6078 4611541200003100      CA AND,.1B3,STAT
36 C6079 0631000000426071      JMP T,NZ,IU.1
37 C6102 0631000000564015      JMP T,MMODE,RAUTO
38 C6103 0631000000600071      JMP F,F,IU.1

;T7: C. LOOP TO ADJUST DESKEW DELAY
;
;
;
; WRKU:= 1BU + CPUADDR
; DATAOUT:= U
; REPEAT
; LAD:= WRKU , WRT
; Q:= CPUJSTATUS
; UNTIL CPUINTERRUPT
;
;
;
; AUTOLOAD ENTRY:
; IF SWITCH 3 = U THEN
; CPUSTEST, SHORT MEM, CAMTEST
; ELSE
; CPUSTEST, LONG MEM, CAMTEST
;
;
;
; REPEAT C. TEST CPUSTEST
; IF STAT(4) THEN GOTO CPUSTEST
;
; C. TEST MEMTEST
; IF STAT(5) THEN GOTO MEMTEST
;
; C. TEST CAMTEST
; IF STAT(6) THEN GOTO CAMTEST
;
;
;
; IF CPUINTERRUPT THEN GOTO INTIN
; IF NORMAL MODE THEN
; REPEAT TEST
; IF AUTOLOADENTRY THEN GOTO RAUTO
; UNTIL FALSE

```



10133 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 ;CONTENTS OF REGISTER STAT AND CPUST
02 ; AUTOLCADENTRY BIT 0
03 ; ERROR-AND LOOPFLAG BIT 1
04 ; FULL TEST BIT 3
05 ; CPUTEST BIT 4
06 ; MEMTEST BIT 5
07 ; CAMTEST BIT 6
08 ; DOWNFLAG BIT 7
09
10 ;LOOP:
11 ;.LOOP:
12
13
14 C6104 4031000000176106 JMP F,EMODE,LOOP1 POP
15 C6105 663100000066107 JMP T,CPUIN,INT.2 LRTN
16 C6106 603100000066107 LOOP1: JMP T,CPUIN,INT.2 RTN
17
18 C6107 06537020300C3700 INT-2: CZB ADD0,.1,.2
19 C6110 4631000000000023 ;.POP
20 ; IF CPUINTERRUPT THEN GOTO INT.2
21 ; ELSE LOOPRETURN
22 ; END
23
24 ;TERROR:
25 ;.ERROR:
26 C6111 46310000000566153 JMP T,MMODE,ERRAUTO
27 C6112 60310000000176113 JMP F,EMODE,.+3
28 C6113 4641421500001202 ZAS SUB,WRKU,12
29 C6114 4641421540001302 ZAS SUB,WRK1,13
30 C6115 4641421600001402 ZAS SUB,WRK2,14
31 C6117 06537020300C3700 CZB ADD0,.1,.2
32 C6120 4624730425222440 LZQ OR,2125,1122
33 C6121 46310000002004013 CALL F,F,TPTXT
34 C6122 46310000002004007 CALL F,F,CRLF
35 C6123 06007300000001200 SZQ OR,12
36 C6124 06310000002004006 CALL F,F,TPOCT
37 C6125 46310000002004007 CALL F,F,CRLF
38 C6126 46007300000001300 SZQ OR,13

```

10134 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 06127 0631000020004006      CALL  F,F,TPOCT
03 06130 4631000020004007      CALL  F,F,CRLF
04 06131 0600730000001400      SZQ   OR,14
05 06132 0631000020004006      CALL  F,F,TPOCT
06 06133 4631000020004007      CALL  F,F,CRLF
07 06134 0604730000001000      RZQ   OR,ICS
08 06135 0631000020004006      CALL  F,F,TPOCT
09 06136 4631000020004007      CALL  F,F,CRLF
10 06137 4631000020004010      CALL  F,F,GCHAR
11 06140 4665622000002400      IQ     SUBO,0,"P
12 06141 46310000000026146      JMP    F,NZ,PROC
13 06142 4665622000002340      IQ     SUBO,0,"N
14 06143 40310000000026145      JMP    F,NZ,NLOOP
15 06144 4631000000004011      JMP    F,F,TCPI2
16
17 06145 0613531224003305      NLOOP: CABR  OR,,1B1,STAT,STAT,CPUST
18 06146 4631000020004007      PROC:  CALL  F,F,CRLF
19 06147 4603730032001200      SZB   OR,12,WRKU
20 06150 4603730033001300      SZB   OR,13,WRK1
21 06151 4603730034001400      SZB   OR,14,WRK2
22 06152 2000730000001500      SZQ   OR,15
23
24
25 06153 0624730050000000      ERRAUTO: IZQ  OR,24,0
26 06154 0631000020004012      CALL  F,F,DELAY
27 06155 0613551471001706      CABR  CAND,,1B18,CNTR,CNTR,CNTR0
28 06156 0624730050000000      IZQ   OR,24,0
29 06157 0631000020004012      CALL  F,F,DELAY
30 06160 06310000000066107      JMP    T,CPUIN,INT.2
31 06161 0613531471001706      CABR  OR,,1B18,CNTR,CNTR,CNTR0
32 06162 46310000000066153      JMP    F,F,ERRAUTO
33
34
35 06163 4653710032003700      TBERR:  CZB   SUN,,1,WRKU
36 06164 0617730033004100      XZB   OR,IOSTA,WRK1
37 06165 0613541573003400      CAB   AND,,7,WRK1,WRK1
38 06166 46310000000066002      JMP    F,F,TEKRROR

```

```

; TYPE WRK1
;
; TYPE WRK2
;
; TYPE IC
; Q:= GETCHAR
; IF Q='P' THEN
;   GOTO PROCEED
; IF Q='N'
; THEN GOTO NLOOP
; GOTO AFTER TCP INPUT
;
; SET ERRORFLAG
; TYPE 'CR','LF'
; RESTORE WRKU
; RESTORE WRK1
; RESTORE WRK2
; RESTORE Q
;
; ERRORPROCEDURE FOR AUTOLOADENTRY
; REPEAT
;   DELAY(0.26 SEC)
;   CLEAR AUTOLOAD LAMP
;
;   DELAY(0.26 SEC)
;   IF CPUINTERRUPT THEN GOTO INT.2
;   SET AUTOLOAD LAMP
;   UNTIL FALSE
;
;BUSERROR
; WRKU:= -2
; WRK1:= IOSTATUS
; WRK1:= IOSTATUS(21:23)
; GOTO TERROR

```

POP

RTN

10135 CPU823 MICROPROGRAM LBJ 85U816 VERSION 8

```

01
02
03
04 C6167 0640720000001700 CPULP: SZQ SUB,17
05 C6170 0631000000026073 JMP F,NZ,T0.2
06 C6171 0641220000001702 ZQS SUB,17
07 C6172 0631000000006201 JMP F,F,B2U
08
09
10 C6173 46135531471003406 CPUTEST: CABR OR,.7,CNTR,CNTR,CNTR0
11 C6174 4613551471001506 CABR CAND,-1B21,CNTR,CNTR,CNTR0
12 C6175 46247300000056700 IZQ OR,U,2734
13 C6176 0641220000001702 ZQS SUB,17
14 C6177 4624730000000160 IZQ OR,U,7
15 C6200 06310000002164013 CALL F,MMODE,TPTXT
16
17
18
19
20
21
22
23
24
25 06201 0613730032003600
26 06202 0624730000144340
27 06203 0607230037000000
28 06204 0607121634000000
29
30 06205 0607302037000000
31 06206 4213551224003305
32 06207 0631431743000000
33 06210 4607230033000000
34 06211 0605023600000000
35 06212 0631000002426002
36 06213 4631000002006003
37 06214 4037330034000060
38 06215 0607302032000000
39 06216 4631000000006205
;ADM OF NUMBER OF CPUTEST RUNS
; IF RUNCOUNTER=1=0 THEN
; GOTO TEST MEMTEST
; RUN COUNTER:= RUN COUNTER -1
; REPEAT CPUTEST
;CPUTEST START
; TURN LAMP 1 ON
;
; RUN COUNTER:= 1500
; IF TCP THEN
; TYPE(BELL)
;
;TEST OF THE LOAD IMMEDIATE FUNCTION
;THE IMMEDIATE OPERAND AND THE ALU'S ABILITY TO SHIFT LEFT
;ARE TESTED.
;THE IMOP IS LOADED WITH A SHIFTING 'ZERO', WRK2 IS LOADED WITH THE
;EXPECTED DATA
B20:
CZB OR,.8,WRKO
IZQ OR,U,IMBASE/3-1
ZQB OR,WRK5
ABB SUB,WRK2,WRK2
B30:
ZBB ADD0,WRK5
CABR CAND,-1B1,STAT,STAT,CPUST PUSH
CXZA OR,WRK5,F,F
ZQB OR,WRK1
AQ SUB0,WRK2
CALL T,NZ,TERROR
CALL F,F,LOOP
SLZBB OR,WRK2,Z,NL,F,F POP
ZBB ADD0,WRK0
JMP F,F,B3U
; WRKU:= 8'1U
; Q:= ADDR(IMBASE)-1
; WRK5:= POINIER TO IMOP TABLE
; WRK2:= -1
; REPEAT
; WRK5:= WRK5+1
; CLEAR ERKURFLG , PUSH
; Q:= DATAPATTERN
; WRK1:= Q
; IF WRK2<>Q THEN
; TERROR
; TEST LOOPING
; WRK2:= WRK2 SHIFT 1
; WRKU:= WRKU+1
; UNTIL FALSE

```



10137 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

; TEST OF ARITHMETIC AND LOGIC OPERATIONS

```

01 B100:
02 06250 4013730032002000
03 06251 4213551224003305
04
05 06252 4624730000143460
06 06253 4631001503000000
07 06254 0605023600000000
08 06255 463100002426261
09 06256 463100002006003
10 06257 405350332003700
11 06260 0631000000006251
12
13
14
15
16 B100E: ZQB
17 06261 4607230033000000
18 06262 4631000000006002
19
20
21
22
23
24
25
26
27
28
29
30
31
32
33
34
35
36
37

```

ARLOBASE:

```

20 06263 0607340034000000
21 06264 2064700000000000
22 06265 2064722000000000
23 06266 0605230000000000
24 06267 062473177777760
25 06270 606462377777760
26 06271 0653712034003700
27 06272 206470177777760
28 06273 062473177777760
29 06274 6064600000000000
30 06275 206472377777760
31 06276 0605230000000000
32 06277 0624730525252520
33 06300 6007230034000000
34 06301 0624730525252520
35 06302 6064623252525240
36 06303 0624731252525240
37 06304 6007230034000000

```

```

; WRKU:= 8'100
; REPEAT
; CLEAR ERROR FLAG , PUSH
; Q:= ADDR(RLOBASE) -100
; EXECUTE TEST(Q+WRKU)
; IF Q-WRK2<<0 THEN
; BEGIN
; WRK1:=Q
; TERROR
; END
; TEST LOOPING
; WRKU:= WRKU+2
; UNTIL FALSE
;
;100 EXPECTED:= 0
;
;102
;
;104
;
;106 EXPECTED:= -1
;
;110
;
;112
;
;
; EXPECTED:= 8'25252525
;116
;
;
; EXPECTED:= 8'52525252

```

```

; OR,1B17,WRKO POP
; CAND,1B1,STAT,STAT,CPUST PUSH
; OR,0,ARLOBASE/3-100
; ADD,WRKU,F,F
; SUBO,WRK2
; CALL T,NZ,B100E
; F,F,LOOP
; ADDO,-1,WRKU,WRKO POP
; F,F,B101
; OR,WRK1
; F,F,TERROR
; AND,WRK2
; ADD,U,U
; SUBO,0,0
; OR
; OR,7777,7777
; SUBO,7777,7777
; SUNO,-1,WRK2
; ADD,7777,7777
; OR,7777,7777
; ADD,U,0
; SUBO,7777,7777
; OR
; OR,2525,2525
; OR,WRK2
; OR,2525,2525
; SUBO,5252,5252
; OR,5252,5252
; OR,WRK2

```

```

;100
;102
;104
;106
;110
;112
;
;116
;
;
;

```



```

10139 CPU823 MICROPROGRAM LBJ 850816 VERSION 8
01 02 C6351 4624731001774000          IZQ OR,4007,7600          RTN            ;166
03 06352 2064622776003760          IQQ SUB0,3770,0177      ;
04 06353 0624730525252520          IZQ OR,2525,2525        ;
05 06354 6007230034000000          ZQB OR,WRK2           ;
06 06355 0624730525252520          IZQ OR,2525,2525      ;172
07 06356 2024630525252520          IQQ OR,2525,2525      ;
08 06357 0624730525252520          IZQ OR,2525,2525      ;174
09 06360 6024640525252520          IQQ AND,2525,2525     ;
10 06361 0624730525252520          IZQ OR,2525,2525      ;176
11 06362 2024651252525240          IQQ CAND,5252,5252   ;
12 06363 062473177777760          IZQ OR,7777,7777     ;
13 06364 6007230034000000          ZQB OR,WRK2           ;
14 06365 0624730525252520          IZQ OR,2525,2525      ;202
15 06366 2024631252525240          IQQ OR,5252,5252     ;
16 06367 0624731252525240          IZQ OR,5252,5252     ;204
17 06370 2024630525252520          IQQ OR,2525,2525     ;
18 06371 0624730525252520          IZQ OR,2525,2525     ;206
19 06372 2024661252525240          IQQ EXOR,5252,5252   ;
20 06373 0624731252525240          IZQ OR,5252,5252     ;210
21 06374 2024660525252520          IQQ EXOR,2525,2525   ;
22 06375 0624730525252520          IZQ OR,2525,2525     ;212
23 06376 6024670525252520          IQQ EXNOR,2525,2525  ;
24 06377 0624731252525240          IZQ OR,5252,5252     ;214
25 06400 6024671252525240          IQQ EXNOR,5252,5252  ;
26 06401 0624730000000000          IZQ OR,U,0           ;
27 06402 6007230034000000          ZQB OR,WRK2           ;
28 06403 0624730525252520          IZQ OR,2525,2525     ;220
29 06404 6024641252525240          IQQ AND,5252,5252    ;
30 06405 0624731252525240          IZQ OR,5252,5252     ;222
31 06406 6024640525252520          IQQ AND,2525,2525    ;
32 06407 0624730525252520          IZQ OR,2525,2525     ;224
33 06410 2024650525252520          IQQ CAND,2525,2525   ;
34 06411 0624731252525240          IZQ OR,5252,5252     ;226
35 06412 2024651252525240          IQQ CAND,5252,5252   ;
36 06413 0624730525252520          IZQ OR,2525,2525     ;230
37 06414 2024660525252520          IQQ EXOR,2525,2525   ;
            EXPECTED:= 8'25252525
            EXPECTED:= -1
            EXPECTED:= 0

```

10140 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 06415 0624731252525240      IZQ  OR,5252,5252      ;232
03 06416 2024661252525240      IQQ  EXOR,5252,5252      ;
04 06417 0624730525252520      IZQ  OR,2525,2525      ;234
05 06420 6024671252525240      IQQ  EXNOR,5252,5252   ;
06 06421 0624731252525240      IZQ  OR,5252,5252      ;236
07 06422 6024670525252520      IQQ  EXNOR,2525,2525   ;
08 06423 0624731252525240      IZQ  OR,5252,5252      ;
09 06424 6007230340000000      ZQB  OR,WRK2          ;
10 06425 0624731252525240      IZQ  OR,5252,5252      ;242
11 06426 2024631252525240      IQQ  OR,5252,5252      ;
12 06427 0624731252525240      IZQ  OR,5252,5252      ;244
13 06430 6024641252525240      IQQ  AND,5252,5252     ;
14 06431 0624731252525240      IZQ  OR,5252,5252      ;246
15 06432 2024650525252520      IQQ  CAND,2525,2525     ;

```

EXPECTED: 8'52525252

;ALU REGISTER TEST

```

16
17
18
19 06433 002473000000060000      IZQ  OR,0,300        POP
20 06434 0607230032000000      ZQB  OR,WRK0
21 06435 06117300000001501      CZR  OR,.4,LAST
22
23 06436 46137300340003700      CZB  OR,.1,WRK2
24
25 06437 4213551224003305      CABR CAND,.1B1,STAT,STAT,CPUST,PUSH
26 06440 0624730000143240      IZQ  OR,U,REGBASE/J-300
27 06441 4631001503000000      CXAQ ADD,WRK0,F,F
28 06442 4607230033000000      ZQB  OR,WRK1
29 06443 4605123574000000      AB   SUBO,WRK1,WRK2
30 06444 0631000002426002      CALL T,NZ,TEKOR
31 06445 46310000020006003      CALL F,F,LOOP
32 06446 4007101634000000      ABB  ADD,WRK2,WRK2      POP
33 06447 0631000000426437      JMP  T,NZ,B310
34 06450 465350532003700      CAB  ADDO,.1,WRK0,WRK0
35 06451 46310000000006436      JMP  F,F,B300

```

```

; WRK0:= 8'300
; LAST:= 4
; REPEAT
;   WRK2:= 1
;   REPEAT
;     CLEAR ERROR FLAG , PUSH
;     Q:= ADDR(REGBASE)-300
;     EXECUIE TEST(Q+WRK0)
;     WRK1:= Q
;     IF WRK1<>WRK2 THEN
;       TERKOR
;     TEST LOOPING
;     WRK2:= WRK2 SHIFT 1
;     UNTIL WRK2=0
;     WRKU:= WRK0+2
;     UNTIL FALSE

```



10141 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

01  
02  
03 06452 6004431600000000 ZAQ OR,WRK2 RTN ;300  
04 06453 0605230000000000 ZQ OR OR ;  
05 06454 0607431603000000 ZAB OR,WRK2,LAX RTN ;302  
06 06455 2004431100000000 ZAQ OR,W2 RTN ;304  
07 06456 0607431603000000 ZAB OR,WRK2,LAX ;  
08 06457 6004330003000000 ZBQ OK,LAX ;306  
09 06460 4607431620000000 ZAB OR,WRK2,WU RTN ;310  
10 06461 6004431000000000 ZAQ OR,WU ;  
11 06462 4607431620000000 ZAB OR,WRK2,WU RTN ;312  
12 06463 2004330020000000 ZBQ OR,WU ;  
13 06464 0607431621000000 ZAB OR,WRK2,W1 RTN ;314  
14 06465 2004431040000000 ZAQ OR,W1 ;  
15 06466 0607431621000000 ZAB OR,WRK2,W1 RTN ;316  
16 06467 6004330021000000 ZBQ OR,W1 ;  
17 06470 0607431622000000 ZAB OR,WRK2,W2 RTN ;320  
18 06471 2004431100000000 ZAQ OR,W2 ;  
19 06472 0607431622000000 ZAB OR,WRK2,W2 RTN ;322  
20 06473 6004330022000000 ZBQ OR,W2 ;  
21 06474 4607431623000000 ZAB OR,WRK2,W3 RTN ;324  
22 06475 6004431140000000 ZAQ OR,W3 ;  
23 06476 4607431623000000 ZAB OR,WRK2,W3 RTN ;326  
24 06477 2004330023000000 ZBQ OR,W3 ;  
25 06500 4607431625000000 ZAB OR,WRK2,CAUSE RTN ;330  
26 06501 6004431240000000 ZAQ OR,CAUSE ;  
27 06502 4607431625000000 ZAB OR,WRK2,CAUSE RTN ;332  
28 06503 2004330025000000 ZBQ OR,CAUSE ;  
29 06504 4607431626000000 ZAB OR,WRK2,SB RTN ;334  
30 06505 6004431300000000 ZAQ OR,SB ;  
31 06506 4607431626000000 ZAB OR,WRK2,SB RTN ;336  
32 06507 2004330026000000 ZBQ OR,SB ;  
33 06510 0607431627000000 ZAB OR,WRK2,INF RTN ;340  
34 06511 2004431340000000 ZAQ OR,INF ;  
35 06512 0607431627000000 ZAB OR,WRK2,INF RTN ;342  
36 06513 6004330027000000 ZBQ OK,INF ;  
37 06514 0607431630000000 ZAB OR,WRK2,.2 RTN ;  
38 06515 2004431400000000 ZAQ OR,.2 ;

10142 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02 06516 0607431630000000 ZAB OR,WRK2,02
03 06517 6004330030000000 ZBQ OR,02
04 06520 0607431633000000 ZAB OR,WRK2,WRK1
05 06521 2004431540000000 ZAQ OR,WRK1
06 06522 0607431633000000 ZAB OR,WRK2,WRK1
07 06523 6004330033000000 ZBQ OR,WRK1
08 06524 4607431634000000 ZAB OR,WRK2,WRK2
09 06525 6004431600000000 ZAQ OR,WRK2
10 06526 4607431634000000 ZAB OR,WRK2,WRK2
11 06527 2004330034000000 ZBQ OR,WRK2
12 06530 0607431635000000 ZAB OR,WRK2,WRK3
13 06531 2004431640000000 ZAQ OR,WRK3
14 06532 0607431635000000 ZAB OR,WRK2,WRK3
15 06533 6004330035000000 ZBQ OR,WRK3
16 06534 0607431636000000 ZAB OR,WRK2,WRK4
17 06535 2004431700000000 ZAQ OR,WRK4
18 06536 0607431636000000 ZAB OR,WRK2,WRK4
19 06537 6004330036000000 ZBQ OR,WRK4
20 06540 4607431637000000 ZAB OR,WRK2,WRK5
21 06541 6004431740000000 ZAQ OR,WRK5
22 06542 4607431637000000 ZAB OR,WRK2,WRK5
23 06543 2004330037000000 ZBQ OR,WRK5
24 06544 0605431600000004 ZAR OR,WRK2,LCD
25 06545 2004730000003000 RZQ OR,LCS
26 06546 4003730037001700 SZB OR,17,WRK5
27 06547 4631000000006450 JMP F,F,B32U
28 06550 4641421600000002 ZAS SUB,WRK2,0
29 06551 6000730000000000 SZQ OK,U
30 06552 0641421600000102 ZAS SUB,WRK2,1
31 06553 2000730000000100 SZQ OR,1
32 06554 0641421600000202 ZAS SUB,WRK2,2
33 06555 2000730000000200 SZQ OR,2
34 06556 4641421600000302 ZAS SUB,WRK2,3
35 06557 6000730000000300 SZQ OR,3
36 06560 0641421600000402 ZAS SUB,WRK2,4
37 06561 2000730000000400 SZQ OR,4

```

SAVE RUN COUNTER IN WRK5

RETURN

```

;344
;
;346
;
;350
;
;352
;
;354
;
;356
;
;360
;
;362
;
;364
;
;366
;
;370
;
;372
;
;
; SAVE RUN COUNTER IN WRK5
; RETURN
;376
;
;400
;
;402
;
;404
;
;406
;

```

```

RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN
RTN

```

```

OR,WRK2,02
OR,02
OR,WRK2,WRK1
OR,WRK1
OR,WRK2,WRK1
OR,WRK1
OR,WRK2,WRK2
OR,WRK2
OR,WRK2,WRK2
OR,WRK2
OR,WRK2,WRK3
OR,WRK3
OR,WRK2,WRK3
OR,WRK3
OR,WRK2,WRK4
OR,WRK4
OR,WRK2,WRK4
OR,WRK4
OR,WRK2,WRK5
OR,WRK5
OR,WRK2,WRK5
OR,WRK5
OR,WRK2,LCD
OR,LCS
OR,17,WRK5
F,F,B32U
SUB,WRK2,0
OK,U
SUB,WRK2,1
OR,1
SUB,WRK2,2
OR,2
SUB,WRK2,3
OR,3
SUB,WRK2,4
OR,4

```

```

ZAB
ZBQ
ZAB
ZAQ
ZAB
ZBQ
ZAB
ZAQ
ZAB
ZBQ
ZAB
ZAQ
ZAB
ZBQ
ZAB
ZAQ
ZAB
ZBQ
ZAR
RZQ
SZB
JMP
ZAS
SZQ
ZAS
SZQ
ZAS
SZQ
ZAS
SZQ
ZAS
SZQ

```



10144 CPU623 MICROPROGRAM LBJ 850816 VERSION 8

01 ;TEST OF SHIFT FUNCTIONS

02 ;

03 ;

04 ;

05 ; B500: IZQ OR,0,500

06 ; ZQB OR,WRKO

07 ; B501: CABR CAND,-1B1,STAT,STAT,CPUST PUSH

08 ;

09 ; IZQ OR,0,SHIFTBASE/3-500

10 ; CXAQ ADD,WRKU,F,F

11 ; AB SUBO,WRK1,WRK2

12 ; CALL T,NZ,TERROR

13 ; CALL F,F,LOOP

14 ; ZBB ADDO,WRKU POP

15 ; JMP F,F,B501

16 ;

17 ; TB500: IZQ OR,400U,0

18 ; ZBB AND,WRK1

19 ; CZB OR,-1,WRK2

20 ; DLZBB OR,WRK1,Z,NL,F,F RTN

21 ;

22 ;

23 ;

24 ; TB501: CZB SUNO,-1,WRK1

25 ; CAB CAND,-1,WRK1,WRK2

26 ; SLZBB OR,WRK1,Z,NL,F,F RTN

27 ;

28 ;

29 ;

30 ; TB502: CZB SUNO,-1,WRK1

31 ; CAB CAND,-1B0,WRK1,WRK2

32 ; SRZBB OR,WRK1,Z,NL,F,F RTN

33 ;

34 ;

35 ;

;B500:

; WRKO:= 8'500

; REPEAT

; CLEAR ERROR FLAG, PUSH

; Q:= ADDK(SHIFTBASE)-500

; EXECUTE TEST(Q+WRKO)

; IF WRK1<>WRK2 THEN

; TERROR

; TEST LOUING

; WRKO:= WRKO+1

; UNTIL FALSE

;500: TEST DOUBLE SHIFT

; Q:= 8'40000000

; WRK1:= U

; WKK2:= 1 C. EXPECTED DATA

; WRK1,Q:= (WRK1,Q) SHIFT 1

; RETURN

;501: SI=ZERO LEFT SHIFT

; WRK1:= -1

; WRK2:= 8'7777776 C. EXPECTED DATA

; WRK1:= WRK1(1:23) CON 1 EXT 0

; RETURN

;502: SI=ZERO RIGHT SHIFT

; WKK1:= -1

; WRK2:= 1 EXT 0 CON WRK1(1:23)

; C. EXPECTED DATA

; WRK1:= 1 EXT 0 WKK1(1:22)

; RETURN

10145 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 C6644 4613730034CC3700
04 C6645 4613730033CC3500
05 C6646 0637330033CC0060
06 C6647 2037330033CC1060
07
08
09
10 C6650 0613730034CC3500
11 C6651 0613730033CC3700
12 C6652 4635330033CC0060
13 C6653 6035330033CC1060
14
15
16
17 C6654 4613730034CC3700
18 C6655 0604240000CC0000
19 C6656 4674123573CC0040
20 C6657 2037330033CC2060
21
22
23
24 C6660 0624731400CC0000
25 C6661 0607230034CC0000
26 C6662 4613730033CC3500
27 C6663 6035330033CC2060
28
29
30 C6664 4613730033CC3500
31 C6665 0607431574CC0000
32
33
34 C6666 6075101633CC3060
35
36
;503: SI=LINK LEFT SHIFT
; WRK2:= 1 C. EXPECTED DATA
; WRK1:= 1BU
; WRK1:= WRK1(1:23) CON 1 EXT 0
; WRK1:= WRK1(1:23) CON 1 EXT LINK
; RETURN
;504: SI=LINK RIGHT SHIFT
; WRK2:= 1BU
; WRK1:= 1
; WRK1:= 1 EXT 0 CON WRK1(0:22)
; WRK1:= 1 EXT LINK CON WRK1(0:22)
; RETURN
;505: SI=ADC LEFT SHIFT
; WRK2:= 1
; Q:= 0
; WRK1:= 0, ADC:= -,Q(23)
; WRK1:= WRK1(1:23) CON 1 EXT ADC
; RETURN
;506: SI=F(U) RIGHT SHIFT
;
; WRK2:= 8'00000000
; WRK1:= 1BU
; WRK1:= 1 EXT WRK1(0) CON WRK1(0:22)
; RETURN
;507: SI=F(U) EXOR OVERFLOW RIGHT SHIFT
; WRK1:= WRK2:= 1BU
;
; WRK1:= WRK1+WRK2
; WRK1:= 1 EXT(FO EXOR OVERFLOW)
; CON WRK1(0:22)
; RETURN

```

TB503:

```

CZB OR,,1,WRK2
CZB OR,,1B0,WRK1
SLZBB OR,WRK1,Z,NL,F,F
SLZBB OR,WRK1,LNK,NL,F,F RTN

```

TB504:

```

CZB OR,,1B0,WRK2
CZB OR,,1,WRK1
SRZBB OR,WRK1,Z,NL,F,F
SRZBB OR,WRK1,LNK,NL,F,F RTN

```

TB505:

```

CZB OR,,1,WRK2
ZQB AND
DRABB SUBO,WRK1,WRK1,Z,MC,F,F
SLZBB OR,WRK1,ADC,NL,F,F RTN

```

TB506:

```

IZQ OR,6000,0
ZQB OR,WRK2
CZB OR,,1B0,WRK1
SRZBB OR,WRK1,FU,NL,F,F RTN

```

TB507:

```

CZB OR,,1B0,WRK1
ZAB OR,WRK1,WRK2
SRABB ADD,WRK2,WRK1,SGN,NL,F,F RTN

```

10146 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03
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18
19
20
21
22
23
24
25
26
27
28
29
30
31
32
33
34
35
36

SHIFTBASE:
JMP F,F,1B500
JMP F,F,1B501
JMP F,F,1B502
JMP F,F,1B503
JMP F,F,1B504
JMP F,F,1B505
JMP F,F,1B506
JMP F,F,1B507

;TEST OF JUMP CONDITIONS
B540:
IZQ OR,0,540
ZQB OR,WRKU
B541:
CABR CAND,,1B1,STAT,STAT,CPUST,PUSH
IZQ OR,0,JUMPBASE/3-540
CXAQ ADD,WRKU,T,F
CALL F,F,LOOP
ZBB ADD,WRKU
JMP F,F,B541

TNEG:
ZBB AND,WRK2
JMP T,NEG,TERROR
CZB OR,,1B0,WRK2
JMP F,NEG,TERROR

TNZ:
ZBB AND,WRK2
JMP T,NZ,TERROR
ZEB ADD,WRK2
JMP F,NZ,TERROR

; WRKU:= 8'540
; REPEAT
; CLEAR ERROR FLAG ,PUSH
; Q:= ADDR(JUMPBASE)-540
; EXECUTE TEST(Q+WRKU)
; TEST LOOPING
; WRKU:= WRKU+1
; UNTIL FALSE

;540: DBUS(U)
; WRK2:= U
; IF WRK2<U THEN GOTO TERROR
; IF -,8'40000000<U THEN
; GOTO TERROR
; RETURN

;541: F<>0
; WRK2:= U
; IF WRK2<>U THEN GOTO TERROR
; WRK2:= 1
; IF -,WRK2<>U THEN GOTO TERROR
; RETURN

```

IU147 CPU823 MICROPROGRAM LBJ 85U816 VERSION 8

```

01
02
03 06717 46537120340C3500
04 06720 463100000036002
05 06721 06537020340C3500
06 06722 6031000000436002
07
08
09
10 06723 062473177777760
11 06724 46072020340C0000
12 06725 063100000046002
13 06726 06073020340C0000
14 06727 2031000000446002
15
16
17
18 06730 06537200340C3500
19 06731 463100000056002
20 06732 06073020340C0000
21 06733 6031000000456002
22
23
24
25 06734 0624731400C0000
26 06735 06072300340C0000
27 06736 0631000000466002
28 06737 06073400340C0000
29 06740 0631000000466002
30 06741 06137300340C3500
31 06742 463100000066002
32 06743 06137300340C3300
33 06744 203100000066002
34

;542: ARITHMETIC OVERFLOW
; WRK2:= 8'40000000
; IF -,OVERFLOW THEN GOTO TERROR
; WRK2:= 8'40000000 + 1
; IF OVERFLOW THEN GOTO TERROR
; RETURN
;543: CARRY
; WRK2:= 8'77777777 + 1
; IF -,CARRY THEN GOTO TERROR
; WRK2:= WRK2 + 1
; IF CARRY THEN GOTO TERROR
; RETURN
;544: F(0) EXOR OVERFLOW
; WRK2:= 8'40000000 - 1
; IF -,F(U) EXOR OVERFLOW THEN GOTO TERROR
; WRK2:= WRK2 + 1
; IF F(U) EXOR OVERFLOW THEN GOTO TERROR
; RETURN
;545: DBUS(U)<>DBUS(1)
; WRK2:= 8'60000000
; IF DBUS(0)<>DBUS(1) THEN GOTO TERROR
; WRK2:= 0
; IF DBUS(0)<>DBUS(1) THEN GOTO TERROR
; WRK2:= 8'40000000
; IF -,DBUS(U)<>DBUS(1) THEN GOTO TERROR
; WRK2:= 8'20000000
; IF -,DBUS(U)<>DBUS(1) THEN GOTO TERROR
; RETURN

```

RTN

RTN

RTN

RTN

TOVFL: CZB SUN0,1BU,WRK2  
 JMP F,OVFL,TERROR  
 CZB ADD0,1BU,WRK2  
 JMP T,OVFL,TERROR

TCARRY: IZQ OR,7777,7777  
 ZQB ADD0,WRK2  
 JMP F,CARRY,TERROR  
 ZBB ADD0,WRK2  
 JMP T,CARRY,TERROR

TSIGN: CZB SUB,1BU,WRK2  
 JMP F,SIGN,TERROR  
 ZBB ADD0,WRK2  
 JMP T,SIGN,TERROR

TNORU: IZQ OR,6000,0  
 ZQB OR,WRK2  
 JMP T,NORMO,TERROR  
 ZBB AND,WRK2  
 JMP T,NORMU,TERROR  
 CZB OR,1BU,WRK2  
 JMP F,NORMO,TERROR  
 CZB OR,1B1,WRK2  
 JMP F,NORMU,TERROR

10148 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 06745 0624730600000000
04 06746 0607230034000000
05 06747 463100000476002
06 06750 0607340034000000
07 06751 463100000476002
08 06752 0613730034003300
09 06753 063100000076002
10 06754 4613730034003200
11 06755 603100000076002
12
13
14
15 06756 0613730034003500
16 06757 4677302034000600
17 06760 063100000106002
18 06761 0653712034003700
19 06762 0677101634000600
20 06763 2031000000506002
21
22
23
24 06764 4607340034000004
25 06765 063100000516002
26 06766 4613730034003504
27 06767 2031000000116002
28
29
30
31 06770 0624730000001400
32 06771 0607230034000000
33 06772 0211730000003504
34 06773 0631000000526777
35
36 06774 0607310034000000
37 06775 2637330034024060
38 06776 2031000000126002
39
;546: DBUS(1)<>DBUS(2)
; WRK2:= 8'30000000
;
; IF DBUS(1)<>DBUS(2) THEN GOTO TERROR
; WRK2:= U
; IF DBUS(1)<>DBUS(2) THEN GOTO TERROR
; WRK2:= 8'20000000
; IF DBUS(1)<>DBUS(2) THEN GOTO TERROR
; WRK2:= 8'10000000
; IF DBUS(1)<>DBUS(2) THEN GOTO TERROR
; RETURN
;547: LINK<>CARRY
; WRK2:= 8'40000000
; WRK2:= (WRK2+1) SHIFT 1
; IF LINK<>CARRY THEN GOTO TERROR
; WRK2:= -1
; WRK2:= (WRK2+WRK2) SHIFT 1
; IF LINK<>CARRY THEN GOTO TERROR
; RETURN
;550: LC(U)
; WRK2:=LCD:= U
; IF LC(U) THEN GOTO TERROR
; WRK2:=LCD:= 8'40000000
; IF LC(U) THEN GOTO TERROR
; RETURN
;551: LC(U)=U OR MAXLOOP(U)=C
; Q:= 48
; WRK2:= 48
; LCD:= 8'40000000 , PUSH
; IF LC(U)=U OR MAXLOOP(U)=0
; THEN GOTO TMAXERROR
; WRK2:= WRK2-1
; UNTIL WRK2=0
; IF LC(U)=0 OR MAXLOOP(U)=0 THEN
; GOTO TERROR
TNOR1: IZQ OR,5000,U
ZQB OR,WRK2
JMP T,NORM1,TERROR
ZBB AND,WRK2
JMP T,NORM1,TERROR
CZB OR,.1B1,WRK2
JMP F,NORM1,TERROR
CZB OR,.1B2,WRK2
JMP F,NORM1,TERROR
RTN
TDIVOF: CZB OR,.1B0,WRK2
SLZBB ADD0,WRK2,Z,NL,F,F
JMP F,DIVUF,TERROR
CZB SUN0,.1,WRK2
SLABB ADD,WRK2,WRK2,Z,NL,F,F
JMP T,DIVOF,TERROR
RTN
TLCN: ZBBR AND,WRK2,LCD
JMP T,LCN,TERROR
CZBR OR,.1B0,WRK2,LCD
JMP F,LCN,TERROR
RTN
TMAXL: IZQ OR,0,6U
ZQB OR,WRK2
CZR OR,.1B0,LCD
JMP T,MAXLO,TMAXE
PUSH
ZBB SUN,WRK2
NSZBB OR,WRK2,NL,F,NZ
JMP F,MAXLO,TERROR
LRTN
RTN

```



10149 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```
01 ; RETURN
02 ;
03 ;
04 ;
05 ;
06 ;52: LOGADDR(0:20)<>0
07 ; WRK2:=LAST:= 0
08 ; IF LOGADR(U:20)<>0 THEN GOTO TERROR
09 ; WRK2:=LAST:= -1B20
10 ; IF ->LOGADR(0:20)<>0 THEN GOTO TERROR
11 ; RETURN
12
13 ;53: LOGADDR(0)=1
14 ; WRK2:=LAST:= 0
15 ; IF LOGADR(U)=1 THEN GOTO TERROR
16 ; WRK2:=LAST:= 1B0
17 ; IF ->LOGADDR(0)=1 THEN GOTO TERROR
18 ; RETURN
19
20 ;54: MONITORMODE
21 ; WRK2:=CPUST:= .1B0
22 ; IF ->MONITORMODE THEN GOTO TERROR
23 ; WRK2:=CPUST:= 0
24 ; IF MONITORMODE THEN GOTO TERROR
25 ; RETURN
26
27 ;55: ESCAPEMODE
28 ; WRK2:=CPUST:= 1B1
29 ; IF ->ESCAPEMODE THEN GOTO TERROR
30 ; WRK2:=CPUST:= 0
31 ; IF ESCAPEMODE THEN GOTO TERROR
32 ; RETURN
```

TMAXE: ZG OR POP  
JMP F,F,TERROR RTN

TNWADR: ZBBR AND,WRK2, LAST  
JMP T,NWADR,TERROR  
CZBR OR,.1B20,WRK2, LAST  
JMP F,NWADR,TERROR RTN

TNGADR: ZBBR AND,WRK2, LAST  
JMP T,NGADR,TERROR  
CZBR OR,.1B0,WRK2, LAST  
JMP F,NGADR,TERROR RTN

TMMODE: CZBR OR,.1B0,WRK2,CPUST  
JMP F,MMODE,TERROR  
ZBBR AND,WRK2,CPUST  
JMP T,MMODE,TERROR RTN

TEMODE: CZBR OR,.1B1,WRK2,CPUST  
JMP F,EMODE,TERROR  
ZBBR AND,WRK2,CPUST  
JMP T,EMODE,TERROR RTN

10150 CPU823 MICROPROGRAM LBJ 850E16 VERSION 8

```

01
02
03 C7021 063100000000006707
04 C7022 063100000000006713
05 C7023 463100000000006717
06 C7024 063100000000006723
07 C7025 463100000000006730
08 C7026 063100000000006734
09 C7027 063100000000006745
10 C7030 463100000000006756
11 C7031 063100000000006764
12 C7032 063100000000006770
13 C7033 063100000000007001
14 C7034 463100000000007005
15 C7035 463100000000007011
16 C7036 063100000000007015

```

JUMPBASE:

```

JMP
JMP
JMP
JMP
JMP
JMP
JMP
JMP
JMP
JMP
JMP
JMP
JMP
JMP
JMP
JMP
JMP
JMP
JMP

```

```

;
;
;
;
;
;
;
;
;
;
;
;
;
;

```

10151 CPU823 MICROPROGRAM LBJ 850016 VERSION 8

;TEST OF HALFWORD MANIPULATOR

```

01
02
03
04 07037 0024730000014000      IZQ  OR,0,600      POP
05 07040 0607230032000000      ZQB  OR,WRKO
06 07041 4624730000162520      IZQ  OR,0,HMBASE/3
07 07042 0607230037000000      ZQB  OR,WRK5
08 07043 0613730036003700      CZB  OR,,1,WRK4
09
10 07044 4605431700000001      ZAR  OR,WRK4,LAST
11 07045 4613730021000300      CZB  OR,B12,W1
12 07046 4213551224003305      CABR CAND,,1B1,STAT,STAT,CPUSH
13 07047 4624730000145520      IZQ  OR,0,HWRES/3-600
14 07050 0631431743000000      CXZA OR,WRK5,F,F
15 07051 4631001503000000      CXAQ ADD,WRKU,F,F
16 07052 4605123574000000      AE   SUB0,WRK1,WRK2
17 07053 063100002426002      CALL T,NZ,TEHROR
18 07054 463100002006003      CALL F,F,LOOP
19 07055 0007302032000000      ZBB  ADD0,WRKO
20 07056 4607320021000000      ZBB  SUB,W1
21 07057 0651523040000300      CA   SUB0,B12,W1
22 07060 0631000000427046      JMP  T,NZ,B603
23 07061 4607310036000000      ZBB  SUN,WRK4
24 07062 063100000027044      JMP  F,NZ,B602
25 07063 0607302037000000      ZBB  ADD0,WRK5
26 07064 063100000007043      JMP  F,F,B601
27

```

HWRES:

```

28 07065 6013730034000300      CZB  OR,B12,WRK2
29 07066 6007340034000000      ZBB  AND,WRK2
30 07067 6007340034000000      ZBB  AND,WRK2
31 07070 6013730034000300      CZB  OR,B12,WRK2
32 07071 6053712034003700      CZB  SUN0,,1,WRK2
33 07072 6007340034000000      ZBB  AND,WRK2
34 07073 6007340034000000      ZBB  AND,WRK2
35 07074 6053712034003700      CZB  SUN0,,1,WRK2
36 07075 6013730034000300      CZB  OR,B12,WRK2
37

```

; TABLE OF EXPECTED DATA

```

;600: HLOAD,ODD=1,00007777      RTN
;601:                               RTN
;602:                               RTN
;603:                               RTN
;604: EXLOA,ODD=1,UUUU7777      RTN
;605:                               RTN
;606:                               RTN
;607:                               RTN
;610: HSTOR,ODD=1,00007777      RTN

```

10152 CPU823 MICROPROGRAM LBJ 850016 VERSION 8

```
01
02 07076 600734003400000 ZBB AND,WRK2 RTN 77770000
03 07077 6053710034000300 CZB SUN,B12,WRK2 RTN 000=0,00007777
04 07100 600734003400000 ZBB AND,WRK2 RTN 77770000
05 07101 6053710034000300 CZB SUN,B12,WRK2 RTN MASK,000=1,00007777
06 07102 6053710034000300 CZB SUN,B12,WRK2 RTN 000=0,00007777
07 07103 6013750034000300 CZB OR,B12,WRK2 RTN 77770000
08 07104 6013750034000300 CZB OR,B12,WRK2 RTN 77770000
09 07105 60537120340003700 CZB SUN0,.1,WRK2 RTN 000=1,00007777
10 07106 6007340034000000 ZBB AND,WRK2 RTN 77770000
11 07107 60537120340003700 CZB SUN0,.1,WRK2 RTN 000=0,00007777
12 07110 6007340034000000 ZBB AND,WRK2 RTN 77770000
13 07111 6053710034000300 CZB SUN,B12,WRK2 RTN 000=1,00007777
14 07112 6013730034000300 CZB OR,B12,WRK2 RTN 77770000
15 07113 6053710034000300 CZB SUN,B12,WRK2 RTN 000=0,00007777
16 07114 6013730034000300 CZB OR,B12,WRK2 RTN 77770000
17 07115 6007340034000000 ZBB AND,WRK2 RTN LSWAP,000=1,00007777
18 07116 6013750034000300 CZB OR,B12,WRK2 RTN 77770000
19 07117 6007340034000000 ZBB AND,WRK2 RTN 000=0,00007777
20 07120 6013730034000300 CZB OR,B12,WRK2 RTN 77770000
21 07121 6053710034000300 CZB SUN,B12,WRK2 RTN RSWAP,000=1,00007777
22 07122 6007340034000000 ZBB AND,WRK2 RTN 77770000
23 07123 6053710034000300 CZB SUN,B12,WRK2 RTN 000=0,00007777
24 07124 6007340034000000 ZBB AND,WRK2 RTN 77770000
```

HWBASE:

```
26
27 07125 2062731073000000 HZB OR,HLOAD,W1,WRK1 RTN
28 07126 6062731073000100 HZB OR,EXLOAD,W1,WRK1 RTN
29 07127 6062731073000200 HZB OR,HSTOK,W1,WRK1 RTN
30 07130 2062731073000300 HZB OR,MASK,W1,WRK1 RTN
31 07131 6062731073000400 HZB OR,EXT,W1,WRK1 RTN
32 07132 2062731073000500 HZB OR,SWAP,W1,WRK1 RTN
33 07133 2062731073000600 HZB OR,LSWAP,W1,WRK1 RTN
34 07134 6062731073000700 HZB OR,RSWAP,W1,WRK1 RTN
```

```
;
;
;
;
;
;
;
;
;
```

10153 CPU823 MICROPROGRAM LBJ 85U816 VERSION 8

```

01 ;
02 ;
03 ;
04 ;
05 07135 4024730000000100      IZQ  OR,0,4      POP
06 07136 0605640000000500      RG   AND,SWTCH
07 07137 0631000000426167      JMP  T,NZ,CPULP
08 07140 4631000002004016      CALL F,F,CINIR
09 07141 0624730017601760      IZQ  OR,77,77
10 07142 0605230000000021      ZQR  OR,ILIM
11 07143 46147300000000100      XZQ  OR,CIOSTA
12 07144 4624640076000000      IQQ  AND,0370,U
13 07145 0613630037002300      CQB  OR,.1B12,WRK5
14 07146 4662731777000500      HZB  OR,SWAP,WRK5,WRK5
15 07147 0607121634000000      ABB  SUB,WRK2,WRK2
16 07150 4647403634000012      ZABR ADD0,WRK2,WRK2,DATO
17 ;
18 07151 4213551224003305      CABR CAND,.1B1,STAT,STAT,CPUST,PUSH
19 07152 0605431740230011      ZAR  OR,WRK5,LAD      CONT WRT
20 07153 0624730000016020      IZQ  OR,U,701
21 07154 0607230032000000      ZQB  OR,WRK0
22 07155 0657730033000100      XZB  OR,CIOST,WRK1      CONT 0 1
23 07156 0613541573003400      CAB  AND,.7,WRK1,WRK1
24 07157 0631000002426163      CALL T,NZ,TBERR
25 07160 0631000002617241      CALL T,EXINT,CHINT
26 ;
27 07161 4631000002006003      CALL F,F,LOOP
28 07162 0051523600003400      CA   SUB0,.7,WRK2      POP
29 07163 0631000000427150      JMP  T,NZ,B75U
30 ;
31 ;
32 07164 4647403634000012      ZABR ADD0,WRK2,WRK2,DATO
33 ;
34 07165 4213551224003305      CABR CAND,.1B1,STAT,STAT,CPUST,PUSH
35 07166 0605431740230011      ZAR  OR,WRK5,LAD      CONT WRT
36 07167 0624730000016200      IZQ  OR,U,710
37 07170 0607230032000000      ZQB  OR,WRK0

```

;TEST OF EXTERNAL INTERRUPTS

B700:

```

;
; IF CPU822 NOT AVAILABLE THEN
;   GOTO CPUL00P
;
; CLEAR EXTERNAL INTERRUPTS
;
; ILIM:= 63,63 C. ALLOW ALL INTERRUPTS
; Q:= CPUIOSTATUS
; Q:= CPUIOSTATUS(4:8)
; WRK5:= 1B12 OR Q
; WRK5:= WRK5(12:23) CON WRK5(0:11)
; FOR WRK2:= U STEP 1 UNTIL .7 DO
; BEGIN
;   DATAOUT:= WRK2
;   CLEAR ERRORFLAG ,PUSH
;   LAD:= WRK5 ,WRT
;
;   WRK0:= 8'701
;   WRK1:= CPUIOSTATUS
;   IF IOSTATUS(21:23) <> U THEN
;     TBERR
;   IF EXINT THEN
;     CHECK INTERRUPR
;     CHECK LOOPING
;   END
;
; FOR WRK2:= 8 STEP 1 UNTIL 63 DO
; BEGIN
;   DATAOUT:= WRK2
;   CLEAR ERRORFLAG , PUSH
;   LAD:= WRK5 ,WRT
;   WRK0:= 8'710

```

B760:

```

;
; FOR WRK2:= 8 STEP 1 UNTIL 63 DO
; BEGIN
;   DATAOUT:= WRK2
;   CLEAR ERRORFLAG , PUSH
;   LAD:= WRK5 ,WRT
;   WRK0:= 8'710

```



IC155 CPU823 MICROPROGRAM LBJ 85U816 VERSION 8

```

01
02
03 07234 46310000002006003      CALL F,F,LOOP
04 07235 0624750000001760      IZQ OR,U,77
05 07236 0005023600000000      AG SUBO,WRK2
06 07237 4631000000427164      JMP T,NZ,B760
07 07240 0631000000007256      JMP F,F,B1000
08
09
10
11 07241 0605230000000022      CHINT: ZGR OR,ILEVR
12 07242 4705230000000000      ZQ OR
13 07243 4705230000000000      ZQ OR
14 07244 4724730000007760      IZQ OR,U,377
15 07245 0617640033000300      XQB AND,ILEV,WRK1
16 07246 4651523540003400      CA SUBO,,WRK1
17 07247 4631000000426002      JMP T,NZ,TEHROR
18 07250 4605431540000020      ZAR OR,WRK1,INTRG
19 07251 0745230000000000      ZQ OR
20 07252 0745230000000000      ZQ OR
21 07253 0745230000000000      ZQ OR
22 07254 0745230000000000      ZQ OR
23 07255 60310000000616002      JMP T,EXINT,TERROR
24
25
26
27
28
29 07256 0613730034003000      B1000: CZB OR,,6,WRK2
30 07257 06247300000166340      IZQ OR,,B1010/3
31 07260 4607230035000000      ZQB OR,WRK3
32 07261 4631431643000000      B1001: CXZA OR,WRK3,F,F
33 07262 0605230000000012      ZGR OR,DATO
34 07263 0605431600230011      ZAR OR,WRK2,LAD
35 07264 06107300000003400      CZQ OR,,7
36 07265 4655640000000100      XG AND,CIOST
37 07266 46310000000427347      JMP T,NZ,B1100

```

; CHECK INTERRUPT (713)

; CHECK LOOPING

; END

; GOTO TEST 1000

;CHECK INTERRUPT

; ILEVR:= 4

; DELAY(675 NSEC)

CONT 0 2

CONT 0 2

CONT 2

; WRK1:= ILEV(16:23)

; IF WRK1<> 7 THEN

; GOTO TERROR

; CLEAR INTERRUPT(WRK1)

; DELAY(900 NSEC)

CONT 0 3

CONT 0 3

CONT 0 3

CONT 0 3

RTN

; IF EXINT THEN GOTO TERROR

; ELSE RETURN

;TEST OF REGISTERS DEFINED BY INSTRUCTIONREGISTER

;AND TEST OF PREFETCH INSTRUCTIONCOUNTER

; WRK3:= ADDR(B1U10)

; FOR WRK2:= 6 STEP -2 UNTIL 0 DO

; BEGIN

; LAD:= GETCONT(WRK3) , WRT

; IF CPUIOSTATUS(21:23)<>0 THEN

; GOTO B1100 C. NEXT TEST

10156 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 07267 4607302035000000 ZBB ADD0,WRK3
02 07270 4653511634003700 CAB SUN,01,WRK2,WRK2
03 07271 063100000017261 JMP F,NEG,B1001
04 07272 4624730000166560 IZQ OR,U,B1012/3
05 07273 0607230037000000 ZQB OR,WRK5
06 07274 0613730032002200 CZB OR,01B14,WRK0
07
08
09 07275 0653710035003700 B1002: CZB SUN,01,WRK3
10 07276 0605240000250011 ZQR AND,LAD
11 07277 4607340036000000 ZBB AND,WRK4
12
13 07300 0607431674000000 B1003: ZABR OR,WRK3,WRK2,ICD
14 07301 743100000007302 JMP F,F,0+3
15 07302 4213551224003305 CABR CAND,01B1,STAT,STAT,CPUST,PUSH
16 07303 0631431743000000 CXZA OR,WRK5,F,F
17 07304 4605123633000000 AB SUB0,WRK2,WRK1
18 07305 0631000002426002 CALL T,NZ,TERROR
19 07306 4631000002006003 CALL F,F,LOOP
20 07307 4053503675003700 CAB ADD0,01,WRK3,WRK3
21 07310 0607302032000000 ZBB ADD0,WRKU
22 07311 4607302036000000 ZBB ADD0,WRK4
23 07312 4651523700001500 CA SUB0,04,WRK4
24 07313 4631000000427300 JMP T,NZ,B1003
25 07314 4653501777001400 CAB ADD,03,WRK5,WRK5
26 07315 463100000007275 JMP F,F,B1002
27
28
29
30 07316 2024730014600100 IZQ OR,0063,0004
31 07317 2024730010400040 IZQ OR,0042,0002
32 07320 6024730004200000 IZQ OR,0021,0000
33 07321 202473000177740 IZQ OR,0000,7776

```

```

; WRK3:= WRK3+1
; END
;
; WRK5:= ADDK(B1012)
; WRKU:= 8'100C
; REPEAT
;   WRK3:= -2
;   LAD:= U, READ TO INSTR REG
;   WRK4:= U
;   REPEAT
;     WRK2:=ICD:= WRK3
;     LOAD INSTR REG
;     CLEAR ERROR FLAG
;     IF EXPECTED(WRK5)<>
;       RECEIVED(WRK4,WRK5) THEN
;         TERROR
;         TEST LOOPING
;         WRK3:= WRK3+2
;         WRKU:= WRKU+1
;         WRK4:= WRK4+1
;         UNTIL WRK4=4
;     WRK5:= WRK5+3
;     UNTIL FALSE
;
;B1010: C. DATA TO BE LOADED IN
; INSTRUCTIONSREGISTER
; ADDR 6
; ADDR 4
; ADDR 2
; ADDR 0

```

```

CONT REAJ
NEXT
CUST
POP
RTN
RTN
RTN
RTN

```



10157 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01
02
03 07322 6007431173000000
04 07323 6007431033000000
05 07324 2007431073000000
06 07325 2007431133000000
07 07326 6007431173000000
08
09
10 07327 4607431640000000
11 07330 0624730000166460
12 07331 4631001701000000
13 07332 0607431641000000
14 07333 4624730000166440
15 07334 4631001701000000
16 07335 0607431642000000
17 07336 0624730000166460
18 07337 4631001701000000
19 07340 2007730033000000
20 07341 0605230000000000
21 07342 0605230000000000
22 07343 4647501674001000
23 07344 6007730033002000
24 07345 0605230000000000
25 07346 0005230000000000
26
27
28
29
30 07347 46117300000003724
31 07350 46137300200003700
32 07351 0605240000000013
33 07352 4605240000000003
34 07353 0624730000022000
35 07354 0607250032000000
36 07355 46117300000001614
37 07356 46117300000000515
38 07357 06137300360003700
39 07360 46117300000001116

;B1011:
;
;
;
;
;
;B1012:
; 1000 = 1003
;
;
; 1004 = 1007
;
;
; 1010 = 1013
;
;
; 1014 = 1017
;
;
; 1020 = 1023
;
;
;
;
;B1100:
; DISABLE CAM
; FOR BASE:= U, 64 D0
; BEGIN
; ICD:= BASE
; WRKU:= 8'1100
;
; LLIM:= 16
; ULIM:= 22
; FOR CPA:= 12,32 D0
; BEGIN

RTN
RTN
RTN
RTN
RTN

RTN

POP

;TEST OF I/O PROTECTION SYSTEM

B1011:
ZAB OR,W3,WRK1
ZAB OR,W0,WRK1
ZAB OR,W1,WRK1
ZAB OR,W2,WRK1
ZAB OR,W3,WRK1

B1012:
ZAB OR,WRK3,W
IZQ OR,U,B1011/3+1
JXAQ ADD,WRK4,F,F
ZAB OR,WRK3,WPRE
IZQ OR,U,B1011/3
JXAQ ADD,WRK4,F,F
ZAB OR,WRK3,X
IZQ OR,U,B1011/3+1
JXAQ ADD,WRK4,F,F
RZB OR,DISP,WRK1
ZQ OR
ZQ OR
RAB ADD,ICS,WRK3,WRK2
RZB OR,DIC,WRK1
ZQ OR
ZQ OR

B1100:
CZR OR,.1,CAMBC
CZB OR,.1,WU
ZQR AND,BASE
ZQR AND,ICD
B1101: IZQ OR,U,1100
ZQB OR,WRKU
CZR OR,.16,LLIM
CZR OR,.22,ULIM
CZB OR,.1,WRK4
CZR OR,.12,CPA

```

10158 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 02 07361 0624730000170000 B1102: IZQ OR,U,B1110/3
03 07362 0607230037000000 ZQB OR,WRK5
04 07363 4213551224003305 B1103: CABR CAND,,B1,STAT,STAT,CPUST PUSH
05 07364 0631431743000000 CXZA OR,WRK5,F,F
06 07365 4624730000147120 IZQ OR,O,B1111/3-1100
07 07366 4631001503000000 CXAQ ADD,WRKU,F,F
08 07367 0607230034000000 ZQB OR,WRK2
09 07370 4624730000602000 IZQ OR,3,1U
10 07371 0657640033000100 XGB AND,CIOST,WRK1
11 07372 4605123574000000 AB SUBO,WRK1,WRK2
12 07373 0631000024260002 CALL T,NZ,TERROR
13 07374 4631000020006003 CALL F,F,LOOP
14 07375 0007302032000000 ZEB ADDO,WRKO
15 07376 0607302037000000 ZEB ADDO,WRK5
16 07377 4631000000007363 JMP F,F,B1103
17

```

B1110:

```

18 19 07400 6051710000203711 CZR SUN,,1,LAD
20 07401 2051710000223711 CZR SUN,,1,LAD
21 07402 6011730000201311 CZR OR,,10,LAD
22 07403 2011730000221311 CZR OR,,10,LAD
23 07404 6011730000201011 CZR OR,,14,LAD
24 07405 2011730000221011 CZR OR,,14,LAD
25 07406 2051720000200611 CZR SUB,,21,LAD
26 07407 6051720000220611 CZR SUB,,21,LAD
27 07410 6051702000200411 CZR ADDO,,23,LAD
28 07411 2051702000220411 CZR ADDO,,23,LAD
29 07412 6011730000202011 CZR OR,,64,LAD
30 07413 2011730000222011 CZR OR,,64,LAD
31 07414 4011730000001716 CZR OR,,32,CPA
32 07415 4607310036000000 ZBB SUN,WRK4
33 07416 4631000000027361 JMP F,NZ,B1102
34 07417 0607310020000000 ZBB SUN,W0
35 07420 4631000000427425 JMP T,NZ,B1106
36 07421 0651712000003712 CZR SUNO,,1,DATO
37 07422 0611730000002013 CZR OR,,64,BASE
38 07423 4611730000002003 CZR OR,,64,ICD
39 07424 4631000000007353 JMP F,F,B1101

```

```

; FOR LAD:= -2,10,14,20,24,6,4 DO
; FOR FUNC:= READP,WRTP DO
; BEGIN
;   START IO(LAD,FUNC)
;   WRK2:= EXPECTED STATUS(WRKU)
;   WRK1:= CPUIOSTAT AND 8'30010
;   IF WRK1<>WRK2 THEN
;     TERROR
;     TEST LOOPING
;     WRKU:= WRKU+1
;   END FOR FUNC, LAD
;
;   LAD:= -2 , READP
;   LAD:= -2 , WRTP
;   LAD:= 10 , READP
;   LAD:= 10 , WRTP
;   LAD:= 14 , READP
;   LAD:= 14 , WRTP
;   LAD:= 20 , READP
;   LAD:= 20 , WRTP
;   LAD:= 24 , READP
;   LAD:= 24 , WRTP
;   LAD:= 64 , READP
;   LAD:= 64 , WRTP
;
;   END FOR CPA
;   IF BASE=U THEN
;     DATO:= -1
;

```

```

RTN READP
RTN WRTP
RTN READP
RTN WRTP
RTN READP
RTN WRTP
RTN READP
RTN WRTP
RTN READP
RTN WRTP
RTN READP
RTN WRTP
RTN READP
RTN WRTP
RTN READP
RTN WRTP
RTN READP
RTN WRTP
RTN READP
RTN WRTP
RTN READP
RTN WRTP
RTN READP
RTN WRTP
POP

```

CONT 0 1

POP

10159 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```

01 07425 461473000000100 B1106: XZQ OR,CIOST
02 07426 4611640000003400 AND,.7
03 07427 4631000000427443 T,NZ,C11EX
04 07430 0624730000023760 OR,U,1177
05 07431 0607230000000000 OR,WRKU
06 07432 4624730000002500 OR,U,84.
07 07433 06052300000210011 OR,LAD
08 07434 07157020000004000 ADDO,DATI
09 07435 06310000002426002 T,NZ,TERROR
10 07436 02052400000000012 AND,DATO
11 07437 0651720000220611 SUB,.21,LAD
12 07440 4655730000000100 OR,CIOST
13 07441 46310000002006003 F,F,LOOP
14 07442 00052300000000000 OR
15 07443 46052400000000024 AND,CAMBC
16 07444 063100000000006167 F,F,CPULP
17

```

```

; IF CPU10STATUS(21:23)<>0 THEN
; BEGIN
; WRKU:= 8'1177 C. TEST RELOCATING
;
; LAD:= 84 , READ
; IF DATI<>=1 THEN
;   TERROR
; DATO:= U , PUSH
; LAD:= 2U , WRTP
;
; TESTLOOPING
; END
; ENABLE CAM
; GOTO TEST CPURUNS

```

```

CONT READ
CONT U 2
PUSH
CONT WRTP
CONT U 1
POP

```



10161 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

; CONTENTS OF REGISTERS DURING MEMORY TEST

```

;WRK0: STATUS
;WRK1: RECEIVED DATA
;WRK2: EXPECTED DATA
;WRK3: NEWDATA
;WRK4: WORK
;WRK5: ?
;SB: OLDDATA
;W0: CURRENT MEMORY START
;W1: CURRENT INCREMENT
;W3: LAST MEMORY LOCATION
;ICS: MEMORY ADDRESS

```

MEMTEST:

```

17 C7475 46135314710C3406 CABR OR,7,CNTR,CNTR,CNTR0
18 C7476 06071514310C0006 ABBR CAND,2,CNTR,CNTR0
19 C7477 4624730000C0160 IZQ OR,0,7
20 C7500 06310000C2164013 CALL F,MODE,TPTXT
21 C7501 4213551224C03305 CABR CAND,1B1,STAT,STAT,CPUST,PUSH
22 C7502 4611730000C03724 CZR OR,1,CAMBC
23 C7503 06310000C06004 CALL F,F,SIZEM
24 C7504 46310000C2016163 CALL F,NEG,TBERR
25 C7505 46310000C06003 CALL F,F,LOOP
26 C7506 00073400C26C0000 ZBB AND,SB
27 C7507 46073400C35C0000 ZBB AND,WRK3
28 C7510 06537020210C3700 CZB ADD0,1,W1
29 C7511 46137300C37C03400 CZB OR,7,WRK5
30 C7512 0651503140C03715 CAR ADD0,1,W3,ULIM
31 C7513 0651503140C03716 CAR ADD0,1,W3,CPA
32 C7514 0605240000C0013 ZQR AND,BASE
33 C7515 0611730000C03614 CZR OR,8,LLIM

```

A02:

```

34
35 C7516 0613551224002405 CABR CAND,1B7,STAT,STAT,CPUST

```

```

;MEMTEST START
;
; TURN LAMP 2 ON
; IF TCP THEN
;   TYPE(BELL)
; CLEAR ERRORFLAG , PUSH
; CAMBC:= 1 C. DISABLE CAM
; SIZE AND CLEAR MEM
; IF IOERR0R THEN TBERR
; TEST LOOPING
;
; OLDDATA:= NEWDATA:= 0
; W1:= 2 DISPLACEMENT
; WRK5:= 7
;
; ULIM:=CPA:= W3+2
; BASE:= 0
; LLIM:= 8
; WHILE W1< MEMSIZE DO
; BEGIN

```





IU164 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

```
01
02 07622 0637530021000060
03 07623 4611541200003100
04 07624 0631000000427626
05 07625 0637530021000060
06 07626 4605125161000000
07 07627 463100000017516
08 07630 4605240000000024
09 07631 4631000000000075
10
11
12 07632 4611541200003100
13 07633 4631000000427641
14 07634 0624751252525240
15 07635 4607230035000000
16 07636 0605411240000000
17 07637 2031000000027640
18 07640 6007421675000000
19
20 07641 0637330035000060
21 07642 0624730000000060
22 07643 0605013240000000
23 07644 6031000000417645
24 07645 2007502035000000
25
26
27 07646 4617730033000400
28 07647 0607340032000000
29 07650 0607431674000000
30 07651 46310000000006002
31
32
33 07652 4617730033000400
34 07653 46137500320003700
35 07654 4607431334000000
36 07655 46310000000006002
37
```

```
; W1:= IF FULL TEST THEN W1 SHIFT 1
; ELSE W1 SHIFT 2
;
;
; END WHILE DISP< MEMSIZE
; CAMBYPASS:= 0
;
; GETDATA
;
; IF -,FULL TEST THEN
; BEGIN
; NEWDATA:= 8152525252
; IF CAUSE=1 THEN
; NEWDATA:= -,NEWDATA
; END ELSE
; BEGIN
; NEWDATA:= NEWDATA SHIFT 1
; IF CAUSE>24 THEN
; CAUSE:= CAUSE+1
; END
;
; NEWDATAERROR
; WRK1:= DAT1
; WRK0:= 0
; WRK2:= WRK3 EXPECTED:= NEWDATA
; GOTO TERROR
;
; OLDDATAERROR
; WRK1:= DAT1
; WRK0:= 1
; WRK2:= SB EXPECTED:= OLDDATA
; GOTO TERROR
```

```
GETDATA:
CA AND, 1B3, STAT
JMP T, NZ, EXTDATA
I2Q OR, 5252, 5252
ZOB OR, WRK3
ZA SUN, CAUSE
JMP F, NZ, +3
ZAB SUB, WRK3, WRK3
RTN
```

```
EXTDATA:
SLZBB OR, WRK3, Z, NL, F, F
I2Q OR, 0, 24
AG SUN0, CAUSE
JMP T, NEG, +3
ZBB ADD0, WRK3
RTN
```

```
NDERR:
XZB OR, DAT1, WRK1
ZBB AND, WRK0
ZAB OR, WRK3, WRK2
JMP F, F, TERROR
RTN
```

```
ODERR:
XZB OR, DAT1, WRK1
CZB OR, 1, WRK0
ZAB OR, SB, WRK2
JMP F, F, TERROR
RTN
```



10165 CPU823 MICROPROGRAM LBJ 850816 VERSION 8

;PROCEDURE TO CLEAR AND SIZE MEMORY

; CONDITIONS AT RETURN:

NEG=T C. OK  
 NEG=F C. IOERROR

; BEGIN

; WRK4:= 6

; REPEAT

; DATO:= WRK4:= WRK4+2

; LAD:= WRK4 ,WRT

; Q:= 7

; IF IOERROR THEN

    GOTO ADREND

; LAD:= WRK4 ,READ

; IF IOERROR THEN

    GOTO ADREND

; IF DATAIN<>WRK4 THEN

    GOTO ADHEND

; UNTIL FALSE

;ADREND:

; DATAOUT:= 0

; LAD:=W3:= WRK4-2 C. LAST MEM LOC

CONT WRT

CONT 0 1

CONT READ

CONT 0 1

CONT WRT

CONT 0 1

RTN

CONT WRT

CONT 0 1

RTN

;SIZEMEM:

-IZEMEM:

01	02	03	04	05	06	07	08	09	10	11	12	13	14	15	16	17	18	19
07656	46137300360C3000	CZB	OR,6,WRK4															
07657	46535037360C3712	SIZE1:	ADDO,1,WRK4,WRK4,DATO															
07660	4605431700230011	ZAR	OK,WRK4,LAD															
07661	06107300000C3400	CZQ	OK,7															
07662	4655640000000100	XQ	AND,CIOST															
07663	4631000000427671	JMP	T,NZ,ADREND															
07664	0605431700210011	ZAR	OR,WRK4,LAD															
07665	06556400000C4100	XQ	AND,IOSTA															
07666	4631000000427671	JMP	T,NZ,ADREND															
07667	06552370000C4000	XA	SUBO,DATI,WRK4															
07670	46310000000C27657	JMP	F,NZ,SIZE1															

ADREND:

21	22	23	24	25	26	27	28	29	30	31	32
07671	46052400000C0012	ZQR	AND,DATO								
07672	4653511723233711	CABR	SUN,1,WRK4,W3,LAD								
07673	06054317000C0003	ZAR	OR,WRK4,ICD								
07674	46071134360C0000	ABB	SUNO,2,WRK4								
07675	4631000000417702	JMP	T,NEG,ADR2								
07676	46556400000C00100	XQ	AND,CIOST								
07677	20310000000C27700	JMP	F,NZ,+3								
07700	4605431700230011	ZAR	OR,WRK4,LAD								
07701	06310000000C07673	JMP	F,F,ADR1								
07702	46556400000C00100	XQ	AND,CIOST								
07703	60517120000C03712	CZR	SUNO,1,DATO								

53

10166 CPUB23 MICROPROGRAM LBJ 850016 VERSION 8

01

0000 SOURCE LINES IN ERROR

0001 .MAIN DOMUS MACRO ASSEMBLER REV 02.00

01  
02  
03  
04  
05  
06  
07  
08  
09  
10  
11  
12  
13  
14

; INSTRUCTION EXECUTION DECODING MAP

; U: ESCAPE MODE  
; 1: FPU NOT INSTALLED  
; 2: INSTR BIT(6)  
; 3: INSTR BIT (7)  
; 4: INSTR BIT(0)  
; 5: (1)  
; 6: (2)  
; 7: (3)  
; 8: (4)  
; 9: (5)

100C2 -MAIN  
01 000000 -LOC 0  
02  
03 CG000 UG0032 INTO/3  
04 CC001 002274 DO/3  
05 CG002 000250 EL/3  
06 CG003 000227 HL/3  
07 CG004 000073 LA/3  
08 CG005 000100 LO/3  
09 CG006 000105 LX/3  
10 CG007 000112 WA/3  
11 CG010 000121 WS/3  
12 CG011 000053 AM/3  
13 CG012 000145 WMFPU/3  
14 CG013 000057 AL/3  
15 CG014 002472 XI/3  
16 CG015 000624 JL/3  
17 CG016 000657 JD/3  
18 CG017 000674 JE/3  
19 CG020 000304 XL/3  
20 CG021 000272 ES/3  
21 CG022 000260 EA/3  
22 CG023 000240 ZL/3  
23 CG024 000066 RL/3  
24 CG025 002245 SP/3  
25 CG026 001351 RE/3  
26 CG027 000316 RS/3  
27 CG030 000165 WD/3  
28 CG031 000412 RX/3  
29 CG032 000360 HS/3  
30 CG033 000376 XS/3  
31 CG034 005225 GG/3  
32 CG035 002257 DI/3  
33 CG036 002403 AP/3  
34 CG037 002367 UL/3

;FPU INSTALLED

J0663 -MAIN  
01  
02 CC040 000711 C1/3  
03 CC041 000062 AC/3  
04 CC042 000562 NS/3  
05 CC043 000606 ND/3  
06 CC044 000501 AS/3  
07 CC045 000513 AD/3  
08 CC046 000533 LS/3  
09 CC047 000545 LD/3  
10 CC050 002200 SH/3  
11 CC051 002205 SL/3  
12 CC052 002212 SE/3  
13 CC053 002217 SN/3  
14 CC054 002224 SO/3  
15 CC055 002232 SZERO/3  
16 CC056 002237 SX/3  
17 CC057 005232 GP/3  
18 CC060 001270 FAFPU/3  
19 CC061 001343 FSFPU/3  
20 CC062 001066 FMFPU/3  
21 CC063 000032 INTO/3  
22 CC064 001213 FDFPU/3  
23 CC065 000757 CF/3  
24 CC066 000431 DL/3  
25 CC067 000332 DS/3  
26 CC070 000443 AA/3  
27 CC071 000462 SS/3  
28 CC072 002447 DP/3  
29 CC073 003010 MH/3  
30 CC074 002316 LK/3  
31 CC075 003044 IX/3  
32 CC076 000032 INTO/3  
33 CC077 000032 INTO/3

10004 .MAIN

01  
02 000005 .DO 3  
03 CG100 000032 INTG/3  
04 CG101 002274 DO/3  
05 CG102 000250 EL/3  
06 CG103 000227 HL/3  
07 CG104 000073 LA/3  
08 CG105 000100 LO/3  
09 CG106 000105 LX/3  
10 CG107 000112 WA/3  
11 CG110 000121 WS/3  
12 CG111 000053 AM/3  
13 CG112 000145 WMFPU/3  
14 CG113 000057 AL/3  
15 CG114 002472 XI/3  
16 CG115 000651 JLL/3  
17 CG116 000657 JDL/3  
18 CG117 000702 JEL/3  
19 CG120 000304 XL/3  
20 CG121 000272 ES/3  
21 CG122 000260 EA/3  
22 CG123 000240 ZL/3  
23 CG124 000066 RL/3  
24 CG125 002245 SP/3  
25 CG126 001351 RE/3  
26 CG127 000316 RS/3  
27 CG130 000165 WD/3  
28 CG131 000412 RX/3  
29 CG132 000360 HS/3  
30 CG133 000376 XS/3  
31 CG134 005225 GG/3  
32 CG135 002257 DI/3  
33 CG136 002403 AP/3  
34 CG137 002367 UL/3

; W-FIELD<>0

IG005 -MAIN

01  
02 C0140 000711 C1/3  
03 C0141 000062 AC/3  
04 C0142 000562 NS/3  
05 C0143 000606 ND/3  
06 C0144 000501 AS/3  
07 C0145 000513 AD/3  
08 C0146 000533 LS/3  
09 C0147 000545 LD/3  
10 C0150 002200 SH/3  
11 C0151 002205 SL/3  
12 C0152 002212 SE/3  
13 C0153 002217 SN/3  
14 C0154 002224 SO/3  
15 C0155 002232 SZERO/3  
16 C0156 002237 SX/3  
17 C0157 005232 GP/3  
18 C0160 001270 FAFPU/3  
19 C0161 001343 FSFPU/3  
20 C0162 001066 FMFPU/3  
21 C0163 000052 INTG/3  
22 C0164 001213 FDFPU/3  
23 C0165 000757 CF/3  
24 C0166 000431 DL/3  
25 C0167 000352 DS/3  
26 C0170 000443 AA/3  
27 C0171 000462 SS/3  
28 C0172 002447 DP/3  
29 C0173 003010 MH/3  
30 C0174 002316 LK/3  
31 C0175 003044 IX/3  
32 C0176 000052 INTG/3  
33 C0177 000052 INTG/3





10007 .MAIN

01			
02	00240	000711	CI/3
03	00241	000062	AC/3
04	00242	000562	NS/3
05	00243	000606	ND/3
06	00244	000501	AS/3
07	00245	000513	AD/3
08	00246	000533	LS/3
09	00247	000545	LD/3
10	00250	002200	SH/3
11	00251	002205	SL/3
12	00252	002212	SE/3
13	00253	002217	SN/3
14	00254	002224	SO/3
15	00255	002232	SZER0/3
16	00256	002237	SX/3
17	00257	005232	GP/3
18	00260	001270	FAFPU/3
19	00261	001343	FSFPU/3
20	00262	001066	FMFPU/3
21	00263	000032	INTG/3
22	00264	001213	FDFFPU/3
23	00265	000757	CF/3
24	00266	000431	DL/3
25	00267	000332	DS/3
26	00270	000443	AA/3
27	00271	000462	SS/3
28	00272	002447	DP/3
29	00273	003010	MH/3
30	00274	002316	LK/3
31	00275	003044	IX/3
32	00276	000032	INTG/3
33	00277	000032	INTG/3

10CC8 .MAIN  
01  
02                   • ENDC  
03 00300 000032 INTC/3  
04 00301 002274 D0/3  
05 00302 000250 EL/3  
06 00303 000227 HL/3  
07 00304 000073 LA/3  
08 00305 000100 LO/3  
09 00306 000105 LX/3  
10 00307 000112 MA/3  
11 00310 000121 WS/3  
12 00311 000053 AM/3  
13 00312 000145 WMFPU/3  
14 00313 000057 AL/3  
15 00314 002472 XI/3  
16 00315 000631 JLL/3  
17 00316 000657 JDL/3  
18 00317 000702 JEL/3  
19 00320 000304 XL/3  
20 00321 000272 ES/3  
21 00322 000260 EA/3  
22 00323 000240 ZL/3  
23 00324 000066 HL/3  
24 00325 002245 SP/3  
25 00326 001351 RE/3  
26 00327 000316 RS/3  
27 00330 000165 WD/3  
28 00331 000412 RX/3  
29 00332 000360 HS/3  
30 00333 000376 XS/3  
31 00334 005225 GG/3  
32 00335 002257 DI/3  
33 00336 002403 AP/3  
34 00337 002367 UL/3

10009 -MAIN

01  
02 C0340 000711 CI/3  
03 C0341 000062 AC/3  
04 C0342 000562 NS/3  
05 C0343 000606 ND/3  
06 C0344 000501 AS/3  
07 C0345 000513 AD/3  
08 C0346 000533 LS/3  
09 C0347 000545 LD/3  
10 C0350 002200 SH/3  
11 C0351 002205 SL/3  
12 C0352 002212 SE/3  
13 C0353 002217 SN/3  
14 C0354 002224 SO/3  
15 C0355 002232 SZERO/3  
16 C0356 002237 SX/3  
17 C0357 005232 GP/3  
18 C0360 001270 FAFPU/3  
19 C0361 001343 FSFPU/3  
20 C0362 001066 FMFPU/3  
21 C0363 000032 INTC/3  
22 C0364 001213 FDFPU/3  
23 C0365 000757 CF/3  
24 C0366 000431 DL/3  
25 C0367 000332 DS/3  
26 C0370 000443 AA/3  
27 C0371 000462 SS/3  
28 C0372 002447 DP/3  
29 C0373 003010 MH/3  
30 C0374 002316 LK/3  
31 C0375 003044 IX/3  
32 C0376 000032 INTC/3  
33 C0377 000032 INTC/3

10010 -MAIN

01  
02  
03 CC400 000032  
04 CC401 002274  
05 CC402 000250  
06 CC403 000227  
07 CC404 000073  
08 CC405 000100  
09 CC406 000105  
10 CC407 000112  
11 CC410 000121  
12 CC411 000053  
13 CC412 000130  
14 CC413 000057  
15 CC414 002472  
16 CC415 000624  
17 CC416 000637  
18 CC417 000674  
19 CC420 000304  
20 CC421 000272  
21 CC422 000260  
22 CC423 000240  
23 CC424 000066  
24 CC425 002245  
25 CC426 001351  
26 CC427 000316  
27 CC430 000165  
28 CC431 000412  
29 CC432 000360  
30 CC433 000376  
31 CC434 005225  
32 CC435 002257  
33 CC436 002403  
34 CC437 002367

-ENDC

INTG/3  
D0/3  
EL/3  
HL/3  
LA/3  
LO/3  
LX/3  
WA/3  
WS/3  
AM/3  
WM/3  
AL/3  
XI/3  
JL/3  
JD/3  
JE/3  
XL/3  
ES/3  
EA/3  
ZL/3  
RL/3  
SP/3  
RE/3  
RS/3  
WD/3  
RX/3  
HS/3  
XS/3  
GG/3  
DI/3  
AP/3  
UL/3

;FPU NOT INSTALLED

!0011 .MAIN  
01  
02 00440 000711 C1/3  
03 00441 000062 AC/3  
04 00442 000562 NS/3  
05 00443 000606 ND/3  
06 00444 000501 AS/3  
07 00445 000513 AD/3  
08 00446 000533 LS/3  
09 00447 000545 LD/3  
10 00450 002200 SH/3  
11 00451 002205 SL/3  
12 00452 002212 SE/3  
13 00453 002217 SN/3  
14 00454 002224 SO/3  
15 00455 002232 SZERO/3  
16 00456 002237 SX/3  
17 00457 005232 GP/3  
18 00460 001221 FA/3  
19 00461 001276 FS/3  
20 00462 001010 FM/3  
21 00463 000032 INTO/3  
22 00464 001135 FD/3  
23 00465 000757 CF/3  
24 00466 000451 DL/3  
25 00467 000332 DS/3  
26 00470 000443 AA/3  
27 00471 000462 SS/3  
28 00472 002447 DP/3  
29 00473 003010 MH/3  
30 00474 002316 LK/3  
31 00475 003044 IX/3  
32 00476 000032 INTG/3  
33 00477 000032 INTO/3

10012 .MAIN

01  
02           000003 -D0 3  
03 C0500 000032 INTO/3  
04 C0501 002274 D0/3  
05 C0502 000250 EL/3  
06 C0503 000227 HL/3  
07 C0504 000073 LA/3  
08 C0505 000100 LO/3  
09 C0506 000105 LX/3  
10 C0507 000112 WA/3  
11 C0510 000121 WS/3  
12 C0511 000053 AM/3  
13 C0512 000130 WM/3  
14 C0513 000057 AL/3  
15 C0514 002472 XI/3  
16 C0515 000631 JLL/3  
17 C0516 000657 JDL/3  
18 C0517 000702 JEL/3  
19 C0520 000304 XL/3  
20 C0521 000272 ES/3  
21 C0522 000260 EA/3  
22 C0523 000240 ZL/3  
23 C0524 000066 RL/3  
24 C0525 002245 SP/3  
25 C0526 001351 RE/3  
26 C0527 000316 RS/3  
27 C0530 000165 WD/3  
28 C0531 000412 RX/3  
29 C0532 000360 HS/3  
30 C0533 000376 XS/3  
31 C0534 005225 GG/3  
32 C0535 002257 DI/3  
33 C0536 002403 AP/3  
34 C0537 002367 UL/3

; h=FIELD<>0

10013 .MAIN

01  
02 00540 000711 CI/3  
03 00541 000062 AC/3  
04 00542 000562 NS/3  
05 00543 000606 ND/3  
06 00544 000501 AS/3  
07 00545 000513 AD/3  
08 00546 000533 LS/3  
09 00547 000545 LD/3  
10 00550 002200 SH/3  
11 00551 002205 SL/3  
12 00552 002212 SE/3  
13 00553 002217 SN/3  
14 00554 002224 SO/3  
15 00555 002232 SZERO/3  
16 00556 002237 SX/3  
17 00557 005232 GP/3  
18 00560 001221 FA/3  
19 00561 001276 FS/3  
20 00562 001010 FM/3  
21 00563 000032 INTC/3  
22 00564 001135 FD/3  
23 00565 000757 CF/3  
24 00566 000431 DL/3  
25 00567 000332 DS/3  
26 00570 000443 AA/3  
27 00571 000462 SS/3  
28 00572 002447 DP/3  
29 00573 003010 MH/3  
30 00574 002316 LK/3  
31 00575 003044 IX/3  
32 00576 000032 INTC/3  
33 00577 000032 INTC/3

10014 .MAIN

01			
02		-ENDC	
03	CC600	000032	INT0/3
04	CC601	002274	D0/3
05	CC602	000250	EL/3
06	CC603	000227	HL/3
07	CC604	000073	LA/3
08	CC605	000100	LO/3
09	CC606	000105	LX/3
10	CC607	000112	WA/3
11	CC610	000121	WS/3
12	CC611	000053	AM/3
13	CC612	000130	WM/3
14	CC613	000057	AL/3
15	CC614	002472	XI/3
16	CC615	000631	JLL/3
17	CC616	000657	JDL/3
18	CC617	000702	JEL/3
19	CC620	000304	XL/3
20	CC621	000272	ES/3
21	CC622	000260	EA/3
22	CC623	000240	ZL/3
23	CC624	000066	RL/3
24	CC625	002245	SP/3
25	CC626	001351	RE/3
26	CC627	000316	RS/3
27	CC630	000165	WD/3
28	CC631	000412	RX/3
29	CC632	000360	HS/3
30	CC633	000376	XS/3
31	CC634	005225	GG/3
32	CC635	002257	DI/3
33	CC636	002403	AP/3
34	CC637	002367	UL/3



10015 -MAIN

01  
02 CC640 000711 CI/3  
03 CC641 00062 AC/3  
04 CC642 000562 NS/3  
05 CC643 000606 ND/3  
06 CC644 000501 AS/3  
07 CC645 000513 AD/3  
08 CC646 000533 LS/3  
09 CC647 000545 LD/3  
10 CC650 002200 SH/3  
11 CC651 002205 SL/3  
12 CC652 002212 SE/3  
13 CC653 002217 SN/3  
14 CC654 002224 SO/3  
15 CC655 002232 SZERO/3  
16 CC656 002237 SX/3  
17 CC657 005232 GP/3  
18 CC660 001221 FA/3  
19 CC661 001276 FS/3  
20 CC662 001010 FM/3  
21 CC663 000032 INIC/3  
22 CC664 001135 FD/3  
23 CC665 000757 CF/3  
24 CC666 000431 DL/3  
25 CC667 000332 DS/3  
26 CC670 000443 AA/3  
27 CC671 000462 SS/3  
28 CC672 002447 DP/3  
29 CC673 003010 MH/3  
30 CC674 002316 LK/3  
31 CC675 003044 IX/3  
32 CC676 000032 INTO/3  
33 CC677 000032 INTO/3

10016 .MAIN

01				
02				•ENDC
03	00700	000032		INT0/3
04	00701	002274		D0/3
05	00702	000250		EL/3
06	00703	000227		HL/3
07	00704	000073		LA/3
08	00705	000100		LO/3
09	00706	000105		LX/3
10	00707	000112		WA/3
11	00710	000121		WS/3
12	00711	000053		AM/3
13	00712	000130		WM/3
14	00713	000057		AL/3
15	00714	002472		XI/3
16	00715	000631		JLL/3
17	00716	000657		JDL/3
18	00717	000702		JEL/3
19	00720	000304		XL/3
20	00721	000272		ES/3
21	00722	000260		EA/3
22	00723	000240		ZL/3
23	00724	000066		RL/3
24	00725	002245		SP/3
25	00726	001351		RE/3
26	00727	000316		RS/3
27	00730	000165		WD/3
28	00731	000412		RX/3
29	00732	000360		HS/3
30	00733	000376		XS/3
31	00734	005225		GG/3
32	00735	002257		DI/3
33	00736	002403		AP/3
34	00737	002367		UL/3

10017 -MAIN

01  
02 00740 000711 CI/3  
03 00741 000062 AC/3  
04 00742 000562 NS/3  
05 00743 000606 ND/3  
06 00744 000501 AS/3  
07 00745 000513 AD/3  
08 00746 000533 LS/3  
09 00747 000545 LD/3  
10 00750 002200 SH/3  
11 00751 002205 SL/3  
12 00752 002212 SE/3  
13 00753 002217 SN/3  
14 00754 002224 SO/3  
15 00755 002232 SZERO/3  
16 00756 002237 SX/3  
17 00757 005232 GP/3  
18 00760 001221 FA/3  
19 00761 001276 FS/3  
20 00762 001010 FM/3  
21 00763 000032 INTO/3  
22 00764 001135 FD/3  
23 00765 000757 CF/3  
24 00766 000431 DL/3  
25 00767 000332 DS/3  
26 00770 000443 AA/3  
27 00771 000462 SS/3  
28 00772 002447 DP/3  
29 00773 003010 MH/3  
30 00774 002316 LK/3  
31 00775 003044 IX/3  
32 00776 000032 INTO/3  
33 00777 000032 INTO/3

J0018 .MAIN

U1  
U2  
U3  
U4 01000 002303 F00E/3  
U5 01001 002272 00E/3  
U6 01002 000246 ELE/3  
U7 01003 000225 HLE/3  
U8 01004 000071 LAE/3  
U9 01005 000076 LOE/3  
U10 01006 000103 LXE/3  
U11 01007 000110 WAE/3  
U12 01010 000117 WSE/3  
U13 01011 000051 AME/3  
U14 01012 000143 WMFPE/3  
U15 01013 000055 ALE/3  
U16 01014 002470 XIE/3  
U17 01015 000622 JLE/3  
U18 01016 000635 JDE/3  
U19 01017 000672 JEE/3  
U20 01020 000302 XLE/3  
U21 01021 000270 ESE/3  
U22 01022 000256 EAE/3  
U23 01023 000236 ZLE/3  
U24 01024 000064 RLE/3  
U25 01025 002243 SPE/3  
U26 01026 001347 REE/3  
U27 01027 000314 RSE/3  
U28 01030 000163 WDE/3  
U29 01031 000410 RXE/3  
U30 01032 000356 HSE/3  
U31 01033 000374 XSE/3  
U32 01034 005223 GGE/3  
U33 01035 002255 DIE/3  
U34 01036 002401 APE/3  
U35 01037 002365 ULE/3

-ENDC

;ESCAPE MODE  
;FPU INSTALLED

10019 .MAIN

01				
02	01040	000707	CIE/3	
03	01041	000060	ACE/3	
04	01042	000560	NSE/3	
05	01043	000604	NDE/3	
06	01044	000477	ASE/3	
07	01045	000511	ADE/3	
08	01046	000531	LSE/3	
09	01047	000543	LDE/3	
10	01050	002176	SHE/3	
11	01051	002203	SLE/3	
12	01052	002210	SEE/3	
13	01053	002215	SNE/3	
14	01054	002222	SOE/3	
15	01055	002230	SZE/3	
16	01056	002235	SXE/3	
17	01057	005230	GPE/3	
18	01060	001266	FAFPE/3	
19	01061	001341	FSFPE/3	
20	01062	001064	FMFPE/3	
21	01063	002305	F63E/3	
22	01064	001211	FDPE/3	
23	01065	000755	CFE/3	
24	01066	000427	DLE/3	
25	01067	000330	DSE/3	
26	01070	000441	AAE/3	
27	01071	000460	SSE/3	
28	01072	002445	DPE/3	
29	01073	003006	MHE/3	
30	01074	002314	LKE/3	
31	01075	003042	IXE/3	
32	01076	002307	F76E/3	
33	01077	002311	F77E/3	

10020 .MAIN

01  
02 00003 .00 3  
03 01100 002303 F0E/3  
04 01101 002272 D0E/3  
05 01102 000246 ELE/3  
06 01103 000225 HLE/3  
07 01104 000071 LAE/3  
08 01105 000076 LOE/3  
09 01106 000103 LXE/3  
10 01107 000110 WAE/3  
11 01110 000117 WSE/3  
12 01111 000051 AME/3  
13 01112 000143 WMFPE/3  
14 01113 000055 ALE/3  
15 01114 002470 XIE/3  
16 01115 000627 JLE/3  
17 01116 000655 JDLE/3  
18 01117 000700 JELE/3  
19 01120 000302 XLE/3  
20 01121 000270 ESE/3  
21 01122 000256 EAE/3  
22 01123 000236 ZLE/3  
23 01124 000064 RLE/3  
24 01125 002243 SPE/3  
25 01126 001347 REE/3  
26 01127 000314 RSE/3  
27 01130 000163 WDE/3  
28 01131 000410 RXE/3  
29 01132 000356 HSE/3  
30 01133 000374 XSE/3  
31 01134 005223 GGE/3  
32 01135 002255 DIE/3  
33 01136 002401 APE/3  
34 01137 002365 ULE/3

; h=FIELD<>0

10021 -MAIN

01			
02	01140	000707	CIE/3
03	01141	000060	ACE/3
04	01142	000560	NSE/3
05	01143	000604	NDE/3
06	01144	000477	ASE/3
07	01145	000511	ADE/3
08	01146	000531	LSE/3
09	01147	000543	LDE/3
10	01150	002176	SHE/3
11	01151	002203	SLE/3
12	01152	002210	SEE/3
13	01153	002215	SNE/3
14	01154	002222	SOE/3
15	01155	002230	ZE/3
16	01156	002235	SXE/3
17	01157	005230	GPE/3
18	01160	001266	FAFPE/3
19	01161	001341	FSFPE/3
20	01162	001064	FMFPE/3
21	01163	002305	F6JE/3
22	01164	001211	FDFPE/3
23	01165	000755	CFE/3
24	01166	000427	DLE/3
25	01167	000330	DSE/3
26	01170	000441	AAE/3
27	01171	000460	SSE/3
28	01172	002445	DPE/3
29	01173	003006	MHE/3
30	01174	002314	LKE/3
31	01175	003042	IXE/3
32	01176	002307	F76E/3
33	01177	002311	F77E/3

10022 -MAIN  
U1  
02            •ENDC  
03 01200 002303 FUGE/3  
04 01201 002272 DOE/3  
05 01202 000246 ELE/3  
06 01203 000225 HLE/3  
07 01204 000071 LAE/3  
08 01205 000076 LOE/3  
09 01206 000103 LXE/3  
10 01207 000110 WAE/3  
11 01210 000117 MSE/3  
12 01211 000051 AME/3  
13 01212 000143 WMPPE/3  
14 01213 000055 ALE/3  
15 01214 002470 XIE/3  
16 01215 000627 JLE/3  
17 01216 000655 JDLE/3  
18 01217 000700 JELE/3  
19 01220 000302 XLE/3  
20 01221 000270 ESE/3  
21 01222 000256 EAE/3  
22 01223 000236 ZLE/3  
23 01224 000064 RLE/3  
24 01225 002243 SPE/3  
25 01226 001347 REE/3  
26 01227 000314 RSE/3  
27 01230 000163 WDE/3  
28 01231 000410 RXE/3  
29 01232 000356 HSE/3  
30 01233 000374 XSE/3  
31 01234 005223 GGE/3  
32 01235 002255 DIE/3  
33 01236 002401 APE/3  
34 01237 002365 ULE/3



10023 .MAIN

01 02 01240 000707 CIE/3  
03 01241 000060 ACE/3  
04 01242 000560 NSE/3  
05 01243 000604 NDE/3  
06 01244 000477 ASE/3  
07 01245 000511 ADE/3  
08 01246 000531 LSE/3  
09 01247 000543 LDE/3  
10 01250 002176 SHE/3  
11 01251 002203 SLE/3  
12 01252 002210 SEE/3  
13 01253 002215 SNE/3  
14 01254 002222 SOE/3  
15 01255 002230 SZE/3  
16 01256 002235 SXE/3  
17 01257 005230 GPE/3  
18 01260 001266 FAFPE/3  
19 01261 001341 FSFPE/3  
20 01262 001064 FMFPE/3  
21 01263 002305 F63E/3  
22 01264 001211 FDPPE/3  
23 01265 000755 CFE/3  
24 01266 000427 DLE/3  
25 01267 000330 DSE/3  
26 01270 000441 AAE/3  
27 01271 000460 SSE/3  
28 01272 002445 DPE/3  
29 01273 003006 MHE/3  
30 01274 002314 LKE/3  
31 01275 003042 IXE/3  
32 01276 002307 F76E/3  
33 01277 002311 F77E/3

10024 .MAIN

01			
02		•ENDC	
03	01300	002303	FOOE/3
04	01301	002272	DOE/3
05	01302	000246	ELE/3
06	01303	000225	HLE/3
07	01304	000071	LAE/3
08	01305	000076	LOE/3
09	01306	000103	LXE/3
10	01307	000110	MAE/3
11	01310	000117	MSE/3
12	01311	000051	AME/3
13	01312	000143	WMFPE/3
14	01313	000055	ALE/3
15	01314	002470	XIE/3
16	01315	000627	JLLE/3
17	01316	000655	JDLE/3
18	01317	000700	JELE/3
19	01320	000302	XLE/3
20	01321	000270	ESE/3
21	01322	000256	EAE/3
22	01323	000236	ZLE/3
23	01324	000064	RLE/3
24	01325	002243	SPE/3
25	01326	001347	KEE/3
26	01327	000314	RSE/3
27	01330	000163	WDE/3
28	01331	000410	RXE/3
29	01332	000356	HSE/3
30	01333	000374	XSE/3
31	01334	005223	GGE/3
32	01335	002255	DIE/3
33	01336	002401	APE/3
34	01337	002365	ULE/3

10025 - MAIN

01			
02	01340	000707	CIE/3
03	01341	000060	ACE/3
04	01342	000560	NSE/3
05	01343	000604	NDE/3
06	01344	000477	ASE/3
07	01345	000511	ADE/3
08	01346	000531	LSE/3
09	01347	000543	LDE/3
10	01350	002176	SHE/3
11	01351	002203	SLE/3
12	01352	002210	SEE/3
13	01353	002215	SNE/3
14	01354	002222	SOE/3
15	01355	002230	SZE/3
16	01356	002235	SXE/3
17	01357	005230	GPE/3
18	01360	001266	FAFPE/3
19	01361	001341	FSFPE/3
20	01362	001064	FMFPE/3
21	01363	002305	F63E/3
22	01364	001211	FDFPE/3
23	01365	000755	CFE/3
24	01366	000427	DLE/3
25	01367	000330	DSE/3
26	01370	000441	AAE/3
27	01371	000460	SSE/3
28	01372	002445	DPE/3
29	01373	003006	MHE/3
30	01374	002314	LKE/3
31	01375	003042	IXE/3
32	01376	002307	F76E/3
33	01377	002311	F77E/3

10026 -MAIN

01  
02  
03 01400 002303  
04 01401 002272  
05 01402 000246  
06 01403 000225  
07 01404 000071  
08 01405 000076  
09 01406 000103  
10 01407 000110  
11 01410 000117  
12 01411 000051  
13 01412 000126  
14 01413 000055  
15 01414 002470  
16 01415 000622  
17 01416 000655  
18 01417 000672  
19 01420 000302  
20 01421 000270  
21 01422 000256  
22 01423 000236  
23 01424 000064  
24 01425 002243  
25 01426 001347  
26 01427 000314  
27 01430 000163  
28 01431 000410  
29 01432 000356  
30 01433 000374  
31 01434 005223  
32 01435 002255  
33 01436 002401  
34 01437 002365

•ENDC

FUUE/3  
DOE/3  
ELE/3  
HLE/3  
LAE/3  
LOE/3  
LXE/3  
WAE/3  
WSE/3  
AME/3  
WME/3  
ALE/3  
XIE/3  
JLE/3  
JDE/3  
JEE/3  
XLE/3  
ESE/3  
EAE/3  
ZLE/3  
RLE/3  
SPE/3  
REE/3  
RSE/3  
WDE/3  
RXE/3  
HSE/3  
XSE/3  
GGE/3  
DIE/3  
APE/3  
ULE/3

;FPU NOT INSTALLED

10027 .MAIN

01  
02 01440 000707 GIE/3  
03 01441 000060 ACE/3  
04 01442 000560 NSE/3  
05 01443 000604 NDE/3  
06 01444 000477 ASE/3  
07 01445 000511 ADE/3  
08 01446 000531 LSE/3  
09 01447 000543 LDE/3  
10 01450 002176 SHE/3  
11 01451 002203 SLE/3  
12 01452 002210 SEE/3  
13 01453 002215 SNE/3  
14 01454 002222 SOE/3  
15 01455 002230 SZE/3  
16 01456 002235 SXE/3  
17 01457 005230 GPE/3  
18 01460 001217 FAE/3  
19 01461 001274 FSE/3  
20 01462 001006 FME/3  
21 01463 002305 F6JE/3  
22 01464 001133 FDE/3  
23 01465 000755 CFE/3  
24 01466 000427 DLE/3  
25 01467 000330 DSE/3  
26 01470 000441 AAE/3  
27 01471 000460 SSE/3  
28 01472 002445 DPE/3  
29 01473 003006 MHE/3  
30 01474 002314 LKE/3  
31 01475 003042 IXE/3  
32 01476 002307 F76E/3  
33 01477 002311 F77E/3

10028 .MAIN

01  
02 000003 .D0 3  
03 01500 002303 FCCE/3  
04 01501 002272 DOE/3  
05 01502 000246 ELE/3  
06 01503 000225 HLE/3  
07 01504 000071 LAE/3  
08 01505 000076 LOE/3  
09 01506 000103 LXE/3  
10 01507 000110 WAE/3  
11 01510 000117 WSE/3  
12 01511 000051 AME/3  
13 01512 000126 WME/3  
14 01513 000055 ALE/3  
15 01514 002470 XIE/3  
16 01515 000627 JLE/3  
17 01516 000655 JDLE/3  
18 01517 000700 JELE/3  
19 01520 000302 XLE/3  
20 01521 000270 ESE/3  
21 01522 000256 EAE/3  
22 01523 000236 ZLE/3  
23 01524 000064 RLE/3  
24 01525 002243 SPE/3  
25 01526 001347 REE/3  
26 01527 000314 RSE/3  
27 01530 000163 WDE/3  
28 01531 000410 RXE/3  
29 01532 000356 HSE/3  
30 01533 000374 XSE/3  
31 01534 005223 GGE/3  
32 01535 002255 DIE/3  
33 01536 002401 APE/3  
34 01537 002365 ULE/3

; N=FIELD<>0

10029 -MAIN

01  
02 01540 000707 CIE/3  
03 01541 000060 ACE/3  
04 01542 000560 NSE/3  
05 01543 000604 NDE/3  
06 01544 000477 ASE/3  
07 01545 000511 ADE/3  
08 01546 000531 LSE/3  
09 01547 000543 LDE/3  
10 01550 002176 SHE/3  
11 01551 002203 SLE/3  
12 01552 002210 SEE/3  
13 01553 002215 SNE/3  
14 01554 002222 SOE/3  
15 01555 002230 SZE/3  
16 01556 002235 SXE/3  
17 01557 005230 GPE/3  
18 01560 001217 FAE/3  
19 01561 001274 FSE/3  
20 01562 001006 FME/3  
21 01563 002305 F63E/3  
22 01564 001133 FDE/3  
23 01565 000755 CFE/3  
24 01566 000427 DLE/3  
25 01567 000330 DSE/3  
26 01570 000441 AAE/3  
27 01571 000460 SSE/3  
28 01572 002445 DPE/3  
29 01573 003006 MHE/3  
30 01574 002314 LKE/3  
31 01575 003042 IXE/3  
32 01576 002307 F76E/3  
33 01577 002311 F77E/3

10030 .MAIN  
01  
02  
03 01600 002303 F00E/3  
04 01601 002272 D0E/3  
05 01602 000246 ELE/3  
06 01603 000225 HLE/3  
07 01604 000071 LAE/3  
08 01605 000076 LOE/3  
09 01606 000103 LXE/3  
10 01607 000110 WAE/3  
11 01610 000117 WSE/3  
12 01611 000051 AME/3  
13 01612 000126 WME/3  
14 01613 000055 ALE/3  
15 01614 002470 XIE/3  
16 01615 000627 JLLE/3  
17 01616 000655 JDLE/3  
18 01617 000700 JELE/3  
19 01620 000302 XLE/3  
20 01621 000270 ESE/3  
21 01622 000256 EAE/3  
22 01623 000236 ZLE/3  
23 01624 000064 KLE/3  
24 01625 002243 SPE/3  
25 01626 001347 REE/3  
26 01627 000314 RSE/3  
27 01630 000163 WDE/3  
28 01631 000410 HXE/3  
29 01632 000356 HSE/3  
30 01633 000374 XSE/3  
31 01634 005223 GGE/3  
32 01635 002255 DIE/3  
33 01636 002401 APE/3  
34 01637 002365 ULE/3

•ENDC



10031 .MAIN

01  
02 01640 000707 CIE/3  
03 01641 000060 ACE/3  
04 01642 000560 NSE/3  
05 01643 000604 NDE/3  
06 01644 000477 ASE/3  
07 01645 000511 ADE/3  
08 01646 000531 LSE/3  
09 01647 000543 LDE/3  
10 01650 002176 SHE/3  
11 01651 002203 SLE/3  
12 01652 002210 SEE/3  
13 01653 002215 SNE/3  
14 01654 002222 SOE/3  
15 01655 002230 SZE/3  
16 01656 002235 SXE/3  
17 01657 005230 GPE/3  
18 01660 001217 FAE/3  
19 01661 001274 FSE/3  
20 01662 001006 FME/3  
21 01663 002305 F63E/3  
22 01664 001133 FDE/3  
23 01665 000755 CFE/3  
24 01666 000427 DLE/3  
25 01667 000330 DSE/3  
26 01670 000441 AAE/3  
27 01671 000460 SSE/3  
28 01672 002445 DPE/3  
29 01673 003006 MHE/3  
30 01674 002314 LKE/3  
31 01675 003042 IXE/3  
32 01676 002307 F76E/3  
33 01677 002311 F77E/3

10032 .MAIN

01  
 02  
 03 01700 002303 F00E/3  
 04 01701 002272 D0E/3  
 05 01702 000246 ELE/3  
 06 01703 000225 HLE/3  
 07 01704 000071 LAE/3  
 08 01705 000076 LOE/3  
 09 01706 000103 LXE/3  
 10 01707 000110 WAE/3  
 11 01710 000117 WSE/3  
 12 01711 000051 AME/3  
 13 01712 000126 WME/3  
 14 01713 000055 ALE/3  
 15 01714 002470 XIE/3  
 16 01715 000627 JLE/3  
 17 01716 000655 JDLE/3  
 18 01717 000700 JELE/3  
 19 01720 000302 XLE/3  
 20 01721 000270 ESE/3  
 21 01722 000256 EAE/3  
 22 01723 000236 ZLE/3  
 23 01724 000064 RLE/3  
 24 01725 002243 SPE/3  
 25 01726 001347 HEE/3  
 26 01727 000314 RSE/3  
 27 01730 000163 WDE/3  
 28 01731 000410 RXE/3  
 29 01732 000356 HSE/3  
 30 01733 000374 XSE/3  
 31 01734 005223 GGE/3  
 32 01735 002255 DIE/3  
 33 01736 002401 APE/3  
 34 01737 002365 ULE/3

•ENDC

10033 .MAIN

01  
02 01740 000707 CIE/3  
03 01741 000060 ACE/3  
04 01742 000560 NSE/3  
05 01743 000604 NDE/3  
06 01744 000477 ASE/3  
07 01745 000511 ADE/3  
08 01746 000531 LSE/3  
09 01747 000543 LDE/3  
10 01750 002176 SHE/3  
11 01751 002203 SLE/3  
12 01752 002210 SEE/3  
13 01753 002215 SNE/3  
14 01754 002222 SOE/3  
15 01755 002230 SZE/3  
16 01756 002235 SXE/3  
17 01757 005230 GPE/3  
18 01760 001217 FAE/3  
19 01761 001274 FSE/3  
20 01762 001006 FME/3  
21 01763 002305 F63E/3  
22 01764 001135 FDE/3  
23 01765 000755 CFE/3  
24 01766 000427 DLE/3  
25 01767 000330 DSE/3  
26 01770 000441 AAE/3  
27 01771 000460 SSE/3  
28 01772 002445 DPE/3  
29 01773 003006 MHE/3  
30 01774 002314 LKE/3  
31 01775 003042 IXE/3  
32 01776 002307 F76E/3  
33 01777 002311 F77E/3

10034 .MAIN

01

02

03

.ENDC

.END

000C SOURCE LINES IN ERROR

0001 .MAIN DOMUS MACRO ASSEMBLER REV 02.00

```
01  
02  
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04  
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13  
; ADDRESS CALCULATION DECODING MAP  
;  
; 0: PREFETCH ERROR  
; 1: ZERO  
; 2: ESCAPE MODE  
; 3: AFTER ESCAPE  
; 4: AFTER AM  
; 5: INSTR BIT (8)  
; 6: (9)  
; 7: (10)  
; 8: (11)
```

10002 .MAIN  
01 U0000 .LOC 0  
02  
03 000002 .DO 2  
04 C0000 002013 DIR/3  
05 C0001 002015 INDEX/3  
06 C0002 002015 INDEX/3  
07 C0003 002015 INDEX/3  
08 C0004 002017 DIRI/3  
09 C0005 002040 INDXI/3  
10 C0006 002040 INDXI/3  
11 C0007 002040 INDXI/3  
12  
13 C0010 002014 REL/3  
14 C0011 002016 RELX/3  
15 C0012 002016 RELX/3  
16 C0013 002016 RELX/3  
17 C0014 002032 RELI/3  
18 C0015 002046 RELXI/3  
19 C0016 002046 RELXI/3  
20 C0017 002046 RELXI/3  
21  
22 C0020 002112 AMD/3  
23 C0021 002117 AMX/3  
24 C0022 002117 AMX/3  
25 C0023 002117 AMX/3  
26 C0024 002127 AMDI/3  
27 C0025 002142 AMXI/3  
28 C0026 002142 AMXI/3  
29 C0027 002142 AMXI/3  
30  
31 C0030 002110 AMR/3  
32 C0031 002114 AMRX/3  
33 C0032 002114 AMRX/3  
34 C0033 002114 AMRX/3  
35 C0034 002122 AMRI/3  
36 C0035 002134 AMRXI/3  
37 C0036 002134 AMRXI/3  
38 C0037 002134 AMRXI/3

10003 .MAIN

01 000020 .DO 16.  
02 CC040 002003 AENES/3  
03 CC041 002003 AENES/3  
04 CC042 002003 AENES/3  
05 CC043 002003 AENES/3  
06 CC044 002003 AENES/3  
07 CC045 002003 AENES/3  
08 CC046 002003 AENES/3  
09 CC047 002003 AENES/3  
10 CC050 002003 AENES/3  
11 CC051 002003 AENES/3  
12 CC052 002003 AENES/3  
13 CC053 002003 AENES/3  
14 CC054 002003 AENES/3  
15 CC055 002003 AENES/3  
16 CC056 002003 AENES/3  
17 CC057 002003 AENES/3  
18 000020 .DO 16.  
19 CC060 002005 INESC/3  
20 CC061 002005 INESC/3  
21 CC062 002005 INESC/3  
22 CC063 002005 INESC/3  
23 CC064 002005 INESC/3  
24 CC065 002005 INESC/3  
25 CC066 002005 INESC/3  
26 CC067 002005 INESC/3  
27 CC070 002005 INESC/3  
28 CC071 002005 INESC/3  
29 CC072 002005 INESC/3  
30 CC073 002005 INESC/3  
31 CC074 002005 INESC/3  
32 CC075 002005 INESC/3  
33 CC076 002005 INESC/3  
34 CC077 002005 INESC/3

; AFTER ESCAPE=1 ESCAPE=0

; AFTER ESCAPE=1 ESCAPE=0 AFTER AM=1

10UC4 -MAIN

01  
02 CC100 002013 DIR/3  
03 CC101 002015 INDEX/3  
04 CC102 002015 INDEX/3  
05 CC103 002015 INDEX/3  
06 CC104 002054 DIRIE/3  
07 CC105 002072 IDXIE/3  
08 CC106 002072 IDXIE/3  
09 CC107 002072 IDXIE/3  
10  
11 CC110 002014 REL/3  
12 CC111 002016 RELX/3  
13 CC112 002016 RELX/3  
14 CC113 002016 RELX/3  
15 CC114 002065 RELIE/3  
16 CC115 002101 RLXIE/3  
17 CC116 002101 RLXIE/3  
18 CC117 002101 RLXIE/3  
19  
20 CC120 002112 AMD/3  
21 CC121 002117 AMX/3  
22 CC122 002117 AMX/3  
23 CC123 002117 AMX/3  
24 CC124 002151 AMDIE/3  
25 CC125 002160 AMXIE/3  
26 CC126 002160 AMXIE/3  
27 CC127 002160 AMXIE/3  
28  
29 CC130 002110 AMR/3  
30 CC131 002114 AMRX/3  
31 CC132 002114 AMRX/3  
32 CC133 002114 AMRX/3  
33 CC134 002150 AMRIE/3  
34 CC135 002157 ARXIE/3  
35 CC136 002157 ARXIE/3  
36 CC137 002157 ARXIE/3

;ESCAPE = 1 AFTER ESCAPE =0



10005 .MAIN

01  
02 000020 .D0.16.  
03 00140 002002 AFTES/3  
04 00141 002002 AFTES/3  
05 00142 002002 AFTES/3  
06 00143 002002 AFTES/3  
07 00144 002002 AFTES/3  
08 00145 002002 AFTES/3  
09 00146 002002 AFTES/3  
10 00147 002002 AFTES/3  
11 00150 002002 AFTES/3  
12 00151 002002 AFTES/3  
13 00152 002002 AFTES/3  
14 00153 002002 AFTES/3  
15 00154 002002 AFTES/3  
16 00155 002002 AFTES/3  
17 00156 002002 AFTES/3  
18 00157 002002 AFTES/3  
19 000020 .D0.16.  
20 00160 002005 INESC/3  
21 00161 002005 INESC/3  
22 00162 002005 INESC/3  
23 00163 002005 INESC/3  
24 00164 002005 INESC/3  
25 00165 002005 INESC/3  
26 00166 002005 INESC/3  
27 00167 002005 INESC/3  
28 00170 002005 INESC/3  
29 00171 002005 INESC/3  
30 00172 002005 INESC/3  
31 00173 002005 INESC/3  
32 00174 002005 INESC/3  
33 00175 002005 INESC/3  
34 00176 002005 INESC/3  
35 00177 002005 INESC/3

; AFTER ESCAPE=1 ESCAPE=1

; AFTER ESCAPE=1 ESCAPE=1 AFTER AM=1

10006 •MAIN

01  
02 •ENDC  
03 C0200 002013 DIR/3  
04 C0201 002015 INDEX/3  
05 C0202 002015 INDEX/3  
06 C0203 002015 INDEX/3  
07 C0204 002017 DIRI/3  
08 C0205 002040 INDXI/3  
09 C0206 002040 INDXI/3  
10 C0207 002040 INDXI/3  
11  
12 C0210 002014 REL/3  
13 C0211 002016 RELX/3  
14 C0212 002016 RELX/3  
15 C0213 002016 RELX/3  
16 C0214 002032 RELI/3  
17 C0215 002046 RELXI/3  
18 C0216 002046 RELXI/3  
19 C0217 002046 RELXI/3  
20  
21 C0220 002112 AMD/3  
22 C0221 002117 AMX/3  
23 C0222 002117 AMX/3  
24 C0223 002117 AMX/3  
25 C0224 002127 AMDI/3  
26 C0225 002142 AMXI/3  
27 C0226 002142 AMXI/3  
28 C0227 002142 AMXI/3  
29  
30 C0230 002110 AMR/3  
31 C0231 002114 AMRX/3  
32 C0232 002114 AMRX/3  
33 C0233 002114 AMRX/3  
34 C0234 002122 AMRI/3  
35 C0235 002134 AMRXI/3  
36 C0236 002134 AMRXI/3  
37 C0237 002134 AMRXI/3

10CC7 .MAIN  
01 000020 .DO 16.  
02 00240 002003 AENES/3  
03 00241 002003 AENES/3  
04 00242 002003 AENES/3  
05 00243 002003 AENES/3  
06 00244 002003 AENES/3  
07 00245 002003 AENES/3  
08 00246 002003 AENES/3  
09 00247 002003 AENES/3  
10 00250 002003 AENES/3  
11 00251 002003 AENES/3  
12 00252 002003 AENES/3  
13 00253 002003 AENES/3  
14 00254 002003 AENES/3  
15 00255 002003 AENES/3  
16 00256 002003 AENES/3  
17 00257 002003 AENES/3  
18 000020 .DO 16.  
19 00260 002005 INESC/3  
20 00261 002005 INESC/3  
21 00262 002005 INESC/3  
22 00263 002005 INESC/3  
23 00264 002005 INESC/3  
24 00265 002005 INESC/3  
25 00266 002005 INESC/3  
26 00267 002005 INESC/3  
27 00270 002005 INESC/3  
28 00271 002005 INESC/3  
29 00272 002005 INESC/3  
30 00273 002005 INESC/3  
31 00274 002005 INESC/3  
32 00275 002005 INESC/3  
33 00276 002005 INESC/3  
34 00277 002005 INESC/3

; AFTER ESCAPE=1 ESCAPE=0

; AFTER ESCAPE=1 ESCAPE=0 AFTER AM=1

10008 .MAIN

;ESCAPE = 1 AFTER ESCAPE = 0

01  
02 CC300 002013 DIR/3  
03 CC301 002015 INDEX/3  
04 CC302 002015 INDEX/3  
05 CC303 002015 INDEX/3  
06 CC304 002054 DIRIE/3  
07 CC305 002072 IDXIE/3  
08 CC306 002072 IDXIE/3  
09 CC307 002072 IDXIE/3  
10  
11 C0310 002014 REL/3  
12 C0311 002016 RELX/3  
13 C0312 002016 RELX/3  
14 C0313 002016 RELX/3  
15 C0314 002063 RELIE/3  
16 C0315 002101 RLXIE/3  
17 C0316 002101 RLXIE/3  
18 C0317 002101 RLXIE/3  
19  
20 C0320 002112 AMD/3  
21 C0321 002117 AMX/3  
22 C0322 002117 AMX/3  
23 C0323 002117 AMX/3  
24 C0324 002151 AMDIE/3  
25 C0325 002160 AMXIE/3  
26 C0326 002160 AMXIE/3  
27 C0327 002160 AMXIE/3  
28  
29 C0330 002110 AMR/3  
30 C0331 002114 AMRX/3  
31 C0332 002114 AMRX/3  
32 C0333 002114 AMRX/3  
33 C0334 002150 AMRIE/3  
34 C0335 002157 ARXIE/3  
35 C0336 002157 ARXIE/3  
36 C0337 002157 ARXIE/3

10009 -MAIN

01 000020 -D0 '16.  
02 00340 002002 AFTES/3  
03 00341 002002 AFTES/3  
04 00342 002002 AFTES/3  
05 00343 002002 AFTES/3  
06 00344 002002 AFTES/3  
07 00345 002002 AFTES/3  
08 00346 002002 AFTES/3  
09 00347 002002 AFTES/3  
10 00350 002002 AFTES/3  
11 00351 002002 AFTES/3  
12 00352 002002 AFTES/3  
13 00353 002002 AFTES/3  
14 00354 002002 AFTES/3  
15 00355 002002 AFTES/3  
16 00356 002002 AFTES/3  
17 00357 002002 AFTES/3  
18 000020 -D0 '16.  
19 00360 002005 INESC/3  
20 00361 002005 INESC/3  
21 00362 002005 INESC/3  
22 00363 002005 INESC/3  
23 00364 002005 INESC/3  
24 00365 002005 INESC/3  
25 00366 002005 INESC/3  
26 00367 002005 INESC/3  
27 00370 002005 INESC/3  
28 00371 002005 INESC/3  
29 00372 002005 INESC/3  
30 00373 002005 INESC/3  
31 00374 002005 INESC/3  
32 00375 002005 INESC/3  
33 00376 002005 INESC/3  
34 00377 002005 INESC/3  
35

; AFTER ESCAPE=1 ESCAPE=1

; AFTER ESCAPE=1 ESCAPE=1 AFTER AN=1

10010 .MAIN

01  
02  
03  
04  
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.ENDC

.DO 8.

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; PREFETCH ERROR

10011 .MAIN

01  
02  
03                   •ENDC  
04                   •DO 32.  
05    000040        PFINT/3  
06    000036        PFINT/3  
07    000036        PFINT/3  
08    000036        PFINT/3  
09    000036        PFINT/3  
10    000036        PFINT/3  
11    000036        PFINT/3  
12    000036        PFINT/3  
13    000036        PFINT/3  
14    000036        PFINT/3  
15    000036        PFINT/3  
16    000036        PFINT/3  
17    000036        PFINT/3  
18    000036        PFINT/3  
19    000036        PFINT/3  
20    000036        PFINT/3  
21    000036        PFINT/3  
22    000036        PFINT/3  
23    000036        PFINT/3  
24    000036        PFINT/3  
25    000036        PFINT/3  
26    000036        PFINT/3  
27    000036        PFINT/3  
28    000036        PFINT/3  
29    000036        PFINT/3  
30    000036        PFINT/3  
31    000036        PFINT/3  
32    000036        PFINT/3  
33    000036        PFINT/3  
34    000036        PFINT/3  
35    000036        PFINT/3

10012 .MAIN

01  
02  
03            - ENDC  
04    000040    .00 32.  
05    000036    PFINT/3  
06    000036    PFINT/3  
07    000036    PFINT/3  
08    000036    PFINT/3  
09    000036    PFINT/3  
10    000036    PFINT/3  
11    000036    PFINT/3  
12    000036    PFINT/3  
13    000036    PFINT/3  
14    000036    PFINT/3  
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18    000036    PFINT/3  
19    000036    PFINT/3  
20    000036    PFINT/3  
21    000036    PFINT/3  
22    000036    PFINT/3  
23    000036    PFINT/3  
24    000036    PFINT/3  
25    000036    PFINT/3  
26    000036    PFINT/3  
27    000036    PFINT/3  
28    000036    PFINT/3  
29    000036    PFINT/3  
30    000036    PFINT/3  
31    000036    PFINT/3  
32    000036    PFINT/3  
33    000036    PFINT/3  
34    000036    PFINT/3  
35    000036    PFINT/3



10013 .MAIN

01  
02  
03           000040           .ENDC  
04    00540 000036    PFINT/3    .DO 32.  
05    00541 000036    PFINT/3  
06    00542 000036    PFINT/3  
07    00543 000036    PFINT/3  
08    00544 000036    PFINT/3  
09    00545 000036    PFINT/3  
10    00546 000036    PFINT/3  
11    00547 000036    PFINT/3  
12    00550 000036    PFINT/3  
13    00551 000036    PFINT/3  
14    00552 000036    PFINT/3  
15    00553 000036    PFINT/3  
16    00554 000036    PFINT/3  
17    00555 000036    PFINT/3  
18    00556 000036    PFINT/3  
19    00557 000036    PFINT/3  
20    00560 000036    PFINT/3  
21    00561 000036    PFINT/3  
22    00562 000036    PFINT/3  
23    00563 000036    PFINT/3  
24    00564 000036    PFINT/3  
25    00565 000036    PFINT/3  
26    00566 000036    PFINT/3  
27    00567 000036    PFINT/3  
28    00570 000036    PFINT/3  
29    00571 000036    PFINT/3  
30    00572 000036    PFINT/3  
31    00573 000036    PFINT/3  
32    00574 000036    PFINT/3  
33    00575 000036    PFINT/3  
34    00576 000036    PFINT/3  
35    00577 000036    PFINT/3

10014 .MAIN

01  
02  
03 .ENDC  
04 000040 .DO 32.  
05 CC600 000036 PFINT/3  
06 CC601 000036 PFINT/3  
07 CC602 000036 PFINT/3  
08 CC603 000036 PFINT/3  
09 CC604 000036 PFINT/3  
10 CC605 000036 PFINT/3  
11 CC606 000036 PFINT/3  
12 CC607 000036 PFINT/3  
13 CC610 000036 PFINT/3  
14 CC611 000036 PFINT/3  
15 CC612 000036 PFINT/3  
16 CC613 000036 PFINT/3  
17 CC614 000036 PFINT/3  
18 CC615 000036 PFINT/3  
19 CC616 000036 PFINT/3  
20 CC617 000036 PFINT/3  
21 CC620 000036 PFINT/3  
22 CC621 000036 PFINT/3  
23 CC622 000036 PFINT/3  
24 CC623 000036 PFINT/3  
25 CC624 000036 PFINT/3  
26 CC625 000036 PFINT/3  
27 CC626 000036 PFINT/3  
28 CC627 000036 PFINT/3  
29 CC630 000036 PFINT/3  
30 CC631 000036 PFINT/3  
31 CC632 000036 PFINT/3  
32 CC633 000036 PFINT/3  
33 CC634 000036 PFINT/3  
34 CC635 000036 PFINT/3  
35 CC636 000036 PFINT/3  
36 CC637 000036 PFINT/3

10015 .MAIN

01

02 .ENDC

03 000040 .D0 32.

04 00640 000036 PFINT/3

05 00641 000036 PFINT/3

06 00642 000036 PFINT/3

07 00643 000036 PFINT/3

08 00644 000036 PFINT/3

09 00645 000036 PFINT/3

10 00646 000036 PFINT/3

11 00647 000036 PFINT/3

12 00650 000036 PFINT/3

13 00651 000036 PFINT/3

14 00652 000036 PFINT/3

15 00653 000036 PFINT/3

16 00654 000036 PFINT/3

17 00655 000036 PFINT/3

18 00656 000036 PFINT/3

19 00657 000036 PFINT/3

20 00660 000036 PFINT/3

21 00661 000036 PFINT/3

22 00662 000036 PFINT/3

23 00663 000036 PFINT/3

24 00664 000036 PFINT/3

25 00665 000036 PFINT/3

26 00666 000036 PFINT/3

27 00667 000036 PFINT/3

28 00670 000036 PFINT/3

29 00671 000036 PFINT/3

30 00672 000036 PFINT/3

31 00673 000036 PFINT/3

32 00674 000036 PFINT/3

33 00675 000036 PFINT/3

34 00676 000036 PFINT/3

35 00677 000036 PFINT/3

10016 .MAIN

01  
02  
03 .ENDC  
04 000040 .DO 32.  
05 000036 PFINT/3  
06 000036 PFINT/3  
07 000036 PFINT/3  
08 000036 PFINT/3  
09 000036 PFINT/3  
10 000036 PFINT/3  
11 000036 PFINT/3  
12 000036 PFINT/3  
13 000036 PFINT/3  
14 000036 PFINT/3  
15 000036 PFINT/3  
16 000036 PFINT/3  
17 000036 PFINT/3  
18 000036 PFINT/3  
19 000036 PFINT/3  
20 000036 PFINT/3  
21 000036 PFINT/3  
22 000036 PFINT/3  
23 000036 PFINT/3  
24 000036 PFINT/3  
25 000036 PFINT/3  
26 000036 PFINT/3  
27 000036 PFINT/3  
28 000036 PFINT/3  
29 000036 PFINT/3  
30 000036 PFINT/3  
31 000036 PFINT/3  
32 000036 PFINT/3  
33 000036 PFINT/3  
34 000036 PFINT/3  
35 000036 PFINT/3

10017 .MAIN

01  
02  
03            • ENDC  
04            • DO 32.  
05    000040    000036    PFINT/3  
06    000036    000036    PFINT/3  
07    000036    000036    PFINT/3  
08    000036    000036    PFINT/3  
09    000036    000036    PFINT/3  
10    000036    000036    PFINT/3  
11    000036    000036    PFINT/3  
12    000036    000036    PFINT/3  
13    000036    000036    PFINT/3  
14    000036    000036    PFINT/3  
15    000036    000036    PFINT/3  
16    000036    000036    PFINT/3  
17    000036    000036    PFINT/3  
18    000036    000036    PFINT/3  
19    000036    000036    PFINT/3  
20    000036    000036    PFINT/3  
21    000036    000036    PFINT/3  
22    000036    000036    PFINT/3  
23    000036    000036    PFINT/3  
24    000036    000036    PFINT/3  
25    000036    000036    PFINT/3  
26    000036    000036    PFINT/3  
27    000036    000036    PFINT/3  
28    000036    000036    PFINT/3  
29    000036    000036    PFINT/3  
30    000036    000036    PFINT/3  
31    000036    000036    PFINT/3  
32    000036    000036    PFINT/3  
33    000036    000036    PFINT/3  
34    000036    000036    PFINT/3  
35    000036    000036    PFINT/3

10018 .MAIN

01

02

03

.ENDC

.END

000C SOURCE LINES IN ERROR

00C1 .MAIN DOMUS MACRO ASSEMBLER REV 02.00  
;CPU823 PREFETCH AND OPERANDFETCH DECODING MAP

; ADDRESS

; 0: PREFETCH ERROR  
; 1: 0=UNCONDITIONAL PREFETCH  
; 2: (ESCAPE)  
; 3: INSTRUCTION C  
; 4: .1  
; 5: .2  
; 6: .3  
; 7: .4  
; 8: .5

; OUTPUT

; U: NOT USED  
; 1: ENABLE PREFETCH  
; 2: ENABLE OPERANDFETCH  
; 3: JUMP

000004 PREFETCH=1B13.  
000002 READOP=1B14.  
000001 JUMP=1B15.

01  
02  
03  
04  
05  
06  
07  
08  
09  
10  
11  
12  
13  
14  
15  
16  
17  
18  
19  
20  
21  
22  
23  
24  
25

10002 .MAIN

01  
02 00002 .D0 2  
03 00000 000004 PREFETCH  
04 00001 000004 PREFETCH  
05 00002 000006 PREFETCH+READOP  
06 00003 000006 PREFETCH+READOP  
07 00004 000006 PREFETCH+READOP  
08 00005 000006 PREFETCH+READOP  
09 00006 000006 PREFETCH+READOP  
10 00007 000006 PREFETCH+READOP  
11 00010 000006 PREFETCH+READOP  
12 00011 000004 PREFETCH  
13 00012 000006 PREFETCH+READOP  
14 00013 000004 PREFETCH  
15 00014 000004 PREFETCH  
16 00015 000007 PREFETCH+READOP+JUMP  
17 00016 000007 PREFETCH+READOP+JUMP  
18 00017 000007 PREFETCH+READOP+JUMP  
19 00020 000006 PREFETCH+READOP  
20 00021 000006 PREFETCH+READOP  
21 00022 000006 PREFETCH+READOP  
22 00023 000006 PREFETCH+READOP  
23 00024 000006 PREFETCH+READOP  
24 00025 000004 PREFETCH  
25 00026 000006 PREFETCH+READOP  
26 00027 000004 PREFETCH  
27 00030 000006 PREFETCH+READOP  
28 00031 000006 PREFETCH+READOP  
29 00032 000006 PREFETCH+READOP  
30 00033 000006 PREFETCH+READOP  
31 00034 000004 PREFETCH  
32 00035 000004 PREFETCH  
33 00036 000004 PREFETCH  
34 00037 000006 PREFETCH+READOP

; UNCONDITIONAL PREFETCH  
; UNASSIGNED  
; D0  
; EL  
; HL  
; LA  
; LO  
; LX  
; WA  
; WS  
; AM  
; WM  
; AL  
; XI  
; JL  
; JD  
; JE  
; XL  
; ES  
; EA  
; ZL  
; RL  
; SP  
; RE  
; RS  
; WD  
; RX  
; HS  
; XS  
; GG  
; DI  
; AP  
; UL



10003 .MAIN

01  
02 CC040 000004 PREFETCH  
03 CC041 000004 PREFETCH  
04 CC042 000006 PREFETCH+READOP  
05 CC043 000006 PREFETCH+READOP  
06 CC044 000004 PREFETCH  
07 CC045 000004 PREFETCH  
08 CC046 000004 PREFETCH  
09 CC047 000004 PREFETCH  
10 CC050 000004 PREFETCH  
11 CC051 000004 PREFETCH  
12 CC052 000004 PREFETCH  
13 CC053 000004 PREFETCH  
14 CC054 000004 PREFETCH  
15 CC055 000004 PREFETCH  
16 CC056 000004 PREFETCH  
17 CC057 000004 PREFETCH  
18 CC060 000006 PREFETCH+READOP  
19 CC061 000006 PREFETCH+READOP  
20 CC062 000006 PREFETCH+READOP  
21 CC063 000004 PREFETCH  
22 CC064 000006 PREFETCH+READOP  
23 CC065 000004 PREFETCH  
24 CC066 000006 PREFETCH+READOP  
25 CC067 000004 PREFETCH  
26 CC070 000006 PREFETCH+READOP  
27 CC071 000006 PREFETCH+READOP  
28 CC072 000004 PREFETCH  
29 CC073 000004 PREFETCH  
30 CC074 000004 PREFETCH  
31 CC075 000006 PREFETCH+READOP  
32 000002 .D0 2  
33 CC076 000004 PREFETCH  
34 CC077 000004 PREFETCH

; CI  
; AC  
; NS  
; ND  
; AS  
; AD  
; LS  
; LD  
; SH  
; SL  
; SE  
; SN  
; SO  
; SZ  
; SX  
; GP  
; FA  
; FS  
; FM  
; UNASSIGNED  
; FD  
; CF  
; DL  
; DS  
; AA  
; SS  
; DP  
; MH  
; LK  
; IX  
;  
; UNASSIGNED  
; UNASSIGNED

100C4 .MAIN

01  
02  
03 CC100 000004  
04 CC101 000004 PREFETCH  
05 CC102 000006 PREFETCH+READOP  
06 CC103 000006 PREFETCH+READOP  
07 CC104 000006 PREFETCH+READOP  
08 CC105 000006 PREFETCH+READOP  
09 CC106 000006 PREFETCH+READOP  
10 CC107 000006 PREFETCH+READOP  
11 CC110 000006 PREFETCH+READOP  
12 CC111 000004 PREFETCH  
13 CC112 000006 PREFETCH+READOP  
14 CC113 000004 PREFETCH  
15 CC114 000004 PREFETCH  
16 CC115 000007 PREFETCH+READOP+JUMP  
17 CC116 000007 PREFETCH+READOP+JUMP  
18 CC117 000007 PREFETCH+READOP+JUMP  
19 CC120 000006 PREFETCH+READOP  
20 CC121 000006 PREFETCH+READOP  
21 CC122 000006 PREFETCH+READOP  
22 CC123 000006 PREFETCH+READOP  
23 CC124 000006 PREFETCH+READOP  
24 CC125 000004 PREFETCH  
25 CC126 000006 PREFETCH+READOP  
26 CC127 000004 PREFETCH  
27 CC130 000006 PREFETCH+READOP  
28 CC131 000006 PREFETCH+READOP  
29 CC132 000006 PREFETCH+READOP  
30 CC133 000006 PREFETCH+READOP  
31 CC134 000004 PREFETCH  
32 CC135 000004 PREFETCH  
33 CC136 000004 PREFETCH  
34 CC137 000006 PREFETCH+READOP

•ENDC

? UNASSIGNED  
? DO  
? EL  
? HL  
? LA  
? LO  
? LX  
? WA  
? WS  
? AM  
? WM  
? AL  
? XI  
? JL  
? JD  
? JE  
? XL  
? ES  
? EA  
? ZL  
? RL  
? SP  
? RE  
? RS  
? WD  
? RX  
? HS  
? XS  
? GG  
? DI  
? AP  
? UL

10005 .MAIN

01  
02 C0140 000004 PREFETCH  
03 C0141 000004 PREFETCH  
04 C0142 000006 PREFETCH+READOP  
05 C0143 000006 PREFETCH+READOP  
06 C0144 000004 PREFETCH  
07 C0145 000004 PREFETCH  
08 C0146 000004 PREFETCH  
09 C0147 000004 PREFETCH  
10 C0150 000004 PREFETCH  
11 C0151 000004 PREFETCH  
12 C0152 000004 PREFETCH  
13 C0153 000004 PREFETCH  
14 C0154 000004 PREFETCH  
15 C0155 000004 PREFETCH  
16 C0156 000004 PREFETCH  
17 C0157 000004 PREFETCH  
18 C0160 000006 PREFETCH+READOP  
19 C0161 000006 PREFETCH+READOP  
20 C0162 000006 PREFETCH+READOP  
21 C0163 000004 PREFETCH  
22 C0164 000006 PREFETCH+READOP  
23 C0165 000004 PREFETCH  
24 C0166 000006 PREFETCH+READOP  
25 C0167 000004 PREFETCH  
26 C0170 000006 PREFETCH+READOP  
27 C0171 000006 PREFETCH+READOP  
28 C0172 000004 PREFETCH  
29 C0173 000004 PREFETCH  
30 C0174 000004 PREFETCH  
31 C0175 000006 PREFETCH+READOP  
32 000002 .DO 2  
33 C0176 000004 PREFETCH  
34 C0177 000004 PREFETCH

; CI  
; AC  
; NS  
; ND  
; AS  
; AD  
; LS  
; LD  
; SH  
; SL  
; SE  
; SN  
; SO  
; SZ  
; SX  
; GP  
; FA  
; FS  
; FM  
; UNASSIGNED  
; FD  
; CF  
; DL  
; DS  
; AA  
; SS  
; DP  
; MH  
; LK  
; IX  
; UNASSIGNED  
; UNASSIGNED

100C6 .MAIN

U1  
U2  
U3  
U4 CG200 000000  
U5 CG201 000004 PREFETCH  
U6 CG202 000006 PREFETCH+READOP  
U7 CG203 000006 PREFETCH+READOP  
U8 CG204 000006 PREFETCH+READOP  
U9 CG205 000006 PREFETCH+READOP  
U10 CG206 000006 PREFETCH+READOP  
U11 CG207 000006 PREFETCH+READOP  
U12 CG210 000006 PREFETCH+READOP  
U13 CG211 000004 PREFETCH  
U14 CG212 000006 PREFETCH+READOP  
U15 CG213 000004 PREFETCH  
U16 CG214 000000  
U17 CG215 000003 READOP+JUMP  
U18 CG216 000003 READOP+JUMP  
U19 CG217 000003 READOP+JUMP  
U20 CG220 000006 PREFETCH+READOP  
U21 CG221 000006 PREFETCH+READOP  
U22 CG222 000006 PREFETCH+READOP  
U23 CG223 000006 PREFETCH+READOP  
U24 CG224 000006 PREFETCH+READOP  
U25 CG225 000004 PREFETCH  
U26 CG226 000002 READOP  
U27 CG227 000004 PREFETCH  
U28 CG230 000006 PREFETCH+READOP  
U29 CG231 000006 PREFETCH+READOP  
U30 CG232 000006 PREFETCH+READOP  
U31 CG233 000006 PREFETCH+READOP  
U32 CG234 000004 PREFETCH  
U33 CG235 000004 PREFETCH  
U34 CG236 000004 PREFETCH  
U35 CG237 000006 PREFETCH+READOP

•ENDC

•DO 2

U 000002

; CONDITIONAL PREFETCH  
; UNASSIGNED  
; DO  
; EL  
; HL  
; LA  
; LO  
; LX  
; WA  
; WS  
; AM  
; WM  
; AL  
; XI  
; JL  
; JD  
; JE  
; XL  
; ES  
; EA  
; ZL  
; RL  
; SP  
; RE  
; RS  
; WD  
; RX  
; HS  
; XS  
; GG  
; DI  
; AP  
; UL

10007 .MAIN

01  
02 00240 000004 PREFETCH  
03 00241 000004 PREFETCH  
04 00242 000006 PREFETCH+READOP  
05 00243 000006 PREFETCH+READOP  
06 00244 000004 PREFETCH  
07 00245 000004 PREFETCH  
08 00246 000004 PREFETCH  
09 00247 000004 PREFETCH  
10 00250 000004 PREFETCH  
11 00251 000004 PREFETCH  
12 00252 000004 PREFETCH  
13 00253 000004 PREFETCH  
14 00254 000004 PREFETCH  
15 00255 000004 PREFETCH  
16 00256 000004 PREFETCH  
17 00257 000004 PREFETCH  
18 00260 000006 PREFETCH+READOP  
19 00261 000006 PREFETCH+READOP  
20 00262 000006 PREFETCH+READOP  
21 00263 000000 0  
22 00264 000006 PREFETCH+READOP  
23 00265 000004 PREFETCH  
24 00266 000006 PREFETCH+READOP  
25 00267 000004 PREFETCH  
26 00270 000006 PREFETCH+READOP  
27 00271 000006 PREFETCH+READOP  
28 00272 000004 PREFETCH  
29 00273 000004 PREFETCH  
30 00274 000000 0  
31 00275 000002 READOP  
32 000002 .DO 2  
33 00276 000000 0  
34 00277 000000 0

; CI  
; AC  
; NS  
; ND  
; AS  
; AD  
; LS  
; LD  
; SH  
; SL  
; SE  
; SN  
; SU  
; SZ  
; SX  
; GP  
; FA  
; FS  
; FM  
; UNASSIGNED  
; FD  
; CF  
; DL  
; DS  
; AA  
; SS  
; DP  
; MH  
; LK  
; IX  
; UNASSIGNED  
; UNASSIGNED

10008 .MAIN

```

01
02
03 00300 000000 0
04 00301 000004 PREFETCH
05 00302 000006 PREFETCH+READOP
06 00303 000006 PREFETCH+READOP
07 00304 000006 PREFETCH+READOP
08 00305 000006 PREFETCH+READOP
09 00306 000006 PREFETCH+READOP
10 00307 000006 PREFETCH+READOP
11 00310 000006 PREFETCH+READOP
12 00311 000004 PREFETCH
13 00312 000006 PREFETCH+READOP
14 00313 000004 PREFETCH
15 00314 000000 0
16 00315 000003 READOP+JUMP
17 00316 000003 READOP+JUMP
18 00317 000003 READOP+JUMP
19 00320 000006 PREFETCH+READOP
20 00321 000006 PREFETCH+READOP
21 00322 000006 PREFETCH+READOP
22 00323 000006 PREFETCH+READOP
23 00324 000006 PREFETCH+READOP
24 00325 000004 PREFETCH
25 00326 000002 READOP
26 00327 000004 PREFETCH
27 00330 000006 PREFETCH+READOP
28 00331 000006 PREFETCH+READOP
29 00332 000006 PREFETCH+READOP
30 00333 000006 PREFETCH+READOP
31 00334 000004 PREFETCH
32 00335 000004 PREFETCH
33 00336 000004 PREFETCH
34 00337 000006 PREFETCH+READOP

```

-ENDC

```

; UNASSIGNED
; DO
; EL
; HL
; LA
; LO
; LX
; MA
; MS
; AM
; WM
; AL
; XI
; JL
; JD
; JE
; XL
; ES
; EA
; ZL
; RL
; SP
; RE
; RS
; WD
; RX
; HS
; XS
; GG
; DI
; AP
; UL

```

10009 .MAIN

01  
02 CC340 000004 PREFETCH  
03 CC341 000004 PREFETCH  
04 CC342 000006 PREFETCH+READOP  
05 CC343 000006 PREFETCH+READOP  
06 CC344 000004 PREFETCH  
07 CC345 000004 PREFETCH  
08 CC346 000004 PREFETCH  
09 CC347 000004 PREFETCH  
10 CC350 000004 PREFETCH  
11 CC351 000004 PREFETCH  
12 CC352 000004 PREFETCH  
13 CC353 000004 PREFETCH  
14 CC354 000004 PREFETCH  
15 CC355 000004 PREFETCH  
16 CC356 000004 PREFETCH  
17 CC357 000004 PREFETCH  
18 CC360 000006 PREFETCH+READOP  
19 CC361 000006 PREFETCH+READOP  
20 CC362 000006 PREFETCH+READOP  
21 CC363 000000 0  
22 CC364 000006 PREFETCH+READOP  
23 CC365 000004 PREFETCH  
24 CC366 000006 PREFETCH+READOP  
25 CC367 000004 PREFETCH  
26 CC370 000006 PREFETCH+READOP  
27 CC371 000006 PREFETCH+READOP  
28 CC372 000004 PREFETCH  
29 CC373 000004 PREFETCH  
30 CC374 000000 0  
31 CC375 000002 READOP  
32 000002 .DO 2  
33 CC376 000000 0  
34 CC377 000000 0

; CI  
; AC  
; NS  
; ND  
; AS  
; AD  
; LS  
; LD  
; SH  
; SL  
; SE  
; SN  
; SO  
; SZ  
; SX  
; GP  
; FA  
; FS  
; FM  
; UNASSIGNED  
; FD  
; CF  
; DL  
; DS  
; AA  
; SS  
; DP  
; MH  
; LK  
; IX  
; UNASSIGNED  
; UNASSIGNED

```

10010 .MAIN
01
02
03      .ENDC
04      .DO 14
05      000004
06      000000 0
07      000000 0
08      000002 READOP
09      000002 READOP
10      000002 READOP
11      000002 READOP
12      000002 READOP
13      000000 0
14      000002 READOP
15      000000 0
16      000000 0
17      000003 READOP+JUMP
18      000003 READOP+JUMP
19      000003 READOP+JUMP
20      000002 READOP
21      000002 READOP
22      000002 READOP
23      000002 READOP
24      000002 READOP
25      000000 0
26      000002 READOP
27      000000 0
28      000002 READOP
29      000002 READOP
30      000002 READOP
31      000002 READOP
32      000000 0
33      000000 0
34      000000 0
35      000002 READOP

```

```

; PREFETCH ERROR
; UNASSIGNED
; DO
; EL
; HL
; LA
; LO
; LX
; WA
; WS
; AM
; WM
; AL
; XI
; JL
; JD
; JE
; XL
; ES
; EA
; ZL
; RL
; SP
; RE
; RS
; WD
; RX
; HS
; XS
; GG
; DI
; AP
; UL

```



10011 .MAIN

01  
02 CC440 000000 0  
03 CC441 000000 0  
04 CC442 000002 READOP  
05 CC443 000002 READOP  
06 CC444 000000 0  
07 CC445 000000 0  
08 CC446 000000 0  
09 CC447 000000 0  
10 CC450 000000 0  
11 CC451 000000 0  
12 CC452 000000 0  
13 CC453 000000 0  
14 CC454 000000 0  
15 CC455 000000 0  
16 CC456 000000 0  
17 CC457 000000 0  
18 CC460 000002 READOP  
19 CC461 000002 READOP  
20 CC462 000002 READOP  
21 CC463 000000 0  
22 CC464 000002 READOP  
23 CC465 000000 0  
24 CC466 000002 READOP  
25 CC467 000000 0  
26 CC470 000002 READOP  
27 CC471 000002 READOP  
28 CC472 000000 0  
29 CC473 000000 0  
30 CC474 000000 0  
31 CC475 000002 READOP  
32       000002 .D0 2  
33 CC476 000000 0  
34 CC477 000000 0

? CI  
? AC  
? NS  
? ND  
? AS  
? AD  
? LS  
? LD  
? SH  
? SL  
? SE  
? SN  
? SO  
? SZ  
? SX  
? GP  
? FA  
? FS  
? FM  
? UNASSIGNED  
? FD  
? CF  
? DL  
? DS  
? AA  
? SS  
? DP  
? MH  
? LK  
? IX  
? UNASSIGNED  
? UNASSIGNED

```

10012 .MAIN
01          .ENDC
02 C0500 00000 0
03 C0501 00000 0
04 C0502 00002 READOP
05 C0503 00002 READOP
06 C0504 00002 READOP
07 C0505 00002 READCP
08 C0506 00002 READOP
09 C0507 00002 READOP
10 C0510 00002 READOP
11 C0511 00000 0
12 C0512 00002 READOP
13 C0513 00000 0
14 C0514 00000 0
15 C0515 00003 READOP+JUMP
16 C0516 00003 READOP+JUMP
17 C0517 00003 READOP+JUMP
18 C0520 00002 READOP
19 C0521 00002 READOP
20 C0522 00002 READOP
21 C0523 00002 READOP
22 C0524 00002 READOP
23 C0525 00000 0
24 C0526 00002 READOP
25 C0527 00000 0
26 C0530 00002 READOP
27 C0531 00002 READOP
28 C0532 00002 READOP
29 C0533 00002 READOP
30 C0534 00000 0
31 C0535 00000 0
32 C0536 00000 0
33 C0537 00002 READOP

```

```

; UNASSIGNED
; DO
; EL
; HL
; LA
; LO
; LX
; WA
; WS
; AM
; WM
; AL
; XI
; JL
; JD
; JE
; XL
; ES
; EA
; ZL
; RL
; SP
; RE
; RS
; WD
; RX
; HS
; XS
; GG
; DI
; AP
; UL

```

1U013 .MAIN

01  
02 CC540 000000 0  
03 CC541 000000 0  
04 CC542 000002 READOP  
05 CC543 000002 READOP  
06 CC544 000000 0  
07 CC545 000000 0  
08 CC546 000000 0  
09 CC547 000000 0  
10 CC550 000000 0  
11 CC551 000000 0  
12 CC552 000000 0  
13 CC553 000000 0  
14 CC554 000000 0  
15 CC555 000000 0  
16 CC556 000000 0  
17 CC557 000000 0  
18 CC560 000002 READOP  
19 CC561 000002 READOP  
20 CC562 000002 READOP  
21 CC563 000000 0  
22 CC564 000002 READOP  
23 CC565 000000 0  
24 CC566 000002 READOP  
25 CC567 000000 0  
26 CC570 000002 READOP  
27 CC571 000002 READOP  
28 CC572 000000 0  
29 CC573 000000 0  
30 CC574 000000 0  
31 CC575 000002 READOP  
32 CC576 000002 .DO 2  
33 CC576 000000 0  
34 CC577 000000 0

? CI  
? AC  
? NS  
? ND  
? AS  
? AD  
? LS  
? LD  
? SH  
? SL  
? SE  
? SN  
? SO  
? SZ  
? SX  
? GP  
? FA  
? FS  
? FM  
? UNASSIGNED  
? FD  
? CF  
? DL  
? DS  
? AA  
? SS  
? DP  
? MH  
? LK  
? IX  
? UNASSIGNED  
? UNASSIGNED

IC014 -MAIN

```
01          -ENDC
02 CC600 000000 0
03 CC601 000000 0
04 CC602 000002 READOP
05 CC603 000002 READOP
06 CC604 000002 READOP
07 CC605 000002 READOP
08 CC606 000002 READOP
09 CC607 000002 READOP
10 CC610 000002 READOP
11 CC611 000000 0
12 CC612 000002 READOP
13 CC613 000000 0
14 CC614 000000 0
15 CC615 000003 READOP+JUMP
16 CC616 000003 READOP+JUMP
17 CC617 000003 READOP+JUMP
18 CC620 000002 READOP
19 CC621 000002 READOP
20 CC622 000002 READOP
21 CC623 000002 READOP
22 CC624 000002 READOP
23 CC625 000000 0
24 CC626 000002 READOP
25 CC627 000000 0
26 CC630 000002 READOP
27 CC631 000002 READOP
28 CC632 000002 READOP
29 CC633 000002 READOP
30 CC634 000000 0
31 CC635 000000 0
32 CC636 000000 0
33 CC637 000002 READOP
```

UNASSIGNED

```
; DO
; EL
; HL
; LA
; LO
; LX
; WA
; WS
; AM
; WM
; AL
; XI
; JL
; JD
; JE
; XL
; ES
; EA
; ZL
; RL
; SP
; RE
; RS
; WD
; RX
; HS
; XS
; GG
; DI
; AP
; UL
```

10015 .MAIN

01 CC640 000000 0  
02 CC641 000000 0  
03 CC642 000002 READOP  
04 CC643 000002 READOP  
05 CC644 000000 0  
06 CC645 000000 0  
07 CC646 000000 0  
08 CC647 000000 0  
09 CC650 000000 0  
10 CC651 000000 0  
11 CC652 000000 0  
12 CC653 000000 0  
13 CC654 000000 0  
14 CC655 000000 0  
15 CC656 000000 0  
16 CC657 000000 0  
17 CC660 000002 READOP  
18 CC661 000002 READOP  
19 CC662 000002 READOP  
20 CC663 000000 0  
21 CC664 000002 READOP  
22 CC665 000000 0  
23 CC666 000002 READOP  
24 CC667 000000 0  
25 CC670 000002 READOP  
26 CC671 000002 READOP  
27 CC672 000000 0  
28 CC673 000000 0  
29 CC674 000000 0  
30 CC675 000002 READOP  
31 CC676 000002 .00 2  
32 CC677 000000 0  
33 CC677 000000 0  
34 CC677 000000 0

; CI  
; AC  
; NS  
; ND  
; AS  
; AD  
; LS  
; LD  
; SH  
; SL  
; SE  
; SN  
; SO  
; SZ  
; SX  
; GP  
; FA  
; FS  
; FM  
; UNASSIGNED  
; FD  
; CF  
; DL  
; DS  
; AA  
; SS  
; DP  
; MH  
; LK  
; IX  
; UNASSIGNED  
; UNASSIGNED

10016 -MAIN

```
01      .ENDC
02 CC700 000000 0
03 CC701 000000 0
04 CC702 000002 READOP
05 CC703 000002 READOP
06 CC704 000002 READOP
07 CC705 000002 READOP
08 CC706 000002 READOP
09 CC707 000002 READOP
10 CC710 000002 READOP
11 CC711 000000 0
12 CC712 000002 READOP
13 CC713 000000 0
14 CC714 000000 0
15 CC715 000003 READOP+JUMP
16 CC716 000003 READOP+JUMP
17 CC717 000003 READOP+JUMP
18 CC720 000002 READOP
19 CC721 000002 READOP
20 CC722 000002 READOP
21 CC723 000002 READOP
22 CC724 000002 READOP
23 CC725 000000 0
24 CC726 000002 READOP
25 CC727 000000 0
26 CC730 000002 READOP
27 CC731 000002 READOP
28 CC732 000002 READOP
29 CC733 000002 READOP
30 CC734 000000 0
31 CC735 000000 0
32 CC736 000000 0
33 CC737 000002 READOP
```

; UNASSIGNED

```
; DO
; EL
; HL
; LA
; LO
; LX
; WA
; WS
; AM
; WM
; AL
; XI
; JL
; JD
; JE
; XL
; ES
; EA
; ZL
; RL
; SP
; RE
; RS
; WD
; RX
; HS
; XS
; GG
; DI
; AP
; UL
```

10017 -MAIN

01  
02 C0740 00000 0  
03 C0741 00000 0  
04 C0742 00002 READOP  
05 C0743 00002 READOP  
06 C0744 00000 0  
07 C0745 00000 0  
08 C0746 00000 0  
09 C0747 00000 0  
10 C0750 00000 0  
11 C0751 00000 0  
12 C0752 00000 0  
13 C0753 00000 0  
14 C0754 00000 0  
15 C0755 00000 0  
16 C0756 00000 0  
17 C0757 00000 0  
18 C0760 00002 READOP  
19 C0761 00002 READOP  
20 C0762 00002 READOP  
21 C0763 00000 0  
22 C0764 00002 READOP  
23 C0765 00000 0  
24 C0766 00002 READOP  
25 C0767 00000 0  
26 C0770 00002 READOP  
27 C0771 00002 READOP  
28 C0772 00000 0  
29 C0773 00000 0  
30 C0774 00000 0  
31 C0775 00002 READOP  
32 U00002 -D0 2  
33 C0776 00000 0  
34 C0777 00000 0

; C1  
; AC  
; NS  
; ND  
; AS  
; AD  
; LS  
; LD  
; SH  
; SL  
; SE  
; SN  
; SO  
; SZ  
; SX  
; GP  
; FA  
; FS  
; FM  
; UNASSIGNED  
; FD  
; CF  
; DL  
; DS  
; AA  
; SS  
; DP  
; MH  
; LK  
; IX  
; UNASSIGNED  
; UNASSIGNED

1001E .MAIN

01

02

.ENDC

.END

000C SOURCE LINES IN ERROR



**RETURN LETTER**

Title: CPU823, Microprogram.

RCSL No.: 991 10172

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Please comment on this manual's completeness, accuracy, organization, usability, and readability:

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Do you find errors in this manual? If so, specify by page.

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How can this manual be improved?

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Other comments?

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Name: \_\_\_\_\_ Title: \_\_\_\_\_

Company: \_\_\_\_\_

Address: \_\_\_\_\_

Date: \_\_\_\_\_

Thank you

..... **Fold here** .....

..... **Do not tear - Fold here and staple** .....

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postage  
here

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