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Title:

FPA803 MAINTENANCE PROCEDURES

Skew Compensation

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Keywords:

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Abstract:

This paper describes how to skew compensate FPA803 front-end processor adapter.

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1. MAINTENANCE PROCEDURES

Skew Compensation.

Two potentiometers (P1 and P2) are used to compensate for driver/receiver skew, cable skew and logic delays when data or address is received from RC8000 bus. The potentiometers are located beside positions 146 and 156 together with wrap-pin testpoints used for the adjustments of the delays. See fig. 1.1.

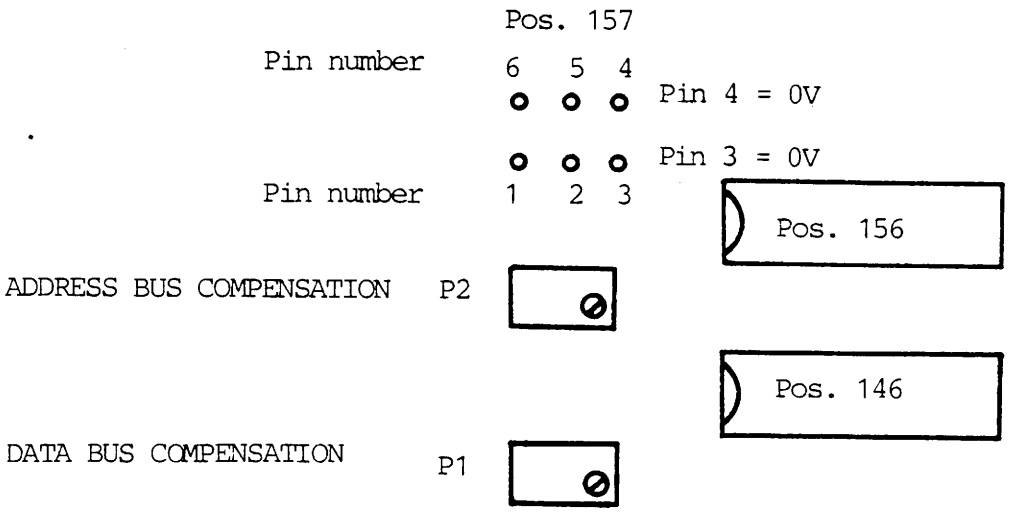

Fig. 1.1

DATA BUS COMPENSATION is used when data is received from the RC8000 bus. The compensation delay must be adjusted to approx. 120 nS. Data bus compensation delay is adjusted as follows (refer to diagram 23 in TECHNICAL MANUAL and fig. 1.2):

1. Set oscilloscope to trigger on the rising edge of the delay for ACK IN on wrap-pin pos. 157-pin 1.

2. Observe the falling edge of -, (MASTER * DEL ACK * DATA IN) pos. 157-pin 6, and adjust the potentiometer until a delay of 120 nS. is obtained.

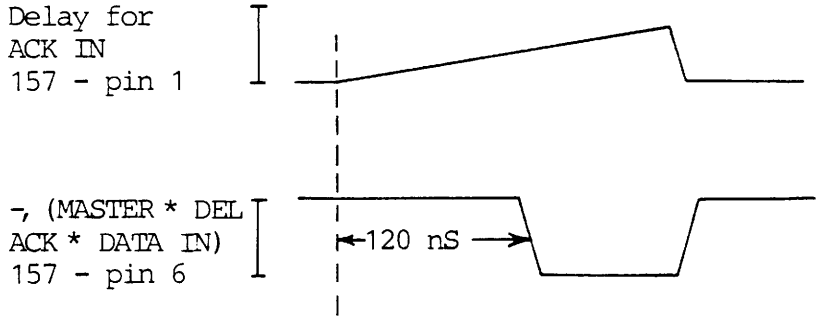

Fig. 1.2

ADDRESS BUS COMPENSATION is used when FPA803 is addressed from the RC8000 address bus (start channel program and Reset Unit). The compensation delay must be adjusted to approx. 150 nS.

Address bus compensation delay is adjusted as follows (refer to diagram 24 in TECHNICAL MANUAL and fig. 1.3):

1. Set oscilloscope to trigger on the rising edge of the delay for DATA READY on wrap-pin pos. 157-2.

2. Observe the falling edge of pos. 157-5, and adjust the potentiometer until a delay of 150 nS. is obtained.

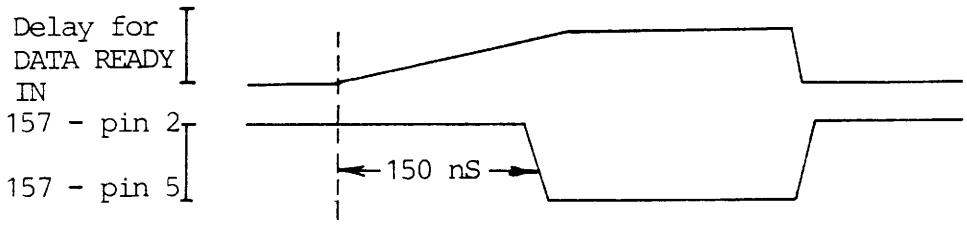

Fig. 1.3

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