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BOSS

Basic Internal Formats Maintenance Manual



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1. INTRODUCTION

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This book is a maintenance manual, and so it is a working document for the people maintaining BOSS: It is a collection of descriptions that are needed over and over again when programming for BOSS, and so a general understanding of the structure of the system is more or less presupposed. 1.

1

The program text for the online system is contained in 10 files, compiled separately (but using the same option file). These files are referred to as MODULES.

The first MODULE, named 'bos' (or central) contains the basic elements:

- Central Logic procedures (or C.L.)
- Pager coroutine
- Initialization and loader/linker.

Besides this, the commonly used data structures are described. These descriptions are closely related to the method of referenc-. ing the structure in slang (suppose the absolute address of first word is contained in register W1):

+0:		first word, referred as "Xl".
+2:		second word, as "X1+2".
+4:		third word, as "X1+4".
+6:	+7:	hw No 7, as "X1+6". hw No 8, as "X1+7".

COMMON DATA STRUCTURES

2.1 Common Resident Tables are described in section 5.

2.

The common data structures resident in BOSS core are placed in tables in the high-address end of BOSS core at run time.

In this survey, they are mentioned from the low address end:

- core table - segment table Allocated after first loader pass.

- semaphore table Allocated during first loader pass.

- terminal buffers

- sender table

- coroutine table

Allocated at init (before first loader pass).

- file access table

- run test buffers /

(- top address of BOSS)

2.1.1 Core Table

Core table holds one entry for every segment place in page core (see chapter 5). Coretable is indexed with core index: segment places counted from 0. Segment number (in +3) is relative to first drum segment (cf. 5.6).

Core index: 0 1 2 3 4 5 - - - -

Entries: 0 1 2 3 4 5 - - - -

Format of a core table entry (size = 4 hw):

+0: core index for first segment of section
+1: priority (= 0 for later segments)
+2: length of section
+3: segm number of first segment of section

2.1.1

2.1 1

2.1

2.

core in lex for first sego of section Priority (12 bits): (7 bits) 1 = write page_ priority (2-254) l = fast buffer (send and wait fast) 1 = slow buffer (send and wait slow) _l = requested and in core already 1 = job place priority: upkild at each reformed (forther) Length: 4 * number of segments for first segment in section, 0 for later segments, 4 for free segments and later segment places for a job. of places: filler Balan capet lands release core] Core table is terminated with a dummy top entry with the format: first index = 0priority = 4095 length = core table length -4segment No = 4095mfor far first sayin of section 2.1.2 Segment Table 2.1.2

Segment table holds one entry for every segment in virtual core (drumcore/disccore - see chapter 5).

Segment table is indexed with segment No: first entry is segm No = (last addr of BOSS) //512+1, so only segment places of virtual core are contained in segment table.

sorball affait our "virtual addresses" (sec f. S)

Format of a segment table entry: Length = 1 hw, 12 bits: $\int 0 = \text{first segm in section}$ (1 = 1 ater segm in section)Core index for this $\int 0 =$ section in core segment, if section (1 = section not in core is in core (0-1023). Notice that when the toro least significant bits are marked off, the segment table entry directly studes the relative address of the corresponding core table entry (because calcore table entry has a length 2.1.3 2.1.3 Semaphore Table of 4 hur), The semaphore table is allocated (and defined in size) during first loader pass, by reservations in external 19. The semaphore table contains semaphore descriptions and also a diversity of private datastructures, not described here. Format of a semaphore description: length = 6 hwSEMAPHORE >+0: SEM. VALUE (option el3) +2: FIRST IN QUEUE ADDR +4: LAST IN QUEUE Examples of contents of semaphore descriptions: CORUNO is addresses of a coroutine description VIRT.OP is virtual address of an operation. - SIMPLE SEMAPHORE: (OPEN USED) -2 2 -1 CORUNO 0 0 FIRST CORUNO LAST CORUNO CORUNO 0

- CHAINED SEMAPHORE: (OPEN CHAINED USED)



2.1.4 Terminal Buffers

Used for terminal input/output. Allocated at init. A buffer for each possible terminal supported. Number of buffers = option i4+1. Buffer length = option i3 hws.

2.1.5 Sender Table

Each entry holds a description of a (maybe unknown) process sending messages to BOSS, and a semaphore to open when it occurs. Allocated at init. Number of entries = option el5.

When C.L. detects a message arrived from another process, this event is signalled to a waiting coroutine by making open chained of an operation to a specific semaphore. The possible senders and related semaphores are found in sender table.

Format of sender table:

ONE TABLE ENTRY: (length = 10 hws)

+0: Process descr. addr of a known sender						
+2: Address of semaphore to signal						
+4: OPER	: +0: CHAIN					
+6:	+2: free, op.kind (=0)					
+8:	+4: Mess. buf. addr of received mess.					
	or = 1: mess in queue, but no coroutine waiting					
	or = 0: no message in queue					

2.1.5

2.1.4

Total number of sender table entries:

- el5: = commandios + unknown sender (2)
 - + pseudojobs + operator display (1);

C.L. performs, when message is received:

- 1. find sender in sendertable (last entry = unknown sender)
- 2. if oper already describes a mess ready for coroutine or coroutine not waiting (sem. value > -1) then skip the message (in the event queue) else: insert mess buf addr in oper. simulate open chained (oper) page 2 (activated coroutine): = abs addr of oper. get event (mess. buf addr);

The coroutine performs:

1. lock chained (SEM).

On SEM, many different operations may arrive, also an operation describing an arrived message (as address of SEM is inserted in a sender table entry).

"Message-operations" are distinguished by having fouth hw (OPER+3) = 0 (op.kind = 0), by convention.

Having received such an operation, proceed with:

- 2. Copy message (maybe).
- 3. Take actions on message.
- 4. Send answer and set OPER(+4):= 0

OPER(+2) := 0 (= free), maybe. (hw).

5. Proceed with other tasks, maybe.

Now, a new message may be handled by C.L., but only one. 6. Return to 1.

The coroutine table contains coroutine descriptions. Allocated at init. Number of entries = option el4 (= number of coroutines).

In the code of C.L., the variable named fl (called coruno) holds the abs address of current coroutine description (the abs address of fl is in ext (26)). And the variable named f2 (called coruno code) holds the abs address of current coroutine code page.

Format of an entry in coroutine table, (see details below, length = 16 hws, option el2):

+0	CHAIN	
+2	STATE	
+4	COROUT.IDENT, TEST, REL.RETURN	
+6	PAGEO	
+8	PAGE1 (Virtual	
+10	PAGE2 (address	
+12	PAGE3	
+14	PAGE4	
	(cf. see	45)
7		

The virtual addresses of pages should be interpreted: if it is > max core addr of BOSS, it is a virtual address. Otherwise it is some core address (e.g. CL will sometimes use page 2 for a core address).

On exit to a corutine, CL promises that all pages having a real virtual address are in core.

Detailed format of a coroutine description: length = 16 hws (option el2)

CORUNO-> +0 CHAIN: (f1) - TO NEXT IN QUEUE IF IN SOME QUEUE - OR = 0 IF LAST IN QUEUE - OR UNDEF +2 STATE: = 0 when running, and other cases = 1 after lock/lock chained, when SEM.VAL <= 0. (for testoutput of operation at exit). < FIRST of BOSS: waiting for answer (= message buffer addr). < LAST of BOSS: after lock/lock chained, when SEM VAL > 0 during check-pages. (= SEM ADDR) andoes fili regenters < O >=LAST of BOSS: during openchained, waiting for virtual operation to be moved to core (to update the chains) +4 COROUTINE IDENT <15 (No, 1-999) 112 -+ TEST PATTERN <12 (3 BITS, TESTOUTPUT) + REL. RETURN (The relative addr to return to in page 0, when exit is done to courutine) +6 PAGEO: (VIRTUAL ADDR) CODE PAGE currently used by the coroutine +8 PAGE1: (VIRTUAL ADDR) VARIABLE PAGE, variables used by the coroutine +10 PAGE2: (VIRTUAL ADDR) SET BY CENTRAL LOGIC: = OPERATION after lockchained INTERNAL USE IN CENTRAL LOGIC: = VIRT: of last operation in queue during opench = 0 after send and wait/stop and wait = CORETABLE REF. OF BUFFER after send and wait fast-slow +12 PAGE3: (VIRTUAL ADDR) SET BY COROUTINE +14 PAGE4: (VIRTUAL ADDR) SET BY COROUTINE

2.1.7 File Access Table

The file access table is used to control the access from coroutines to bs files. It is closely coupled to the name table of the monitor.

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Each entry in file accesstable is 1 hw, and corresponds to one area process description in the monitor (= number of entries). Allocated during init.



The file access table is indexed via the name table address (NTA), which is the absolute address of the word in name table pointing to the process description in question. NTA is set by C.L. procedure prepare access, which creates an area process.

"count" is counting number of simultaneous "openings" to the file described by the area process.

An "opening" is understood as the time between call of prepare access and terminate access.

2.1.7

A negative count will lead to bossfault 2. If count > 1, more than one coroutine (e.g. terminals) have opened to this specific file, and it must not be destroyed or overwritten.

2.1.8 Run Test Buffers

Allocated during init. 2 buffers, maybe only one (if option e7 = -1).

Each buffer is 512 hws (+ one dummy word). Used by C.L. procedures for testoutput. When a buffer is full (no room for next testoutput record), it is sent to the device.

The device may be a bs file name "bosstest" or a magtape station named "bosstest". In the latter case, option e6 = +1 is required.

The selection of magtape/bs file is done on basis of "reserve process". If this succeeds, it must be a peripheral process already existing (magtape assumed). Otherwise an area process is created.

2.2 Internal Queues in Central Logic

2.2

C.L. works on 3 queues, used in administration of the coroutines.

The queues are operated like semaphores and so the format is similar.

CORUNO denotes a coroutine description address.

- <u>Active queue</u>. Chains of coroutines ready to activate (internal name: f4).

f4 -> (-2 : no value of the queue)+0: FIRST CORUNO IN QUEUE (or = 0)

+2: LAST CORUNO IN QUEUE (or = 0)

2.1.8

- Answer queue. Chain of coroutines waiting for an answer to a message (internal name: f3).

f3 \rightarrow (-2: no value of the queue) +0: FIRST CORUNO IN QUEUE (or = 0) +2: LAST CORUNO IN QUEUE (or = 0)

- Pager queue. Chain of coroutines, whose pages are not in core, and whose pages must be moved to core in order to proceed with some operation (internal name: f5).

f5 -> +0: VALUE +2: FIRST CORUNO IN QUEUE (or = 0) +4: LAST CORUNO IN QUEUE (or = 0)

2.3 Datastructures in Virtual Core

> The datastructures not resident in BOSS core are placed in pages in the virtual core (drumcore/disccore), and moved to (and maybe from) the core by the pager coroutine (see chapter 5).

2.3

The datastructures placed on pages, are:

- Code pages. Used as page0 in coroutines.
- Variable pages. Used as pagel in coroutines.
- Operations. Chained to a semaphore, or in no chain (if delivered to a coroutine).
- Job file pages.
- Job description pages.
- Work pages, no general formats.

Pages are created by the initialization code in each module file (coroutine file), when activated by the loader.

2.3.1 Code Pages



2.3.1

Code pages contain code.

Format of a code page (values are set at EXIT to coroutine):

ABS.

PAGEO ->	+0:	ABS. PAGEO ADDR. (=ITSELF)
ADDR	+2:	ABS. PAGE1 ADDR. (VARIABLES)
(f2.)	+4:	ABS. PAGE2 ADDR. (USED BY C.L.)
	+6:	ABS. PAGE3 ADDR
	+8:	ABS. PAGE4 ADDR
	+10:	CODEPAGE IDENT (hw)
	+11:	REL. RETURN at exit from CL (hw)
		i.e.: rel.addr of the instruction
	1	to which C.L. jumped at exit.
		1
	1	(the rest contains code)

Before exit to a coroutine, C.L. assigns all the fields mentioned, except CODEPAGE IDENT.

All code pages are created as "real" pages. Except for the pager, which is resident in core. But the head of the pager code has the same format.

2.3.2 Variable Pages

Contain variables, mostly dedicated to a specific coroutine. Used as pagel.

There exist no conventions about format. Note that C.L. procedure wl-call stores virtual return address on pagel:

+0 Virtual page0 addr

+2 | Relative return in page0

2.3.3

Common	conventions	for	format:

	يجمد ومحمد الأربية المستحدة الأربية المحمد والمتحد والمتحد والمتحد والمتحد والمحمد والمحمد والمحمد والمحمد والم		
+0 [chain (if chained)		
+2	"Free"-mark/banker op.code	≥ HW	defines use and
+3	Op.kind	HW	contents of operation
+4	Answer Semafor addr (abs)	(if sending	coroutine expects an
		(answer)	
1			
I	(defined by 2nd word)		· .
i			

13

In an operation, C.L. only uses 1st word, and only when the operation is chained to a semaphore. The chain is a virtual address of the next in chain.

Note that the length of the operation is not part of the datastructure. It is defined at creation and not signalled later on.

Examples of semafor states:

- Semaphore with value 3 and chain (operations queued):

SEM.DESCR.

(VIRTUAL) CORE:



2.3.3

- Semaphore with value 3 and no chain:

SEM.DESCR:



- Semaphore with value 0:

SEM.DESCR:



- Semaphore with value -2 (2 coroutines chained):

SEM.DESCR:

CORUT.DESCR:



2.3.4 Job File Pages

This page is not a C.L. datastructure. It is created and used in the module term1.

One page for each commandio coroutine.

2.3.4

nu materi

Layout of JOB FILE PAGE:

; sl 14.6.71 job file page term1 g0=k=4-146 ; begin page 1, job file page return virt and rel for commandio procs. d7=4, d8=4 ; param list and line buffer (146 hw) also used as working area by command actions. d34=k-2-g0, d35=d8-2 ; last of line, base line ; variables for next block and attention; d36=k-a0, 0 ext 177 : job descr page d38#k-g0, g63: 53 ext 221 : terminal reserve. binary sematore. 2 d39=k-y0, g64: ;+2 ext 223 : attention semafore. h. e13*14+e13, 19 W... ; d40=k-g0, g65: ext 222 : terminal sender descr ; h. 10*14+10, 20 w. ; d41=k-g0, 0, r.5 ; ext 2178300: terminal name, nta <* the two externals are equivalent *> d42=k-g0, g66: 150 ; ext 178 : first addr of terminal buffer. c. d42+2-e11-1 m.*** e11 too small z. ; variables for next char and central actions: d1=k-g0, 0 I partial word d2=k-g0, -1 ; ext 289 : special char. logout cause 3+2 state. initially; start state, testing only. d3=k-g0, g20 d4=k-g0, 0 curr word. abs address in input buffer. d5=k-g0, top but. 0 --d6=k-q0, 29<12+0 : console state < 12 + jobstate</pre> . ext: 179 ; console state: 8 + ready*4 + user*2 + operator*1 + unlogged*16 job state: defined in tjob, to page d37=k=y0, 0 ;+2 conversational * 8 ; d10 is used to count buffers returned with timer status ; variables for edit actions: d9=k-g0, d14 top corr list, rel address within page 1. - 2 3+2 work, line number, timer count(from commany input). d10=k−g0, () d11=k-g0, 0 work, return rel inside page. ; d12=k=g0, d15 ; curr corr segm. rel address within page 1. d15≖k-g0, 1000 001 first corr segm: list of corr segments each ; r. 2*144 ; ext 228 : described by two words, thus: ; first word: _ top corr list when the corr segment became fully large ; until it is full. second word: virt address of corr segment. d86=k=g0, 0 ; ext 135 ; start line for autoline d87=k-g0, 0 ; ext 136 : linedistance in autoline > variables for next line and init line: d16=k-g(), 0 ext 189 : work page, buffer for basis file. ; : abs or page. relative on bulk file page. d18=k+g0, 0 ext 188 2 (page 0-4 are indicated by value 0-8, ; abs addr by: ext 155} d27=k-g0, 0 :=2 last store rel, used by next line. d19=k-g0, 0 ;+0 ext 187 : store rel. d20=k-g0, 0 ;+2 return rel. d21=k-g0, 0 : segment number in basis file. ; ext 231 d22=k-g0, 0, r.5 ext 218 : basis file name, nta ; d26=k=g0 logical status at alarm. ; d33=k-g0, 0 ; ext 190 : line got, used by next line. d28=k-g0, 0 next corr line ; 7+2 next corr index d29=k=g0, 0 d30=k-g0, 0 used already from corr line. ; d31=k-g0, 0 next file line d32=k=a0, C ;+2 ext 230 : next file index d25=k+2-g0, 0, 0 ext 257 ; : return virt and rel from nextline etc., and from print catalog entry

Layout of JOB FILE PAGE (continued):

; sl 12.11.71 job file page term1 ; variables for accounting and stdinterval : time logged in (seconds) d43=k-g0, 0 ;-2 ext 494 d44=k=g0, 0 ;+2 ext 226
; ext 227 : operations performed d45=k+2~g0, 0, 0 : stdinterval ; variables for psjob, primary io: d50=k-g0, g67: ext 175 : psjobque, chained semafor. . ; 19 w. h. e13*145/ 2 d51≖k−g0, g68: ext 176 :: job in core, binary semafor. ; h. e13×145, 19 W. . ext 180 : exhaust count. d52=k=g0, 0 ; ; job controlled printer: d53=k-g0, 0 ext 181 : rest room on page ; d54=k-g0, C ;+2 ext 182 : job buffer index. : get current virt buffer addr >>d55=k-g0, 0 \$+4 ext 183 ext 184 ext 185 : message addr : first of job (even) d56=k-gi), 0 : d57=k=g0, 0 ; d58=k=g0, 0 ext 186 : top of job (even) ; ; primary output: ; ext 191 : : rest room on page d59=k=g0, 0 d60=k-g0, 0 3+2 ext 192 : job buffer index d61=k-g0, 0 ;+4 ext 193 : virt buf : first buf byte d62=k-g0, 0 ext 194 . ext 195 ext 196 : primary output file: name, nta : _ _ _ : Length (s d63#k-g0, 0, r.5 2 1 : d64=k-g0, 0 - : Length (segments) d65=k-g0, g69: 54 : terminal full, simple semafor ; ext 197 / card reader: d66≖k=g0, 0 ... ext 198,450; prog state, start rel ; ext 199 d67=k-g0, 0 : curr virt address of card buffer 2 d68=k-g0, 0 ext 200 : last received status of card reader • : card length d69=k=g0, 0 ext 201 ; ;-2 ext 367 d82=k-g0, 0 : free buffer semafore : full buffer semafore d83=k-g0, 0 ext 368 ; d84=k-g0, 0 3-2 ext 369 : virt first buffer addr 185=k=g0, 0 ext 370 : virt top buffer addr : ; terminal output: ext 299 : state d71≠k=g0, 0 ; d72=k-g0, 0,0 ext 301,302: next byte, buf byte ; ext 303,304: virt buf, appetite ; d73=k+g0, 0,0 d74=k-g0, 0 ext 305 : : out bytes > job controlled tape reader: d75=k-g0, 0 ext 446 : prog state ; d76=d75+1 447: start rel : ext d77=k-g0, 0 ext 448 : curr virt address of tape buffer 3 d78=k+g0, 0 : last received status of tape reader ; ext 449 ext 518 : job reader free : job reader full d90=k-g0, 0 ; ext 519 ext 520 d91=k=40, 0 ; : first virt buffer d92=k=g0, 0 : d93=k-q0, 0 : ext 521 : top virt buffer ; card reader and reader: d81=k-g(), 0 ext 429 : next store in job input area ; ; job controlled printer: d79=k-g0, 0 : current paper type ext 474 ; d80=k-g0, g140: 81 ; ext 306 : terminal input semafore ; variables for various commands ext 134 ext 236 : terminal user rights (privilege) d88=k−g0, 0 ; d70=k-g0, g100: : commandio answer semafore ; h. e13*i4+e13, 19 w.

Layout of JOB FILE PAGE (continued):

; sl 14.6.71 job file page term1 ; line table, correction list, related g-names. d17=k-g0, 1000-000 line table, contains one word for each ; ; segment in the basis file, specifying the line number for the first ; character on the segment. if the file is not terminated by an em-; character, the top segment is considered containing the em-character. ; the table is terminated by one more word containing a large value. d14=d17+2+2*147 corr list. one word for each correction ; specifying the line number corrected. g2 =d14+2+2*i48 ; end page 1, job file page. g2 = length g12=g2, g13=510-146 : max top corr list, max last on corr page g14=d15+2*144=2 ; max current corr segm. g15=8+4 required edit bits: conversational, ready. ; g16=146, g19=13 ; max line length, length terminal buffer ; the corrected lines are stored on correction segments, each ; segment composed like this: ; 0: last on corr page (1 word, specifying last used on this page) ; 2; corrected line (possibly some words containing the characters, 2 one word containing line number, one word containing -length of corrected line) ; ; corrected line, ...

2.3.5 Job Description Pages

This page is not a C.L. datastructure. It is described in a separate module text, and used in the modules: job, jobstart and procs.

It is created (as a page) in the module JOBSTART

One page for each pseudo-job.

Layout of JOB DESCRIPTION PAGE:

; pm 16.12.71 boss 2, tpsjobdescr. ... 1... ; definition of pajob descr page common for pajob start, ; lookup usercat and psjob end. ; versionid: 78 07 14,20 b. d160, w. ; £0: 0,0 ; return point d11=-f0., 0, r.8*i49 ;+4 param 0 ; rel return 'set reader device no' d144=-10., d146=-f0.,f9:h. r99*145,19 w.; abs ref permanent core for rb messages d97=-f0. ; top param d12=-f0., 0 2 switch d0=-f0., f6: 0 J+2 jobfile d3=-f0., 0 3+4 virt line buffer d91=-f0., f1: 63 3 psjob que d93=-f0., 0 3 request line d110=-f0.,f5: 64 ; job in core d96=-f0., f71 0 ; psjobindex * e13 d108=-f0_,f2: h. 145+10,20 w.; sender table addr d15=-f0./ 0 ; tail: size <:disc:> ;+2 docname d105=-f0., 0, 1.7 ;+10 zeroes d105: zero for set catbase (tail+6) d87=-f0., ;+0 semafore operation: 0 chain 2*e13, 19 w. ;+2 opcode: skip 2 psjob buf sem f8: h. d82=-f0., f4:h.145*e13,19 w.1+4 psjob buf d14=-f0., 0 3+6 banker operation: chain f3: 0 J+2: opcode, osjobnr 7+4: all, stations ext 349: newjob operation (28 bytes) 3+6: disc 3+8: reader #+10: converts, accounts ;+12: bufs/ areas J+14: internals, suspends 3+16: drum J+18: device 3+20: -7+22: #1ze 3+24: keys, priority J+26: gross time ;+28: net time J+30: arrival J+32: max wait d89 - d89+18 is used for lookup entry : in convert from commandio ; these variables are located in banker operation d89=d14+10 ;+0 message: code ;+2 first 2+4 last ;+6 d86=d89+6 segment d1=d86+2 2+0 name addr in cat proc ;+2 interval -2+4 ;+6 length, function 1+8 rel return

```
: pm 7.12.71
                                 hoss 2, tpsjobdescr, ...2...
 d9≊
     d14 +34
                       ;
                             text shift, state save
 d6≖ d9
                       ;+2 return from next line and set claims
          +2
 d7=
     d 6
           +2
                             count
 d8≠ d7
          +2
                        ;
                             addr
 d10≖ d8
          +2
                        ;
                             text addr
 d5= d1() +2
                        ;
                             work
d15 = d5
          +2
                             command state (c15 job command must be
                        ;
                                 read now, c38 claims not set,
                       ;
                            c16 claims set: only load command admitted
d16= d13 +2
                        ;
                            job param base
; d9 through d13 are used as scratch during convert from commandio too
; job parameters
d17≃ d16
                       :
                             time of arrival of job \pm 2 \pm (-13)
d23= d17 +4
                       ;−10 user name
d25= d23 +8
                       index
d20= d25 +2
                             project no
                        .
d26 = d20 + 4
                             project base used in connection whith convert
                        :
                       . .
                            operations on netprinters described in catalog
                        ;
                             entries, not equal to maxbase in case of a userpool
d19= d26 +2
                        ;
                            job name
d18 = d19 + 8
                       ;
                            prog name, name table entry
d63∓ d18 +2
                       ;
d140=d18 +10
                       ;
                           no of conversational input lines
d70= d140+1
                       ;
                            tape table entries
d143=d70 +1
                       ;
                            terminal user rights
do1= d143+2
                            usercat ref post address of user
                       ;
d62= d61 +2
                       ;
                            no of useable private kits
455= 495 +5
                            usercat refdiscrest claim key 3
                       ;
                       ; '
d60 = d22 + 2
                            usercat ref drumrest claim key 3
d58= d60 +2
                       ;
                           discrest key 1
d69= d58 +2
                       ;
d55= d69 +2
                       ;
                                          З
d59= d55 +2
                       ;
                           drumrest key
                                         1
d67= d59 +2
                       ;
                                          2
d56= d67 +2
                       ;
                                          3
d28= d56 +2
                       ;
                            first of private kit table
         +0
                       - 2
                            kit name
d29≓
         +8
                       ;
                            usercat ref rest claim key 3
157≖
        +10
                       ;
                            job claims
d113=
         12
                       ;
                            slice length
d80= d28 +14+130+2
                            top private kit table
                       :
```

boss 2, tpsjobUescr, ...3... ; pm 1.11.71 d75=d80 ; conversation d102=d75 +1 degree of information wanted: : 0 maximal information <:no:>
1 minimal information <:yes:> 3 core lock time d77= d102+1 ; d132=d77 +2 2 priority preserve catalog 0 catalog is cleaned <:no:> d134=d132+1 ; 1 catalog is preserved <:yes:> ; d138=d134+1 ; waiting time deliberately swopped out link ; d148=d135 +2 d41= d148+2 ; keys d49= d41 +1 stations ; d43= d49 +1 2 mounts tapes d51= d43 +1 ; d68≖ d51 +1 ; converts . 7 accounts d31= d68 +1 d45= d31 +1 ; output slices d71= d45 +1 suspendings 2 time * 2**(-13) first storage address d53= d71 +1 ; size bufs top d47= d53 +2 2 d35= d47 +2 ; d33= d35 +1 ; areas • ; internals d39= d33 +1 d37* d39 +1 func ; 16 protection reg d60 = d37 + 1; max wait * 2**(-13) 17 protection key d66 +1 ; d21= d66 +4 ; max d24= o21 +4 std ; user interval 2 device d30 = d24 + 6; d81= d30 +4 - d80 ;+ d7o= d81 +d75 ; max conversation d103=d81 +d102 max degree ; d78= d81 +d77 max core lock time ; ; d133=081 +d132 max priority d135=d81 +d134 max preserve catalog 2 max waiting time deliberately swopped out d139=d81 +d138 ; d147=d81 +d148 ; max link d42= d81 +d41 : max keys d50= d81 +d49 max stations 3 d44= d81 +d43 ; max mounts d52= d81 +d51 max tapes ; d72= d81 +d68 ; max converts d32≖ d81 +d31 ; max accounts d46= d81 +a45 ; max output d73= a81 +d71 ; max suspendings d54= d81 +d53 ; max time d48= d81 +d47 ; max size d36= d81 +d35 max bufs ; d34# d81 +d33 max areas ; d40= d81 +d39 ; . max internals d38≢ d81 +d37 max func ; d27= d81 +d66 ; respite d65= d27 +2 2 max devices

; pm 27.12.72		boss 2, t	psjobdescr/4
d88≖ d65 +6	;	psiob status:	1 <d reader="" reserved<="" th=""></d>
-	;		1<1 change of jobname after replace
	;		1<2 operator message when enrolled
	;		1<3 online job
	;		1<4 error message pending
	;		1<5 card job
	;		1<6 Internal
	;		1<7 convert claim
	;		1<8 offline lob
			1<9 convert from commin
	;		1<10 replace tob
d90≖ d88 +1	;	output Length	
d92= d90 +1	;	paper tape cou	nt
d95 = d92 + 1	3	swop area leng	th (slices)
d116×d95 +1		card file no	
d84= d116+2	;	expected finis	time (sec after midnht)
		Postident	n
d120=d119+2		dh. linkno < 1	2 + dh. hostno
d122=0119+4		dh. hostid	· · · · · · · · · · · ·
d121=d122+2+2		name of primou	t printer (last of dbword)
		tirst word =	0 => std printer
			1 => some rb-printer
d123=d121+2	;	device kind	
d131=d123+2	;	device number (of reader
d112=d131+2	;	primary output	name and name table addr
d100=d112+10	;.	job file name a	and name table addr
d101=d100+10	;		interval
d2 =d101+4	;	total net time	(0.8 sec)
918 295 +5	;,	cputime used(0,	.8 sec)
d74 = d79 +2		convert claim	
a83# d74 +2	3-2	Length of input	t area (segments)
d174=d85 +2	;	op buf addr foi	r answer commandio
d64 =d114+4	2	returnpoint for	r load and pagejump to set claims
d118=d64 +2	;	cardfilecount	
	;+2;	card block count	t (segments)
	**4	card reader que	Je sem
	.+0		t virt butter
	/+8:	= = top	
	2+10:	tree	5 C m
4461=4118+17	;+12:	full	
0141-0110+12		start of reader	table:
	**2	tape reader seg	ment count
	2 + 4	que	ue sem
	2+0	tir	st virt buffer
	;+8	top	virt buffer
	2+10	Tre	e sem
d1/2 = d1/1 = d07	2+12	ful	l sem .
UI42-UI41-U87 ASS #A1/4147		*****	
965 - 0141+14	;	temp drum, disc	
d98≖ d85 +2	;	rest mounts	
d99= d98 +1	;	rest suspends	
d111=d99 +1	;	rest converts,	rest accounts

; re 5.2.75 d104#d111+2 ; finis cause a115=d1()4+2 ;+0: net run left at finis ;+2: n r l at run time exceeded ;+4: n r l at dump ;+6: n r l at provoke before dump ;+8: n r l at high priority exceeded ;+10: n r l at corelock exceeded ;=2 summa waiting times
; actual waiting time_rel return in low/high prio start d136=d115+12 d137=d136+2 d106=d137+2 proc descr addr of job ; ;d107=6: harderror swopin op; chain ;d107-4: free, opcode=16 ;d107-2: logical status d107=d106+8 ; swop area spec: name ;+2 ;+4 • ;+6 -;+8 name table addr ;+10 timer op: chain ;+12 free, opcode ;+14 net run time left d4=d107+16 line huffer base ; d94=d4+i46 line buffer last ; d117=d94+2 ; save array for go parameters d109=d117+d88-d18 ; top jobdescr i. t.

Time Representations

2.4

Time representations are always based on the monitor clock in address 110 (and 108).

It is loaded like this: dl Wl 110

The registers now will contain:

WO	Wl
24 bits	24 bits
48 bits re	al time clock
in unit	s of 0.1 msec.

Time in testoutput header

Algorithm in pseudo-algol second header-word: = time:= ((montime - bossstarttime) extract 29) //100; After subtract of boss start time:

WO	1	W1	
19 bits	5 bits		
not used			
	29	bits,	used

time is in units of 0.01 sec. Max. value is 5368709, stored in 24 bits.

Units of 0.8192 seconds

Algorithm in pseudo-algol: time:= (montime shift (-13)) extract 24;



time unit is: 8192 x 0.0001 = 0.8192 sec.

Shortclock in catalog entry

Algorithm in pseudo-algol: time: = (montime shift(-19)) extract 24;

, V	ŴÖ	V	ท				
5 bits 1	19 bits	5 bits	19 bits				
2	24 bits, us	sed					
		ŧ	- 	1	shift	19 =	524288.

time unit is: 524288 x 0.0001 = 52.4288 sec.

CENTRAL LOGIC PROCEDURES

3.

The functions of C.L. procedures are described below. The return from the C.L. procedure to the coroutine is in most cases done as an "exit to coroutine". Thereby it is checked that all pages stated in the page list of the coroutine description are present in core (page core). So, before call, the coroutine may change its page list, and in the same call get some other pages (note that this is not working in procedures marked with no delay).

At "exit to coroutine", C.L. promises that all pages, mentioned in the coroutine description, are in core. And on pageO itself, the corresponding abs. addresses of the pages is placed (see subsection 2.3.1).

Note that if an operation is delivered as page2, this may be a page (comming from virtual core), but it could also point to e.g. sender table (if it signals that a message has arrived).

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з.

•	EX-	1				1					
NAME	TER-	CALL			RETURN				ե	FLAY	
	NAL	WO	W	W2	W3	wo	WI	W2	W3	PAGE2	1
OPEN	4			SEM	RETURN	UNCH.	UNCH	Ø	PAGE 1	UNCH	1N-
OPEN CHAINED	6		OP	SEM	RETURN	ø	ø	ø	PAGE	0	м
LOCK	3			SEM	RETURN	Ø	ø	Ø	PAGEL	UNCH	TM
LOCK CHAINED	5			SEM	RETURN	ø	OP	ø	PAGEI	OP. PAGE	м
GET PAGES	7				RETURN	ø	ø	8	PAGEL	UNCH	M
PAGE JUMP	8			PAGEO	REL	lø :	ø	ā	PAGE	UNCH	M
(WI-) CALL	25		RETURN	PAGEO	REL	0	ø	d d	PAGEL	INCH	M
WO - CALL	34	STORE	RETURN	PAGEO	RFL	ø	ø	8	PACET	UNCH	M
WAIT ANSWER	35			MESSBUF	RETURN	STATUS	ANSW	8	PAGEI	0	V
SEND AND WAIT	1		MESS	NAME	RETURN	STATUS	ANSW	a	PAGE	õ	
SEND AND WAIT FAST	2		MESS	NAME	RETURN	STATUS	ANSW	å	PAGE	BUEPAGE	1
SEND AND WAIT SLOW	9		MESS	NAME	RETURN	STATUS	ANSW	Ø	PAGE]	DUFPAGE	1 v
STOP AND WAIT	30			NAME	RETURN	STATUS	ANSW	d	DACE)	0	l÷ I
CLEAR CORE	11		PLACES		RETURN	ø	ø	đ	PAGEI	UNCH	l.
PREPARE ACCESS	32	LOW.BASE	UP .BASE	NAME	RETURN	LOW AREA	IP AREA	ACTESS	DACE	INCL	1
TERMINATE ACCESS	33			NAME	RETURN	1 OW AREA	ID ADEA	ACCECC	DACE)	UNCH	N N
PRIV-OUT	21	HEAD	TAIL		RETTIEN	a	d	d	DACE	UNCU	
JD-1	-	WO	W	W2	wa	UNCH	1000	10,271	LINCH	UEVLET	
JD-XXX	_	LENCTH	TATT.		n3	UNCT INCO	UNCH	UNCH	UNCH	UNLH	N
			MALLI			UNCH	UNUN	UNCH	UNCH	UNCH	IN _

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LEGEND TO SURVEY OF C.L. PROCEDURES

SEM RETURN PAGE1 OP MESS NAME ANSW MESSBUF STORE UNCH Ø STATUS	 Abs address of semaphore (in semaphore table) Abs address of return-jump. Must be inside current page0. Abs address of first word of page1 Abs address of operation Abs address of 9 words to send as message Abs address of 5 words: name of receiver + NTA of receiver Abs address of 8 words containing the answer Abs address of message buffer sent Abs address of word where virtual return is stored. Unchanged Undefined Logical status of answer: if result = 1 then (2+answ(1)) else 1 shift result
BUFPAGE OP.PAGE PAGE2 PAGE0 REL LOW.BASE LOW.AREA UP.AREA ACCESS HEAD) TAIL EXTERNAL DELAY XXX	 Address of the page used as buffer (virt/abs) Address of the page containing the operation (virt/abs) Page 2 address: in coroutine descr (virt) and on page0 (abs) VIRT address of the wanted new page0 Relative adddress to exit to on the wanted new page0 Lower limit of catalog base to lookup the area Upper limit of catalog base to lookup the area Upper limit of the found area Upper limit of the found area Upper limit of the found area Total no of accesses to the area (accesscount) First word of head: length < 6+kind Abs address of the words to move to tail of record No of the external variable holding address of the procedure Tells if the C.L. delays the coroutine. Y=yes, N=no, M=maybe An integer 201 ⇐ XXX ⇐ 263. Testoutputkind will be XXX-200
PLACES	- Cove places to be cleared; first <12 + top

3.1

condrey

3.2 Procedure Descriptions



3.2.1 Open

Release a semaphore without chain (operation):

sem:= sem+l;

if sem <= 0 then activate another coroutine, waiting for the semaphore. (it is chained to active queue).

Returns immediately (no delay).

3.2.2 Open Chained

Releases a semaphore with a chained queue of operations, i.e. the operation pointed out is turned over to (chained to) the semaphore, whose value is now increased by one.

The operation is chained as the last of the operations queued at the semaphore.

The operation must be in core (pointed out by an abs addr), and must be part of a virtual page.

In the operation itself,

- first word (addr+0) is used by C.L. for chain. (Virtual addr of next in chain)
- fourth hw (addr+3) should be \diamond 0 (as 0 means a message).

Calling coroutine may be delayed. At least the chain from a preceding operation in the queue must be altered, and this requires the corresponding page in core.

Function:

The semaphore address is temporarily saved in the chain of the operation. The address of the operation is transformed from absolute to virtual, a writing bit is added and this virtual address

is saved in the state of the coroutine. Page2 of the coroutine is set to the last operation (or coroutine) chained to the semaphore.

Now the pages are checked and afterwards the page pointed to by the state, the operation, is checked. If any coroutine is waiting for the semaphore (value < 0) the first coroutine is taken out of the chain, the operation is inserted as page2 of this coroutine description and this coroutine is put in the active queue. The semaphore value is incremented by one and the state and page2 of the delivery coroutine are cleared.

If no coroutine is waiting it is checked that no other coroutine has succeeded in sending an operation to the same semaphore during a paging, if any, ordered by check pages. In this case the last-pointer of the semaphore differs from page2 of the coroutine, the page2 is set to the new last-pointer and the algorithm is repeated from the point of page-checking.

If no other coroutine has interfered, then the operation is inserted into the chain of the semaphore, the value of which is increased by one and the state of the coroutine is cleared.

3.2.3 Lock, Lockchained

These procedures are identical, as the same entry in C.L. is used for both.

3.2.3

Purpose:

- lock: wait for a semaphore without chain (operation):
 sem: = sem -1;
 if sem < 0 then wait for activation</pre>
- lock chained: wait for an operation chained to the semaphore: sem: = sem -1; if sem < 0 then wait for activation else deliver first operation on semaphore.

Description:

```
if sem <= 0 then wait for activation
else (sem is >= 1)
    sem:= sem -1;
    if any operations are chained to the semaphore
    then deliver first operation in queue;
     (even in this case the calling coroutine maybe delayed).
```

Note that the type of the semaphore is determined by the open operations performed on the semaphore:

- Open makes the semaphore unchained (simple)

- open chained makes the semaphore chained. - open (simple) fleever the semaphore as it is chained in an operation delivered, yet use open (semple) on a chained semaphore the chained semaphore the chained semaphore value.

- first word (addr+0) contains the new semaphore value.

- second word (addr+2) contains by convention

free, op.kind

op.kind = 0 if the operation signals a message arrived.

Function:

If the semaphore is not available (value ≤ 0) the state of the coroutine is cleared, the semaphore value is decreased and the coroutine is put into the semaphore chain.

If the semaphore is available (value >= 1) then the first operation of the semaphore chain (empty if not chained) is placed as page2 of the coroutine and the semaphore address as state.

Then the pages are checked and after this possibly time consuming operation the semaphore is checked again; if it is not available any longer or if the first operation in the chain now differs from the one which was found before the pages were checked, then some other coroutine has locked the semaphore and snatched away the operation during the paging which must have taken place and the algorithm is repeated from the beginning.

When the pages are ok and no other coroutine has interfered, the semaphore value is decreased and the operation is taken out of the semaphore chain (only if chained).

3.2.4 Get Pages

The pages (page0-page4) described in coroutine description (coroutine table), are moved to core (if not already present), i.e.: check that all the pages of the coroutine are in core. The coroutine continues when this is the case.

3.2.5 Page Jump

The calling coroutine wants to jump to another code page (page0), as indicated in W2-W3.

Note that no return address in the old code page is saved.

The page0 addr is renewed, and the coroutine continues when its pages are ready (in ∞ re).

3.2.6 WI-Call (or just Call)

The calling coroutine wants to jump to another code page (page0), as indicated in W2-W3.

The return address (in W1) is stored in the first two words of (current) pagel, thus:

page 1 addr+0: virt (current) page0 page 1 addr+2: rel return in page0 (W1 - abs addr of page0)

Now page0 addr is renewed, and the coroutine continues when its pages are ready.

3.2.5

3.2.7 WO-Call

The calling coroutine wants to jump to another code page (page0), as indicated in W2-W3.

The return address (in W1) is stored in the abs address pointed out by WO:

addr(WO) -2: virt (current) page0 addr(WO) +0: rel return in page0 (WI - abs addr of page0)

Now page0 addr is renewed, and the coroutine continues when its pages are ready.

3.2.8 Wait Answer

The coroutine asks to wait until answer arrives to a message already sent by the coroutine in the message buffer pointed out by W2 (MESSBUF).

Note that the message sent <u>must not</u> - if it is an I/O message point out a data buffer in some of the pages (virtual core). (in that case use 3.2.10 instead)

If I/O, the data buffer must be in permanent core. Used by transmit. The coroutine is queued to wait for the answer.

3.2.9 Send and Wait

The coroutine wants to send a message and wait for the answer.

The message to send is in addr (W1).

- if the message is an I/O mess, it <u>must not</u> point out a data buffer in some of the pages (virtual core). Use 3.2.6 instead)

- If I/O, the data buffer must be in permanent core.

- page2:= 0;

3.2.8

- send message;
- the coroutine is queued to wait for the answer;

3.2.10 Send and Wait Fast, Send and Wait Slow

The coroutine wants to send a message and wait for the answer. Data are on a virtual page.

The message to send is in addr (W1).

- The message will normally be an I/O mess, and it contains abs. addresses pointing out a data buffer in some of the pages (virtual core).
- The page in question must be in core at call time, and now this page is marked (must stay in core until answer arrives).

- page2:= virtual addr of page containing data buffer.

- send message;
- the coroutine is queued to wait for the answer;

Function:

The procedures are used in connection with input/output to or from a virtual buffer. The corresponding bufferbit is set in the core table entry for the buffer, otherwise it works as send and wait (note input/output to or from a core store buffer is accomplished by means of the normal send and wait). The difference between fast and slow is solely reflected in the order in which the pager selects its victims (i.e. core places to be cancelled).

3.2.11 Stop and Wait

The coroutine wants to perform stop process (a job process).

- page2:= 0

- stop process (monitor 60)

- the coroutine is queued to wait for the answer.

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3.2.12 Clear Core

The purpose is to reserve a part of core store before swop-in of a job process.

in addition the segment places pointed stit Works as get pages but the coroutine has in advance replaced its page4-addr in coroutine description, thus: by w1 will be cleared, and their description in core table will be changed to describe a job place. page4: +0: - first segm place (hw).

-+1: + top segn place (hw) w1: first segn place <12 + top segn place

Used by banker. Minst not be used by other contribut

The pager is activated (put in active queue).

The calling coroutine is put in the pager queue, and the pager will clear the pages occupying the core places pointed out.

3.2.13 Prepare Access

and/or write

3.2.13

3.2.12

The calling coroutine wants to access (read) a bs file. WO, WI contains the "lookup-base" from where to find the file and create area process. Calling coroutine is not delayed.

- 2 return jumps exist:
- 1) return to W3 (from call): No entry visible, the registers are: W0:= result of create area process W1, W2:= undefined W3:= page1 addr page2:= unchanged. In name area (addr(W2)), result of create area process is stored instead of name table address.
- 2) return to W3+2:

Entry found, area process created. Registers set as described in the survey. In name area (addr(W2)), name table address is set (in addr(W2)+8).
The catalog base (of BOSS) is set to the base of the area process.

Access count is increased by 1 (in access table). (if new value is < 0, bossfault 2 occurs).

3.2.14 Terminate Access

The calling coroutine wants to terminate (a previously prepared) access to a bs file.

Access count is decreased by 1 (in access table). (if new value is < 0, bossfault 2 occurs).

The catalog base (of BOSS) is set to the base of the area process.

If the (new) access count = 0, remove process is called.

The coroutine is not delayed.

3.2.15 Priv-Out

Private testoutput from the coroutine. The C.L. procedure coruno output is called. The coroutine is not delayed. The testoutput record has the format:

+0	length<6+kind		length: length of tail in hw.
+2	time		(max 502 hw)
+4	coroutine ident		
+6		}	tail: 'length' hw,
	1		copied from addr(W1) and or

3.2.16 JD-1

The coroutine wants testoutput of registers.

3.2.16

The coroutine executes the (illegal) instruction jd -1, which provokes a 'break0'.

The interrupt routine checks, that this was the case, generates at testoutput record containing registers and instruction counter, and continues with re-established registers in the instruction following the jd-1.

The coroutine is not delayed.

3.2.17

vol

jd-2xx jd-3xx

The coroutine wants to generate private testoutput (as PRIV-OUT). This facility is just a simpler way of implementing it. 3.2.17

The registers (WO, WI) are assigned in advance, with WO = tail length, WI = first (tail). XXX must be an integer, 201 \ll XXX \ll 263. Testoutput kind will be XXX-200.

The coroutine executes the (illegal) instruction jd-XXX, which provokes a 'break0'.

The interrupt routine checks that this was the case, sets WO = length < 6 + kind, WI = first (tail), calls C.L. procedure coruno output and continues with re-established registers in the in-struction following the jd-200.

The coroutine is not delayed.

LOADER

4.

The loader in BOSS is a 2-pass loader/linker.

All the module files are loaded twice and executed. This execution is the initialization of the module, and it transforms the module into one or several code pages, variable pages, creates coroutines, semaphores, operations etc.

Information used in several modules is passed from module to module via the external table. It contains abs addresses (e.g. of C.L. procedures) virtual addresses etc. All values are initially zero and only a few are defined before first loader pass.

The majority of the externals are defined by the modules. They are in turn used by other modules that may have been loaded already. This is the basis reason for the 2 passes.

praisa

The figure shows the core utilization during the two loader passes and during run.

passl:

pass2:

run:



4.2 Init and Load, Step by Step

4.2

4.1

The following description covers all the initialization performed in the module bos, including the two loader passes.

step 1. If FP is present in the BOSS core, all the code is moved, so the FP code is overwritten.

- step 2. Already existing internal processes with BOSS as parent are left unchanged i.e. the last address of BOSS core is set below the smallest of these core processes.
- step 3. Allocate run test buffer(s).
- step 4. Get name of main console.
- step 5. Remove all area processes.
- step 6. Connect testoutput medium: bosstest (bs file or magtape).
- step 7. Zero-fill all BOSS core after the bos module.
- step 8. Overwrite bosstest to zero-fill all segments (if bs file).
- step 9. Allocate core used during loader:

- external table

- drumcore table
- disccore table
- buffer for move to virtual.

step 10. Allocate (some of) common resident tables:

- file access table
- coroutine table
- sender table
- terminal buffers
- top of semaphoretable.

step 13. "Next pass": Assign externals (to initial values)

- 18 = post record of coroutine table
- 19 = post record of semaphor table
- 20 = post record of sender table.

step 14. Load 9 module files, one by one:

step 14.1 load next module file

step 14.2 testoutput of load record

step 14.3 link the module (see subsection 4.3.1)

step 14.4 Jump to initialization code of module file
 (loader procedures called from module: see
 section 4.3).

step 15. Assign externals, that will be active in pass2: 13 = move to virtual 14 = simulate lock24 = put in active queue. anderlege step 16. Adjust all virtual disc addresses in external table (all externals < 0), and virtual addresses in coroutine table. step 17. Allocate and initialize - segment table - core table. step 18. Create and overwrite drumcore and disccore. step 19. Proceed with pass2: Step 13 and 14. step 20. Output external table to testoutput. step 21. In file access table, access count is set = 1 for the files: - bosstest - drumcore - discore - usercat - catalog. step 22. If BOSS is started in monitor mode and monitor mode is not wanted (option el6) and the machine is RC4000 (option e80) then the protection key is changed. step 23. On RC8000: Ensure, that BOSS has only write access to its own core (mode0 removed).

step 24. Jump to C.L. entry, where first coroutine in active queue is activated.

4.3 Loader Procedures

The initialization code in a module calls some procedures to handle the common datastructures.

4.3.1 Algorithm for Linking a Module

Before exit to a module file, the link algorithm transfers the current value of externals to places in the module itself.

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4.3

4.3.1

The linking is also used for reserving core in semaphore-, coroutine- and sendertable.

The module contains <u>an external list</u>. This is a list of relative addresses (REF) meaning:

"in the address relative to this is found an external number".

The list starts in module start +8 (see section 4.4)

MODULE:

-	·	
ю		
+2		
+4		
ю		
⊦8	first external REF	
	second external REF	> external list
l		
	- rel addr. to coroutine)
		2
	external REF	<pre>external list (cont.)</pre>
1	0 = end of external list)
1		

A negative value means: continue in rel.addr - ref. A zero means: end of external list.

Now, the list is scanned, and each positive ref. is interpreted,

thus:

- REF is odd:

In the hw addressed (will always be the right half of a word), the external number is found. Current value of this external is inserted in the hw.

REF is even, and the word addressed contains the value 10:
 Reserve in semaphoretable (usually more than 4095 hws).
 In the word preceding the addressed word is found the number of hws to reserve.

Ext(19) is decreased by number of hws and stored in the word addressed.

(meaning: first address of just reserved core). Ext(10) is increased by number of hws.



- REF is even:

underog

In the word addressed, the external number is found. Current value of this external is inserted in the word.

305 if found it becomes ext(305) -10 if found it becomes ext(-10)

- Special use of external 18, 19, 20: The left hw of the word addressed is used as "number of hws to reserve", and the external (number in right hw) is decreased before stored in the word addressed. So this value means: first address of just reserved core.

l

42

if found: 300 18 300 hws are reserved: ext(18): = ext(18) - 300,

and it becomes:

ext(18)

ext(18) reserves in coroutine table ext(19) reserves in semaphore table ext(20) reserves in sender table.

4.3.2 Set Externals

4.3.2

The abs addr of entry to the procedure is in ext(40). The procedure is used to assign new values to externals. Called from the initialization code of the module.

Call:	Return:
WO = irr	W0: = unchanged
Wl = irr	Wl: = unchanged
W2 = irr	W2: = unchanged
W3 = -2 + abs.addr. of	W3: = undefined
start of list	Return jump is made to the
	address just after the list.

The list consists of a number of entries. Each entry is 2 words, the first is the new value, the second is external number. The last entry must be 0, -1000.

Example of call:

external list:	
g13., g14.,	
· · · · ·	
jl. w3 (2)	; set externals:
g13: 40	; here abs addr of entry is placed.
100, 304	; means set ext(304): = 100
e2, -7	; means set $ext(-7)$: = option e2
g14: 26<12+19	; here is placed abs addr of first word of
	; the 26 hws which are reserved in the
	; semaphore table.
335	; and then set external will assign this
	; value to ext(335)
0 -1000	; end of set external list
(code)	; set externals returns to this addr.

4.3.3 Reserve Virtual

The procedure is used to reserve (find room) in the virtual store for a number of hws, and returns the virtual address of the first word reserved.

The abs addr of entry to the procedure is in ext(12).

The reservation must be specified to either drumcore or disccore.

Call:	Return:
WO = drum or disc	W0: = unchanged
(WO extract $1 = 0$ means drum)	
Wl = length of reservation, in hws	Wl: = unchanged
W2 = irr.	W2: = virtual address
W3 = return addr	W3: = undefined.

- Successive calls of reserve virtual with same value of WO extract 1 and W1 >= 512, will make reservations on consecutive segments.

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4.3.3

- if W1 >= 512, the reserved room will start at a segment start, and an unused part, if any, of last segment will not be used in later reservations.
- if W1 < 512, the procedure will find the smallest suitable
 "hole" (if any), left over from previous calls with W1 < 512

4.3.4 Move to Virtual

4.3.4

4.3.5

The procedure is used to move a just initialized page to virtual core. Note that it is dummy in passl.

The abs addr of entry to the procedure is in ext(13).

Call:	Return:		
WO = first addr to move	WO: = unchanged		
Wl = length in hws	W1: = unchanged		
W2 = virtual address to	W2: = unchanged		
move to			
(W2 extract $1 = writing)$			
W3 = return addr.	W3: = unchanged		

4.3.5 Put in Active Queue

The procedure is used to chain a coroutine to the active queue, so it will be activated when the load is completed.

The abs addr of entry to the procedure is in ext(24). Note that the procedure is dummy in pass1.

Call: Return: WO = irr. WO: = undefined WI = coroutine description addr WI: = unchanged (in coroutine table) W2 = irr. W2: = abs addr of active queue. W3 = return addr. W3: = unchanged

4.3.6 Simulate Lock

The procedure is used to chain a coroutine to a semaphore, so it will be waiting for this semaphore to be opened when the load is completed.

The abs addr of entry to the procedure is in ext(14). Note that the procedure is dummy in pass1.

Ca	11:	:	Retu	urn:
WO	=	irr	W0:	= undefined
WI	=	coroutine descr. addr	w1 :	= unchanged
		(in coroutine table)		
₩2	=	semaphore descr. addr	W2:	= unchanged
		(in semaphore table)		
WЗ		return addr.	W3:	= undefined

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Format of a Module File

4.4

Output from slang compiler must have the following format:

+0 from slang: sum of chars (SO) +2 from slang: double sum of chars (S1) +4 date of version (e.g.: 820220) +6 ident of version (e.g: 75) +8 first external REF External list: +10 second external REF . . . (module code) (maybe continued external list) last external ref 0 (end external list) ENTRY First instruction of initialization code -> POINT (module code)

```
The module files are named:
```

bos	loaded by FP or s. Contains Central Logic,
	pager and loader.
bterm2	
bterml	
bjobstart	
bjob	Loaded by the loader,
bmount	in the order mentioned.
bread	
bprinter	
bprocs	
bbanker	

PAGER, VIRTUAL CORE

5.



The virtual core is defined as follows:

The addressing is based on core addresses. The process description of BOSS gives the addresses for first and last of BOSS core.

The virtual core is addressed as a continuous extension of the BOSS core.

The address space has the limits: - first addr = 0 (= register WO). - last addr = last addr of disccore.

The address space is split into segments, so an address may be decoded, thus:

ADDRESS	5
15 bits	9 bits

Segm number Relative in segm (0-32767)(0-511)

Cagronsel He 0-4095 pga corefulle formal

5.1 A Virtual Address

An address pointing in the virtual core, i.e. a "core" address > last of BOSS core. But the term may also be used for an address which may be both in BOSS core and virtual core, e.g. the page addresses in the coroutine description.

5.1

5.2 A Page

Ply

Initially a page is a number of consecutive hws, residing somewhere in the virtual core and moved to and from page core by the pager. The hws of a page will always remain together, regardless of where it is placed in page core.

A page is created during initialization by call of "reserve virtual" (and output to virtual core by "move to virtual").

The length of a page is not maintained after reservation, but is implicit known to the code.

The page is identified by the virtual address of first word. This is often stored in an external, to pass the address to other modules.

In this way is created

- codepages
- variable pages
- some of the internal operations (e.g. convert operations)
- work pages

After the creation of a page, it might be used and addressed freely.

For example, a variable page is created during initialization. Somewhere on this page, an operation may be set up. It is signalled to a semaphore by the open chained procedure (the abs address of the operation is set at call, and this is converted to a virtual address by C.L.).

When another coroutine receives this operation, the address is placed as page2 of the coroutine (so now only the operation is handled as a page). The receiving coroutine may have no knowledge or interest in the start address of the original page. A section is a number of consecutive segments in virtual core. Sections are what the pager is actually paging (and sometimes the term page is also used for a section).

A section contains an integral number of segments in virtual core. The segments of a section remain consecutive when moved to page core.

A section contains one or several pages as defined to "reserve virtual"). A "big" page (size \geq 512 hws), will occupy one section alone, but otherwise it will probably be placed on a section together with other "small" pages.

Example:



5.4 Use of BOSS Core

The core store of BOSS is divided into the following parts:

- 1) A part containing the code for the central logic and the pager coroutine. This part is of fixed size and resident in core.
- A part containing buffers, coroutine descriptions, semaphores and tables of various kinds. This part is resident in core but the size of it varies with the installation.

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3) A part containing the processes created by BOSS and the currently and most recently used pages from the virtual core of BOSS. The size of this part is determined by the size of the process in which BOSS is operating. The contents of this part of the core will change dynamically.

This gives the following layout of the core store of BOSS (cf. section 4.1):

1)	Central Logic	
	and	
	Pager Coroutine	
3)	Job Processes	
	and	
	Pages from the	
	Virtual Store	
2)	Coroutine Descriptions	
× .	Semaphores	
	Buffers	
	Tables etc.	

The jobs running under BOSS are divided into two groups: a-jobs and b-jobs. The jobs may have different size and the core place is determined by the fact that a-jobs always start at the lower end and b-jobs always end at the higher end of the exchangeable part of BOSS (3).

This exchangeable part may at different times have the following contents:

no jobs in it
one a-job only
one b-job only
one a-job and one b-job.

In any case the remaining core place will be used for pages from the virtual core of BOSS.



5.5 Working Cycle of the Pager

The pager receives operations in the pager queue. An operation to the pager consists of a coroutine description for a coroutine which wants to be put in the active queue but needs one or more pages from the virtual core.

Coroutines are put into the pager queue by the central logic which checks the presence of all specified pages of a coroutine each time it requests to operate on them (e.g. before exit to a coroutine, at lock, lock chained and open chained operations).

The pager builds up a page information list and two chains of pages, i.e. pages_in_core and pages_not_in_core.

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Relations to Core Table and Segment Table

 BOSS CORE
 DRUM CORE
 DISC CORE

 PAGE CORE
 VIRTUAL CORE

 one
 virtual core

 one
 segment

 one
 segments)

 CORE TABLE
 SEGMENT TABLE

 Image: the section of the segment table (i.e. where the section came from).

See descriptions of coretable and segment table in subsections 2.1.1 and 2.1.2.

The relations can be illustrated by this example:

54

6.

The following pages give a quick survey of record types, format and contents.

55

In most modules, the term "kind" is used for what is called "record type" here.

SURVEY (DF	TESTOUTPUT	RECORD	TYPES
----------	-----------	------------	--------	-------

ľ		TEST	
TYPE	Ξ	BIT	SOURCE: CONTENTS, DESCRIPTION
15	SEND	1	CL: MESS, at send and wait etc.
2 I	DCK	1	CL: SEM, at lock (simple or chained).
3 0	DPCH	1	CL: SEM+OP, at open chained.
4 0	OPEN	1	CL: SEM, at open simple.
5 E	XIT	1	CL: PAGE-ADDR+REL.EXIT, at exit to code page.
			After: all except open.
6 M	ÆSS	1	CL: MESSAGE, after: message ready for coroutine.
7 A	ANSW	1	CL: ANSWER, after: answer arrived to coroutine.
8 J	ד–ת	2	CL: WO, W1, W2, W3, at jd-1 call.
95	STOP	(–)	CL: WO-W3, IC, BF etc, at BOSSFAULT + CLOSE.
10 C	OPER	1	CL: SEM+OP, after: lock chained (just before exit).
	11	4	BANKER: PSJOB+FREE RESS, REC1: psjob a+b,
Į			REC2-REC5: free ress.
	12		JOBSTART: jobfile page, jobdescr page
			PSJOB FINIS: timer values.
13 I	LOAD	(-)	START UP: RECl:install.name, REC2: startup params,
			REC3-REC20: from loader.
14 E	EXTN	(-)	START UP: External table, after second loaderpass.
15 I	LINE	2	COMMAND/JOBSTART: line, from terminal or jobfile.
	16	2	BS ADJUST: BS CLAIMS.
	17		JOB TERM/LOGOUT: CAT.ENTRY, when it is removed.
	18		MOUNTER: STATION TABLE, at each activation of
			mounter.
	19		MOUNTER: TAPE TABLE, at each activation of mounter.
	20		PREP./TERM. ACCESS: ENTRY, ACCESS COUNT, output at
			call.
	21		: AREA PROCESS.
22 I	DUMP		CL: CODE DUMP. Primary storage of code leading to
			interrupt. At BOSSFAULT+CLOSE.
23 F	REQU		REQ.DISPL: REQUEST OPERATION, output when received.
	24		START UP: LOAD RESULT. End of loader, end of fixed
			part of testoutput.
25 E	PSJ1		BANKER: PSJOB descr. part 1.
26 E	PSJ2		BANKER: PSJOB descr. part 2.
27 E	PSJ3		BANKER: PSJOB descr. part 3.
28 F	PSJ4		BANKER: PSJOB descr. part 4.

SURVEY OF TESTOUTPUT RECORD TYPES (continued)

1	TEST	
TYPE	BIT	SOURCE: CONTENTS, DESCRIPTION
29		LOOKUP HOST: call params.
30		LOOKUP HOST: return params.
31		LOOKUP DEVICE, LINK-UP REMOTE: call params.
32		LOOKUP DEVICE, LINK-UP REMOTE: message to host.
33		LOOKUP DEVICE, LINK-UP REMOTE: data to host.
34		ALL HOST PROCEDURES: answer from host.
35		ALL HOST PROCEDURES: data from host.
36		LOOKUP DEVICE, LINK-UP REMOTE: return params.
		·

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Format of testoutput records

Types: 1-2-3-4-5-6-7-8



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	1 m 14 . w 15 . w 16 . w 17 . w 18 .		E too high SUSPENDENTRIES SLICES FREE FREE (WINNY) TEMP DR.UM 1 2	-JOBSTART : Tob description pays on 23 consecutive me cords.	- JOBSTART: Job fill page on 23 consculint records,	ston page	
	w10 , w11 , w12 , w13	rds	1,2,3). All unlues our REE STATIC RESOURC RECOUNT NESS AREA LINTER- PRESS BROCS NALS	(10 WORDE)	(10 WORDS)	- PSJOE Livis Part of <u>Job derori</u> Containing my on m	
	1 m m m m m m m m m m m m m m m m m m m	- PSJOB, FIRE RESOU Produced by the B 5 consecution reco	4 MCOTTS (400 CEUSO) C RESOURCES: FR IS STD REMOTE CONVER A READERS OPERIS OPERIS 0	DESCRIPTION PAGE	FILE PAGE	ESCRIPTION PAGE (6 4000)	
TAIL	31 WH 1 W5 1 W6	$ \begin{array}{c c} \mathbf{F} \mathbf{F} \mathbf{S} \mathbf{J} 0 \mathbf{E} & \mathbf{P} \mathbf{S} \mathbf{J} 0 \mathbf{E} \\ \mathbf{N} \mathbf{\Gamma} \mathbf{R} \mathbf{E} \mathbf{L} & \mathbf{R} \mathbf{E} \mathbf{L} \\ \mathbf{A} - \mathbf{J} 0 \mathbf{E} & \mathbf{E} - \mathbf{J} 0 \mathbf{E} \\ (\mathbf{o} \mathbf{T} = 0) & (\mathbf{o} \mathbf{I} = 0) \end{array} $	TT. FREE DYNAMI	H PART OF JOE	T PART OF JOB	T PART OF JOE DI	
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	T WORD		W	2	12	12	
		FIRST	VEXT H Rec			-	

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Types: 11-12

					Types: 1	3-14-15-	-16
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Types: 20-21-22



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Types: 23-24

						Types: 25-26-27-28
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HEN	WORD2	LIME	3 MI	ME	IME	: 771
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Format of testoutput records (continued)



65

Types: 29-30-31-32

w11, w12, w13, w14,	1 2 3 LOOKUP DEVICE IRRELE VANT LINKUP REMOTE: DATA TO HOST. From Procs.	ALL HOST MESSAGES: T ANSWER FROM HOST. FLOW PROCS.	-UNCTION RESULT (12 bit signed mumber)	1: sender stopped 0: OK, function mecuted 1: trouble see durles status 2: dwice reserved by other follost 3: mo ressources at follost 4: mo nessources at dwice lost 5: time out 6: dwice requested with higher priority 4: some link wes present see link Descr. 8: device host withown
1 1 4 10	5T 4	RRELEVAN	<12+ F	5
wt switw	1 2 3 DEVICE NAME IN DEVICE 400	DEVHOST DEV. 4 LINKNO HOST <12+ IDENT T SUB- HOSTVO (8 WORDS)	LINK DESCR. (4 bib mumber)	0: no link 1: timp (remote) link 2: pern Link
ws 1 wb	TIMEOUT LINK <12 + BUFSIZE BUFFERS HWS	H WS CHARS TRANSE TRANSE (DATA) (DATA) (DATA) (DATA)	i <16 +	ol ol nored i rejected i nejected
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word 1 word 1	33 TIME	34 TIME (DEVICE S (8bits)	8 10 10 10 10 10 10 10 10 10 10 10 10 10

TAL

HEAD

Format of testoutput records (continued)

Types: 33-34

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	ALL HOST MESSAGES (1) answ. Alswelf=1) : DATA FROM HOST. FROM PROCS.	LOOKUP DEVICE, LINKUP REMOLE : RETURN PARMETERS FTOM PROCS.			ursuer to the ausurer neart): ausurer neart): = 50+ reserve neart) = 21.) BoSS.
W8, W9, W40, W11, W12, W13, W14	2 1 3 4 JOBHOST JOBHOST P.D.A. EVICE NAME 21NKNO HOST IRR. LINK DEVICE HOST = DEVNO IDENT SUBROC	4 SUB- DEV. MODE LINK LINK HOST HOGT <12+ DEVNOBUFSTE BUFSTEE NO TOENT KIND HWS CHARS	te = 0 Table Address of sub-process sice number of sub-process ted Function Result (MFR) TUS <-12 (cfr. type 34) if MFR < 40 if MFR >40	IFR):	st * 42: nose message (MFR= 40 + rioniky * 45: nese message (MFR= 40 + * 43: neserved * 44: machunction * 44: machunction * 44: machunction * 45: does not exist (* 51: neserved by other - see * 53: process does not exist * 53: process does not exist * 53: process does not exist * 53: process does not exist
HEAD TAIL WORD 1, WORD 2, W 1, W 5, W 6, W 7	35 TIME CORNT. KIND MAX LINK 1 UDENT BUF- BUFSIZE DE FERS CHARS EN	36 TIME CORUT NIA. DEVICE NAME UTA and DEVINO	- If nearly EVICE : both an - If nearly = OK : NTA = Name - If nearly = OK: NTA = Modifie - If nearly = NOTOK: NTA = Modifie DEVNO = STA	MODIFIED FUNCTION RESULT (M	1: trouble, see device status 2: device reserved by other pooloos 3: mo ressources at poblost 4: mo ressources at poblost 5: thueoux 6: device requested with higher p 8: device has permanent Link (ch. 8: device has already a temp (remote) link, which is: 7: reserved by some other proc 7: reserved by some other proc

Types: 35-36

Codepage Identifications

6.1

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Codepage identification is found in the EXIT-record:

central 9: pager

9: pager

tjobstart 11: codepage 1 (job birth, online convert) 12: codepage 2 (load commands, set claim) 13: codepage 3 (command reading) 14: codepage 4 (create job, request alarm) 15: codepage 5 (load, rb-comm)

tterm 1tprinter20: command input70: paper description21: commandio bulk file71: central page22: command print72: triangle page23: snapshot, autoline73: main loop

tjobtprocs40: psjob I/O80: account41: psjob aux81: bsadjust42: psjob break82: init from usercat43: clean catalog83: unknown sender44: psjob finis84: various procedures for remote
devices

tmount tbanker 50: psmount 90: main banker 91: core allocation 52: remoter 54: rewinder 92: resource allocation 56: watchdog 58: comandio, name, label tterm 2 100: kill 101: go, run, job, newjob tread 60: tape reader 102: rename, clear, scope 103: login, get, save 61: card reader 62: start card 104: transmit 63: pstape 105: display 106: kit changer 64: pscard 107: term out 65: psload

108: request display

Coroutine Identifications

6.2

The coroutine identification is found in 3rd word in most testoutput records. 6.2

ident	name	(init-code in)
0	output during start-up	(central)
1	timer	(banker)
2	banker	(banker)
3	unknown sender	(procs)
7	watchdog	(mount) unexercised (Danky
8	request display	(term2)
9	pager	(central)
10	operator display	(term2)
20*	kit changers	(term2)
40*	printers	(printer)
60*	rewinders	(mount)
80*	remoters	(mount)
100*	commandios	(terml)
(198	downer)	(banker)
199	convert	(banker)
200	account job	(jobstart)
200*	psjobs	(jobstart)
300*	termouts	(term2)
400*	card readers	(reader)
450*	readers	(reader)

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Relation between coroutine numbers of psjobs and commandios:


RETURN LETTER

Title: BOSS, Basic Internal Formats Maintenance Manual

RCSL No.: 31-D673

A/S Regnecentralen af 1979/RC Computer A/S maintains a continual effort to improve the quality and usefulness of its publications. To do this effectively we need user feedback, your critical evaluation of this manual.

Please comment on this manual's completeness, accuracy, organization, usability, and readability:

Do you find errors in this manual? If so, specify by page.

How can this manual be improved?

Other comments?

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Fold here

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Do not tear - Fold here and staple

Affix postage here . .



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