

RCSL: 51-VB489

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Edited: July 1969

RC 4000 PERIPHERAL DEVICES

DOT401, 402, 403 AND 404 DIGITAL OUTPUT TERMINAL

PRELIMINARY SPECIFICATIONS

ABSTRACT:

This report describes the logic structure of the Digital Output Terminal DOT401, 402, 403 and 404, when used in connection with the RC 4000 Computer.

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Main Characteristics:

The Digital Output Terminal is designed to apply 24 digital signals to external equipment e.g. actuators, lamps etc. in industrial process control.

The Digital Output Terminal includes a 24 bits buffer register which controls 24 output amplifiers.

For DOT401 and DOT403 the state of the output amplifiers is steady and equal to the actual states of buffer register elements. Updating of the buffer register can take place at any time as determined by the process program in progress as the device always will be in the ready state.

For DOT402 and DOT404 the state of the output amplifiers is equal to the actual states of the buffer register element in a limited time interval after an updating of the buffer register has taken place. During this time interval the device is not ready. When this interval is terminated the device will go to the ready state and all the output amplifiers will go to the 0-state.

The time interval can be selected within the range of 1 ms thru 1000 ms.

Commands:

The contents of the selected working register can be transferred to the Digital Output Terminal by a Write command.

Modifications of the Write command will be ignored as well as the commands Sense, Read and Control.

Interrupt:

The Digital Output Terminal versions DOT402 and DOT404 will generate an interrupt signal when the device becomes ready.

The versions DOT401 and DOT403 will not generate any interrupt signals.