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Title:

COI504 Communication Interface
Technical Manual

Keywords:

COI504, communication interface, RC850.

Abstract:

This manual contains the drawings of the COI504 communication interface.

(16 printed pages)

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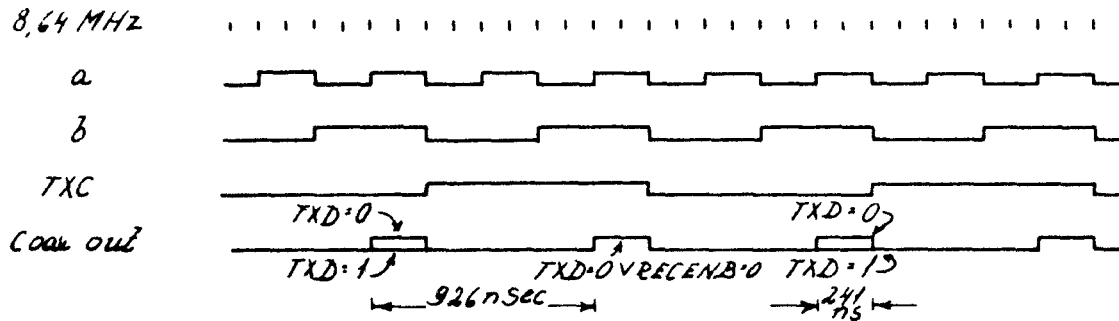
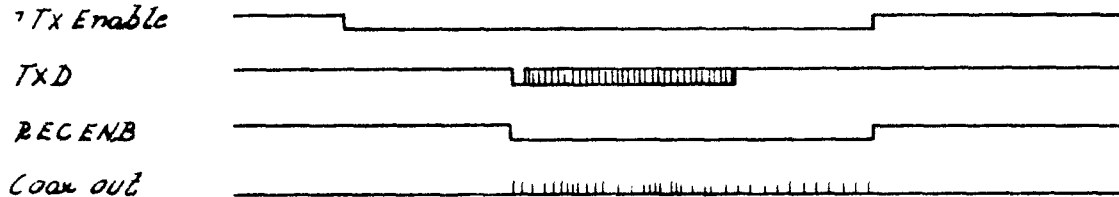
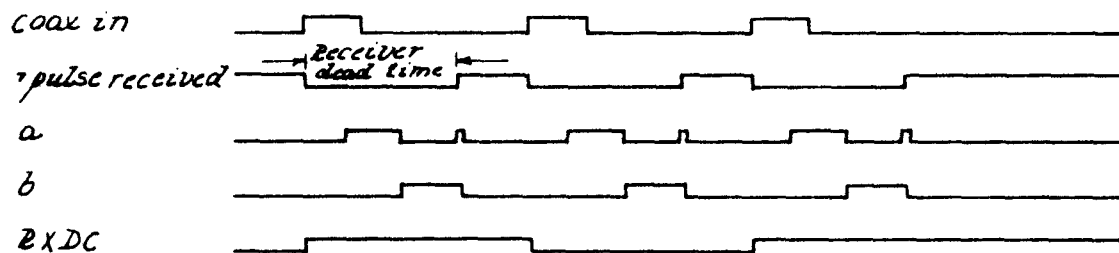
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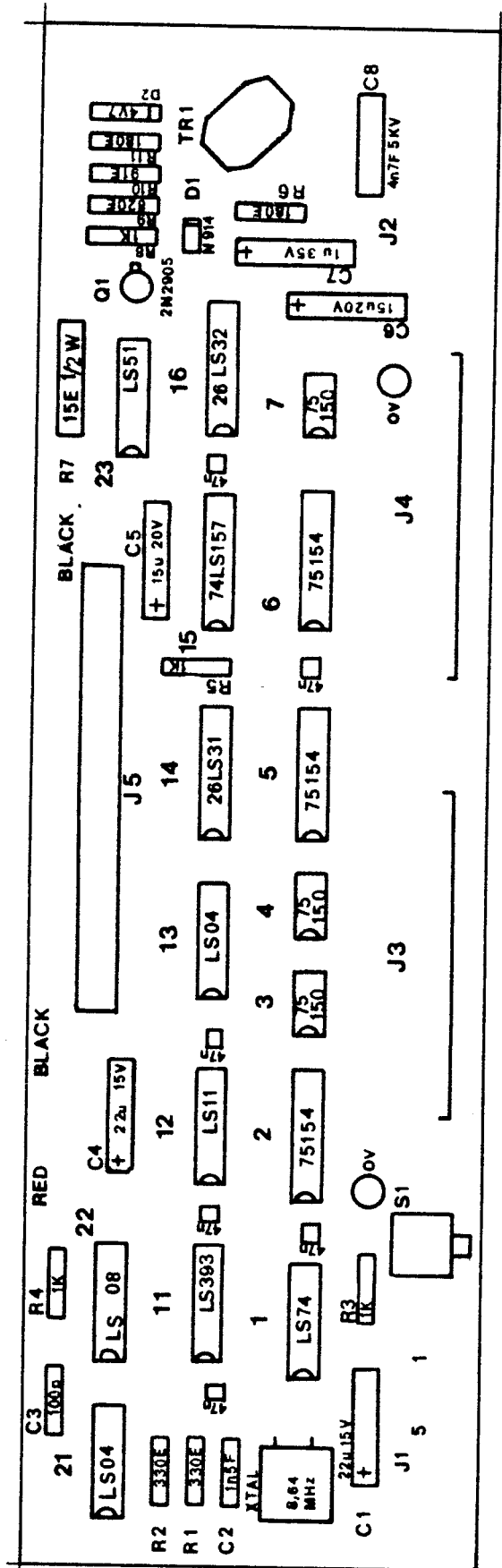
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1. TIMING DIAGRAM

1.

Coax transmitter timing:Coax transmitter enabling:Coax receiver timing:

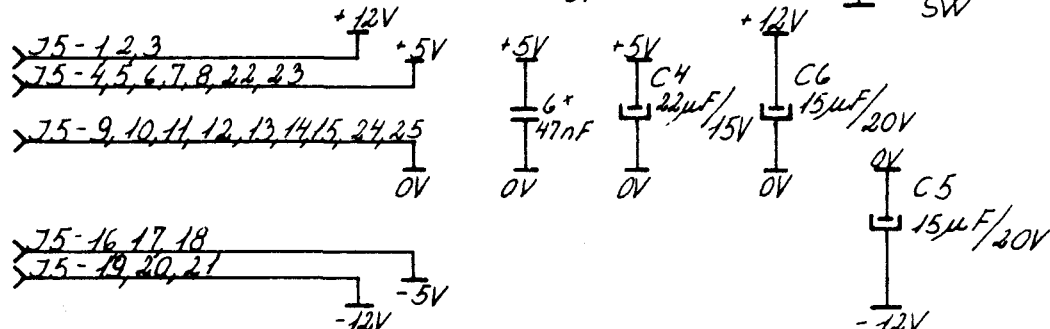
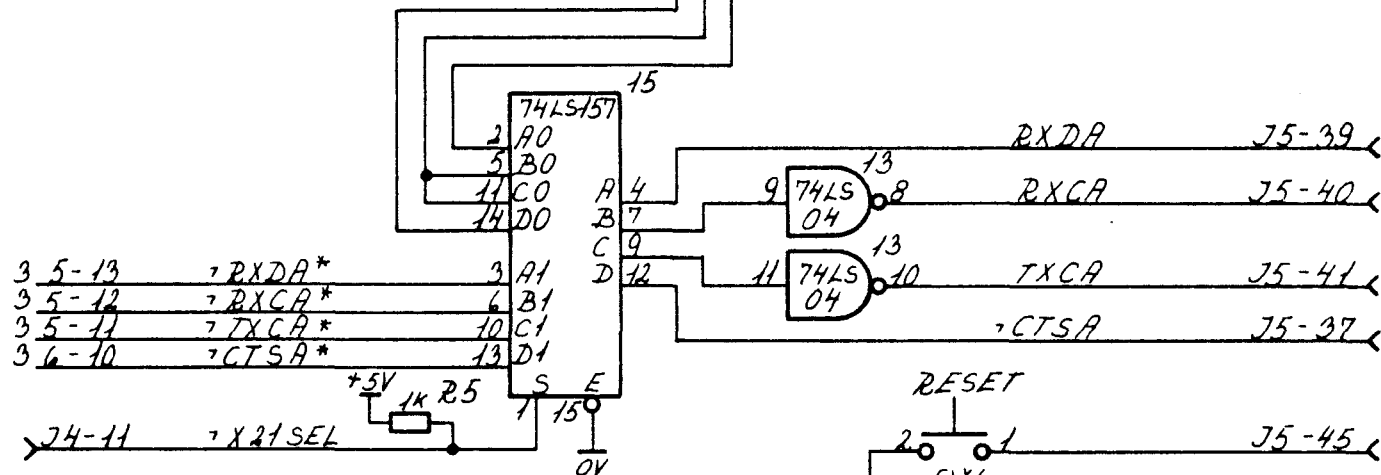
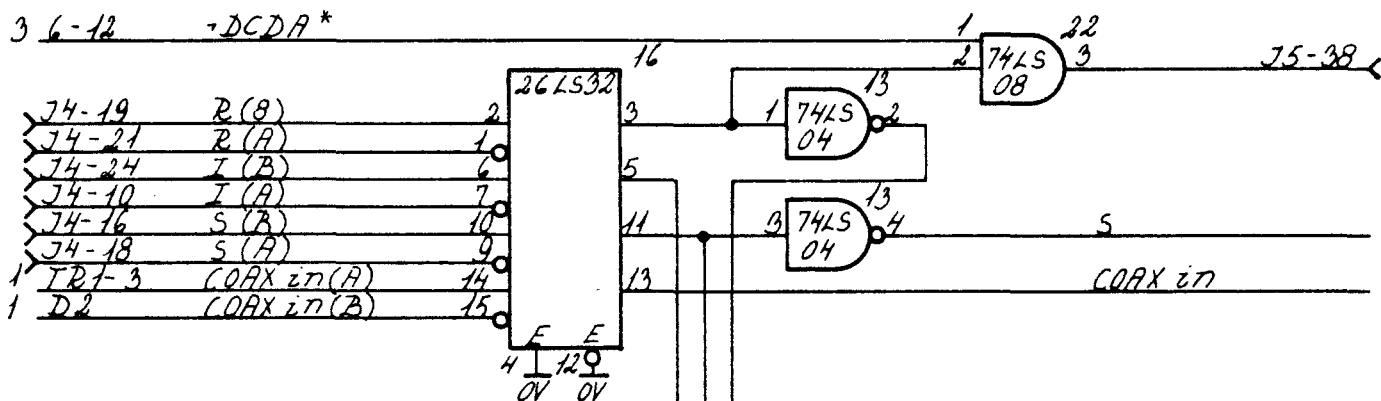
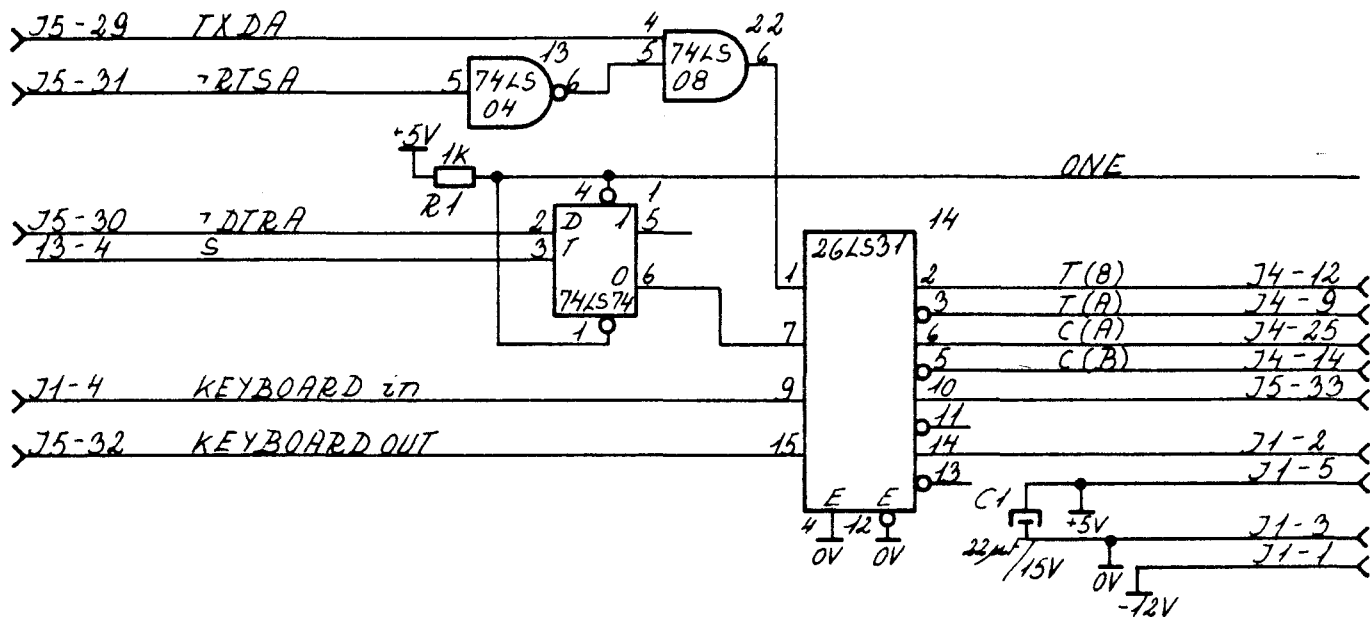


3. LOGIC DIAGRAMS AND FUNCTIONAL DESCRIPTION

3.

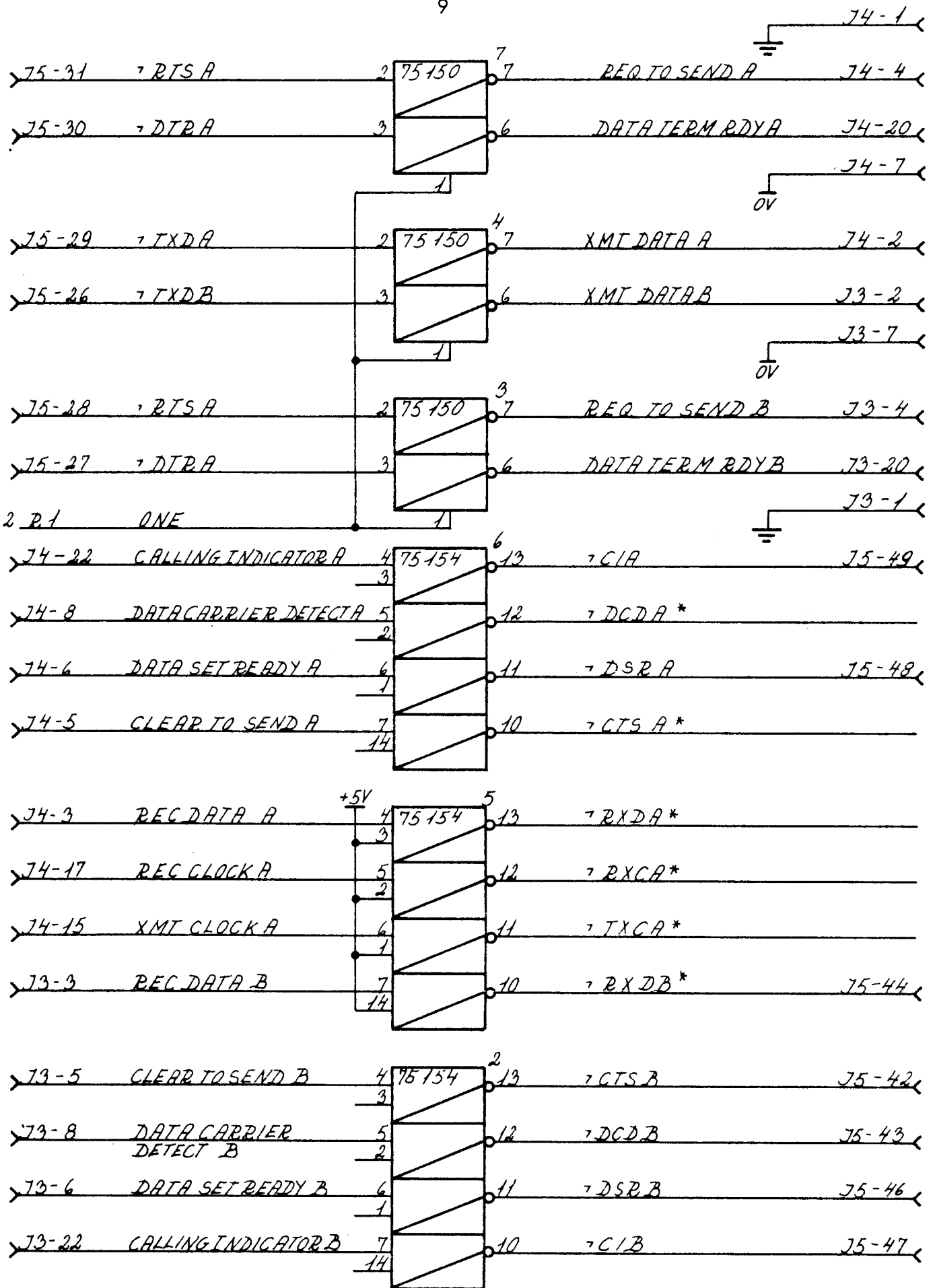
Signal	Destination	Description
8,64 MHz	1	Oscillator output
a	1	4,32 MHz
b	1	2,16 MHz
-,TXC	SIO/MIC board	Transmit clock for Coax 1,08 MHz running, when counter is enabled
CLK ENB	1	Enables clockpulses on coax output
REC ENB	1	Enables CLK ENB when false.
COAX		Connection to coax cable
COAX in(A)	2	Input from coax cable
COAX in(B)	2	
-,PULSE RECEIVED	1	low from leading edge of received pulse until the dead-time has elapsed.
RXDC	SIO/MIC board	toggles on every coaxpulse received.

Signal	Destination	Description
T(B)	j4-12	
T(A)	j4-9	x24/x27 modem signals
C(A)	j4-15	
C(B)	j4-15	
	j5-33	Keyboard received
	j1-2	Keyboard transmitted
	j5-38	Data Carrier Detect
S	2	X24 modem clock
COAX in	1	Coax received
RXDA	j5-39	Received data
RXCA	j5-40	Receiver clock
TXCA	j5-41	Transmitter clock
-,CTSA	j5-37	Clear To Send
	j5-45	Master reset to MIC board.



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Signal	Destination	Description
REQ TO SEND A	j4-4	
DATA TERM RDY A	j4-20	
XMT DATA A	j4-2	V24 modem signals
XMT DATA B	j3-2	
REQ TO SEND B	j3-4	
DATA TERM RDY B	j3-20	
-,CIA	j5-49	Calling indicator
-,DCDA*	2	Data carrier detect char A
-,DSR A	j5-48	Data set ready
-,CTS A*	2	Received data
-,RXDA*	2	Receiver clock
-,TXCA*	2	Transmitter clock
-,RXDB	j5-44	Received data chan. B
-,CTSB	j5-42	Clear to send
-,DCDB	j5-43	Data carrier detect
-,DSRB	j5-46	Data set ready
-,CIB	j5-47	Calling indicator



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