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MON592 - Monitor Technical Manual



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# Abstract:

This manual contains a complete reprint of the OEM service manual applicable to MON592.

(60 printed pages)

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# SERVICE MANUAL

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# TTL DATA DISPLAY 120/150

5-017-1024

REV. A July, 1978



BALL ELECTRONIC DISPLAY DIVISION P.O. BOX 43376 • ST. PAUL, MINNESOTA 55164 • TELEPHONE: (612) 786-8900 • TWX: 910-563-3552



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# PART I

# OPERATING DATA

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This section of the Service Manual provides data concerning the specifications, installation and operation of the TTL120/150 Data Monitor.

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## SECTION 1

### GENERAL INFORMATION

# 1.1 MONITOR DESCRIPTION

The TTL 120/150 Data Monitor is a solid state raster scan display for data terminal application. The Monitor is designed for high quality display of alphanumeric dot characters.

The Data Monitor is available with either a 12 inch or a 15 inch diagonal screen size CRT and each monitor uses a CRT with  $110^{\circ}$  deflection angle which results in a very dense package.

The Data Monitor requires three separate TTL compatible input signals for operation, they are 1) vertical drive, 2) horizontal drive and 3) video. These three separate TTL input signals eliminate costly redundancy associated with the mixing and stripping operation required in a composite (single input) monitor.

The unit is available with the following factory installed options. The TTL compatible skip scan option provides for variable spacing between character rows while using only one scan line per space. The dynamic focus PWA option is available to provide an overall focus improvement from the conventional static focus control. The horizontal frequency can be altered from 15.25kHz to 21kHz. The main PWA is available with or without a brightness control, with or without contrast control and the video termination can be changed.

1.2 CUSTOMER INPUT SIGNAL REQUIREMENTS

1.2.1 PWA Signal Edge Connector (Customer Supplied)

 Viking
 No. 2VK10S/1-2

 Amphenol
 No. 225-21031-101

 Cinch
 No. 250-10-30-170

 Ball EDD
 No. 1-039-0119

 (Refer to Figure 2.1 for wiring detail of this connector)

1.2.2 Video

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#### 1.2.3 Horizontal Drive

Horizontal Frequency:  $(f_H)$ : 15,250Hz min to 21,000Hz max 15,750Hz ±500Hz (Standard) Amplitude: High - 4.0V ±1.5V Low - 0.0V ±0.4V Polarity: Standard - Positive edge triggering Optional - Negative edge triggering Pulse Width  $(t_{SY})$ : 2.5µs min to 30µs max Rise and Fall Time: Less than 100ns (10% to 90%) Horizontal/Video Timing: Refer to Figure 1.1

1.2.4 Vertical Drive

Vertical Frequency (fy): 46Hz min to 63Hz max Amplitude: High - 4.0V ±1.5V Low - 0.0V ±0.4V Polarity: Standard - Negative edge triggering Optional - Positive edge triggering Pulse Width (t<sub>sy</sub>): 75µs min to 1.4ms max Rise and Fall Time: Less than 100ns (10% to 90%) Vertical/Video Time: Refer to Figure 1.2

1.2.5 Skip Scan

Amplitude: High - 4.0V ±1.5V Low - 0.0V ±0.4V Polarity: Skip High Pulse Width (t<sub>SS</sub>): 3μs min to 10μs max Rise and Fall Time: Less than 100ns (10% to 90%) Skip Scan/Horizontal Timing: Refer to Figure 1.3

1.3 INPUT POWER REQUIREMENT

1.3.1 AC Input Power Requirement

1.3.1.1 Input Power Connector

A 9 pin male connector shell (Molex 03-06-1092) with female contacts (Molex 4529) is required to mate with Jl of the AC power supply. Refer to Figure 2.2 for wiring detail of the Jl mating connector.

1.3.1.2 Input Voltage

120V Nominal: 105V RMS min to 130V RMS max 220V Nominal: 198V RMS min to 242V RMS max 240V Nominal: 210V RMS min to 264V RMS max

1.3.1.3 Input Power

Nominal Wattage: 50 watts @ 120VAC, 220VAC or 240VAC Maximum Wattage: 65 watts @ 120VAC, 220VAC or 240VAC

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Figure 1.3 Skip Scan/Horizontal Timing

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1.3.1.4 Input Frequency

49Hz min to 61Hz max

1.3.2 DC Input Power Requirements (Optional)

1.3.2.1 Input Power Connector

The DC input voltage is applied through pin 7 of J101 on the main PWA.

1.3.2.2 Input Voltage

+55VDC ±2%, with line, load and temperature.

1.3.2.3 AC Ripple

100mV p-p max for refresh synchronous with power frequency 35mV p-p max for refresh non-synchronous with power frequency

1.3.2.4 Input Current

Average Current Nominal: 0.5A Average Current Maximum: 0.7A Maximum Peak Current from Horizontal and Vertical AC Components: 1A

1.4 DISPLAY MONITOR ELECTRICAL CHARACTERISTICS

1.4.1 Video Amplifier

Input Impedance: Refer to Figure 1-4



R<sub>IN</sub> (Unterminated) = 5k typ; 3K min C<sub>IN</sub> = 20pF typ; 30pF max

Figure 1.4 Video Amplifier Input Impedance



Input Transfer Function (typ): Refer to Figure 1.5



Figure 1.5 Video Amplifier Input Transfer Function Bandwidth: DC 22MHz @ -3db typ; 20MHz @ -3db min 25V Pulse Response: Refer to Figure 1.6



Figure 1.6 25V Pulse Response



1.4.2 Horizontal Electronics

Input Impedance: Refer to Figure 1.7





Retrace Time: 8.2µs typ; 9.0µs max Horizontal Centering: Adjustment range is equal to \*t<sub>CTRG</sub> = 1.1(R153+R159)C153 which translates to 8µs min to 28µs max

\*t = Time \*CTRG = Centering

1.4.3 Vertical Electronics

Vertical Input Impedance: Refer to Figure 1.8



Figure 1.8 Vertical Input Impedance

Retrace Time: 700µs typ; 820µs max



1.4.4 Skip Scan Electronics

Input Impedance: Refer to Figure 1.9



Figure 1.9 Skip Scan Input Impedance

Vertical Amplifier Delay Plus Settling Time: 60µs max to 90% of final value

# 1.5 CRT DISPLAY CHARACTERISTICS

1.5.1 CRT Resolution (without bonded panel)

	*Resolution	n (TV Lines)
Phosphor	Center	Corner
P4 P39	1000 @ 30fL 1000 @ 15fL	900 @ 30fL 900 @ 15fL

\*Resolution is measured in accordance with EIA RS-375 except Burst Modulation (or depth of modulation) is adjusted for 100%.

# 1.5.2 Geometric Distortion

The perimeter of a full field of characters approaches an ideal rectangle of 4 by 3 aspect ratio to within  $\pm 1.5\%$  of rectangle height.

1.5.3 Linearity

Character height or width shall be within 10% of that of any adjacent character and within 20% of that for any character on the screen.

1.5.4 Jitter

Display jitter as a function of magnetic interference from the power transformer is: 60Hz - 0.006 inch p-p max 50Hz - 0.008 inch p-p max

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### 1.6 MECHANICAL SPECIFICATIONS

Table 1-1 and Figure 1.10 lists the mechanical specifications for the TTL-120/150 Data Monitor. For further information, contact our General Sales Offices. They are:

Addison, Illinois (312) 279-7400 Ocean, New Jersey (201) 922-2800 Santa Clara, California (408) 244-1474 Upland, California (714) 985-7110 Lewisville, Texas (214) 436-2383

## 1.7 ENVIRONMENTAL SPECIFICATIONS

	Operating Range	Storage Range
Ambient Temperature	5°C to 55°C	-40°C to 65°C
Humidity (Non-Condensing)	5% to 80%	5% to 90%
Altitude	Up to 10,000Ft. (3048M)	Up to 30,000Ft. (9144M)

# 1.8 RELIABILITY

The calculated MTBF excluding CRT at  $55^{\circ}$ C is 24,500 hours. The calculations are based on MIL 217A assuming the military K Factors are "ground base equipment."

# 1.9 X-RAY RADIATION

The TTL 120/150 Data Monitor complies with the Federal Regulation for Radiation Control as required by the radiation Control for Health and Safety Act of 1968, and as implemented by Title 21, Subchapter J of the Code of Federal Regulation.

These regulations place certain requirements upon manufacturers of products which can emit x-rays under some conditions of operation or failure. This includes CRT data display monitors.

Certification of compliance with radiation regulations is shown by a label attached to each monitor. The user is responsible for labeling his product in a similar fashion or in making the DHEW label easily visible from the outside of the enclosure. The regulations state that "This (certification) information shall be provided in the form of a tag or label permanently affixed or inscribed on such product so as to be legible and readily accessible to view when the product is fully assembled for use..." Each monitor is supplied with an extra label attached to the CRT. The user will remove this label and use it as stated above.

			DIMENSIO	DIMENSION (Inches/Millimeters)	illimeters)		
MODEL		TILT (°)	) A (in/mm)	B	C	Weight (Lbs/Kg)	OUTLINE DWG
TTL-120B TTL-120B	AC AC	0 2 1 2	4.51/115 4.70/119	4.72/120 4.11/104	9.03/229 8.95/227	15/6.8 15/6.8	2-030-0394 3-030-0391
TTL-120A TTL-120A TTL-120A TTL-120A	AC AC AC	0 5 71 <sub>2</sub> 10	4.58/116 4.72/120 4.78/121 4.80/122	2.97/75 2.57/65 2.37/60 2.16/55	9.09/231 9.06/230 9.01/229 8.95/227	14.5/6.6 14.5/6.6 14.5/6.6 14.5/6.6	2-030-0388 2-030-0403 2-030-0406 2-030-0390
TTL-120D	AC	0	4.66/118	5.30/135	9.20/234	15.5/7.0	2-030-0393
TTL-120D	AC	10	4.90/124	4.78/121	9.02/229	15.5/7.0	2-030-0392
TTL-150A TTL-150A TTL-150A	AC AC	0 51 71	5.33/135 5.42/138 5.47/139	2.38/60 1.91/48 1.67/42	10.66/271 10.62/270 10.57/268	18.5/8.4 18.5/8.4 18.5/8.4	2-030-0395 2-030-0389 2-030-0405
TTL-150D TTL-150D	AC	5 0	.5.30/135 5.92/150	5.40/137 4.58/116	10.62/270 11.03/280	19.75/9.0 19.75/9.0	2-030-0387 2-030-0387
NOTES:							
1. 1	For DC models		subtract 4 lbs/1.8kg	kg			
2. D	<b>)imension</b>	is based of	Dimensions based on standard unit with plain face CRT.	t with plain f	ace CRT.		
			Table 1_1	Mechanical St	Snecifications		
			IGUIE I-T				





Figure 1.10 TTL 120,  $0^{\circ}$  and TTL 150 5° Tilt Dimensions

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# 1.10 USER OPERATING CONTROLS

1.10.1 External Controls

The only external control required for operation of the standard TTL 120/150 display unit is a contrast control. The impedance level of this control shall be no greater than  $500\Omega$  to keep frequency response limitations to a minimum.

An option is available where the contrast control can be placed on the PWA.

Another option is available, where the brightness control is removed from the PWA. A remote brightness control supplied by the customer is used and is connected to the PWA via J101.

1.10.2 Internal Controls

The following controls are mounted on the main PWA:

Width Horizontal Video Centering Vertical Sync Vertical Linearity Height Skip Height (Optional) Internal Contrast (Optional) Brightness Focus Dynamic Focus (Located on Dynamic Focus PWA)



Section 2

#### INSTALLATION

#### 2.1 MECHANICAL

Refer to Table 2-1 for the Display Monitor mounting data and to Section 1.6 for its mechanical specifications.

MODEL	MOUNTING DATA
TTL120B TTL120A	4 Clinch Nuts 4 .188X.500 radius end slots
TTL120D	4 .218X.750 radius end slots
TTL150A	6 .188X.500 radius end slots

Table 2-1 Mounting Data

# 2.2 ELECTRICAL

2.2.1 AC Input

The TTL120/150 Data Monitor with a internal AC power supply operates from either a 120VAC or 220/240VAC source depending on the wiring of the AC mating connector (customer supplied) for J1. Refer to Figure 2.2 for wiring data of J1 mating connector.

2.2.2 DC Input

The TTL120/150 Display Monitor without an internal AC power supply is operated from an external +55VDC source. The DC input voltage of +55V is applied to pin 7 of J101.

# 2.3 LOCATION

The Display Monitor shall not be located in an area such that the operating ambient temperature exceeds +55 °C.

# 2.4 GROUND TECHNIQUES

The Display Monitor is normally mounted in a system where the frame and chassis plate is an integral part of the system ground. The vertical/video ground and horizontal ground (located on pins 1 and 10 of J101) are tied to the chassis plate through a #6 tapping screw located at one corner of the PWA. Ground connections to the Display Monitor can then be made through the chassis mounting plate or via connections to pins 1 and 10 of J101. Refer to Figure 2.1 for Interface Connections.

#### 2.5 VIDEO LEAD IN CONSIDERATIONS

For the best display, the video input line requires special consideration.



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Figure 2.1 PWA Interface Connection and Signal Output





Figure 2.2 Wiring Data for J1 Mating Connector

1. To avoid capacitive coupling from adjacent lines, the video leads should be routed away from all other wiring and the horizontal sync width should not extend into the active video area (Refer to Figure 1.1).

If this is not possible, a shielded or twisted pair video lead may become necessary.

2. The video line should be kept as short as possible in order to minimize line capacitance and the bandwidth limitation this capacitance imposes. Unfortunately low line capacitance is contradictory to a shielded or twisted pair line mentioned in item 1 above. To resolve this contradiction, a terminated system may have to be used, where by the video line is terminated in the characteristic impedance of the shielded (coaxial) or twisted pair line generally  $50 \le Z_0 \le 120\Omega$ . This termination resistor is available on the Main PWA as an optional component.

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## Section 3

# OPERATION

## 3.1 GENERAL

After power, video and drive signals have been applied to the Display Monitor, the contrast and brightness controls are adjusted for the best looking display.

3.2 BRIGHTNESS ADJUST

The brightness control is used to adjust the raster just beyond visual cutoff; in this manner, maximum contrast ratio is obtained when video is applied.

3.3 CONTRAST ADJUST

The contrast control is used to vary the data brightness level as dictated by ambient lighting conditions.



# PART II

# SERVICE DATA

Section 4 through 6 and the supplement are for qualified service personnel.

The TTL 120/150 has no end user serviceable parts inside. Refer service to qualified service personnel.



#### Section 4

## THEORY OF OPERATION '

#### 4.1 VIDEO AMPLIFIER

Video amplification is provided by Q101 and Q102. Q101 and Q102 are connected in a cascode arrangement; Q102 operates as an emitter follower and Q101 operates in the common base configuration. This minimizes the Miller effect input capacitance and the defining breakdown parameter for Q101 becomes  $B_VCBO$  as opposed to  $B_VCEO$  for the common emitter configuration. This enables selection of a higher speed/lower breakdown transistor to be used in the video amplifier.

The video amplifier is normally off. That is, in the absence of a video input signal or with a signal level of less than 0.6V at J101-8, the CRT cathode voltage at the collector of Q101 will be at +55V. When the input signal exceeds 0.6V, the amplifier begins operation in the linear region and the CRT cathode voltage starts to decrease with a nominal voltage gain of 35 V/V.

The dynamic range capability of the amplifier is 48V which provides more than adequate head room to meet the normal viewing brightness requirement of 18 to 30 volts of modulation. Emitter degeneration resistor R106 stabilizes the gain against hfe variations and  $V_{\rm BE}$  temperature variations. High frequency peaking is provided by C104 and L101 resulting in a nominal bandwidth of 22MHz. CR101 provides arc protection for Q101 by clamping the collector voltage to a maximum value of 55V.

Optional resistors R102, R103 and R174 (Refer to Figure 4.1) are used to tailor the video amplifier characteristics to meet certain performance requirements. R102 is a termination resistor used for those applications where the frequency limiting effects of the input cable capacitance must be minimized. R103 is a contrast control which can be provided as an internal adjustment while R174 is used to convert the video amplifier to a normally on Class A amplifier. R174 eliminates the initial 0.6V dead zone which normally exists and modifies the amplifier transfer characteristic (Refer to Figure 1.5) for compatibility with competitive monitors with Class A video amplifiers.

## 4.2 VERTICAL ELECTRONICS

The vertical electronics circuit consists of a positive sync inverter, a vertical oscillator, an emitter follower, a vertical output amplifier and the vertical deflection coil of the yoke. Refer to figure 4.2.

Q105 is a programmable unijunction transistor and operates as a relaxation sawtooth oscillator. The free running frequency range of 40Hz to 65Hz is governed by R115, C109, C110 and the voltage at the gate of Q105. CR102 provides temperature compensation for the free running frequency. Q105 operates like a switch; in the off condition, a ramp voltage is produced at the anode of Q105 (TP104). This ramp voltage will increase until the anode voltage exceeds the gate voltage and turns Q105 on to rapidly discharge the voltage across C109 and



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C110 through L102. The decay time is governed by the resonant frequency of the tuned circuit consisting of L102, C109 and C110.

The oscillator is synchronized by the application of a negative pulse at the gate of Q105. Negative sync at J101-9 is standard but positive sync can be accommodated with inverter Q103 by removing the negative sync jumper wire between R109 and C107 and adding the positive jumper wire between the collector of Q103 and C107. The negative sync pulse lowers the gate voltage and forces the oscillator to run at the incoming frequency as long as the vertical sync control R113 is adjusted to produce a free running frequency slightly lower than the incoming frequency.

Height control R116 varies the amplitude of the sawtooth voltage and subsequent raster height by controlling the effective B+ voltage applied to R115. The B+ voltage is established through the voltage divider action between R111, R112, R113, R116 and R119.

If a true sawtooth current waveform (linear ramp) is supplied to the yoke, the displayed video would be stretched at the top and bottom of the CRT because of the large difference between the faceplate radius curvature and the yoke center of deflection. As such, the sawtooth waveform must be modified to produce the classic "S" shaped yoke current waveform as shown in Figure 4.3.



Figure 4.3 Yoke Current Waveform

The required "S" shaping is produced by linearity electronics Q106, R117, R118 and C110 in combination with the low frequency response limitation of the vertical amplifier. The linearity electronics modifies the sawtooth waveform at the emitter of Q106 by integrating this waveform through R117, R118 and C110 and inserting the resultant parabola in series with the ramp. Q106 is a darlington emitter follower which provides excellent isolation between the vertical oscillator and output amplifier.

The output amplifier consists of Q106 through Q110 and chassis mounted transistors Q1 and Q2. As indicated by Figure 4.4, the amplifier is of the closed loop variety which makes the input to output transfer function independent of the transistor parameters. The Class AB push-pull output stage eliminates the requirement for an output transformer or choke which is required for a Class A output amplifier. If one neglects the finite open loop gain, the input to

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output transfer function (voltage at the base of Q108 to the yoke current) is defined by:

 $\frac{I_{out}}{E_{IN}} = \frac{R125}{R127} X \frac{1}{R135}$ 

This insures that the output yoke current becomes an exact replica of the input ramp voltage.

A voltage replica of the yoke current is produced across R135. This voltage is fed back through C113 to the base of Q107. Q107 together with Q108 comprises a high gain error amplifier that amplifies the voltage difference between the two bases. The amplified voltage difference produces the necessary base current for emitter follower Q109. The base current requirement is a function of the current gain (hfe) of Q109, Q1, Q2 and the output yoke current. Q1 and Q2 comprise a class AB emitter follower output stage. Q1 supplies current during the first half of vertical scan (CRT screen top to center) with Q2 supplying current during the last half of scan (CRT screen center to bottom). During vertical retrace, the yoke current is quickly reversed when the peak yoke current is transferred from Q2 to Q1. The retrace time is governed by the flyback voltage which is defined by the natural clamping action of the amplifier at +52V (TP112). Q110 is a current source of sufficient magnitude to handle the peak base current requirement of Q2. The class A bias to eliminate crossover distortion is provided by CR103 and R130. R121, R122, R123, R124 and R133 are DC biasing resistors to insure the amplifier operates within the linear region.

Optional skip scan electronics Q104, U101, C105, C108 and VR102 provides the ability to step the vertical amplifier. Q104 operates as a normally off switch. With the application of a positive pulse at J101-5, Q104 turns on and the collector voltage jumps to 22V as defined by zener VR102. The collector voltage and the 7.5K, 100K resistors in U101 combine to charge C109 and C110 for the duration of the input pulse (as defined in section 1.2.5). The resultant voltage step across C109 and C110 (TP104) is processed by the vertical amplifier to produce a corresponding jump in yoke current. For CRT deflection jumps of <0.16", the settling time of the vertical amplifier is  $<50\mu$ s. Capacitor C108 couples the ramp waveform back to Q104 to provide linearity correction to the skip scan waveform.

#### 4.3 HORIZONTAL ELECTRONICS

#### 4.3.1 Horizontal Deflection

Integrated circuits U102 and U103 are used as one shot delays to perform 2 functions: 1) U102 and one half U103 provide an adjustable one horizontal line sync delay which permits the video to be centered horizontally within the raster and 2) the other half of U103 generates the proper duty cycle for the horizontal output transistor.

The standard main PWA is wired for a positive sync signal applied at J101-6. The input circuit can be modified to accept a negative sync signal at J101-6 by removing the positive sync jumper wire between the collector of Q112 and C126 and inserting a jumper wire connecting R145 to C126.

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The positive sync pulse is inverted by Q112 and applied to pin U102-2 to produce a positive going pulse at pin U102-3. The time out of this positive pulse is equal to 1.1(R151 C130). After the pulse times out, the negative going edge applied to pin U103-6 produces another positive pulse at pin U103-5. In this case, the pulse duration is variable according to 1.1(R153+R154)C133. This completes the one line sync delay. The negative going edge of this pulse is coupled to pin U103-8 by C134 to produce another positive pulse at pin U103-9 with a time duration equal to 1.1(R158 C136). This provides a duty cycle consistant with proper operation of the horizontal output transistor.

The complete timing relationship is shown in Figure 4.5.



Figure 4.5 Horizontal Timing Diagram of U102 & U103

Horizontal driver Q114 and horizontal output transistor Q3 both operate as switches. The positive pulse at pin U103-9 drives Q114 into saturation. Transformer T101 is phased so that Q3 is turned off during this time. When the primary current of T101 is interrupted by the turn off of Q114, the secondary voltage reverses to turn Q3 on.

The horizontal output stage performs 2 functions: 1) to supply the yoke with the necessary scanning current and 2) develop the CRT anode, G1, G2 and focus voltages. Item 2 will be discussed in section 4.3.2.

Just before horizontal flyback (or when the electron beam moves rapidly from the extreme right side of the CRT screen to the left), maximum yoke current is flowing out of terminal El15 through switch Q3 to ground. Flyback is initiated by the turn off of Q3. With Q3 off, the yoke energy as well as the energy in the flyback primary (T1) is transferred through resonance action to retrace capacitor CJ42, the yoke distributed capacitance and T1 capacitance. When this

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energy transfer is complete, the capacitor voltage (TP120) is maximum (400-600V depending upon line rate), the inductive current is zero and the electron beam has returned to screen center. Now the capacitive energy transfers back to inductive energy to produce a yoke current of the same magnitude but of the opposite direction to that just prior to flyback. This completes beam retrace to the left side of the screen.

The capacitor voltage now swings negative but is clamped near ground by damper diode CR108 which results in the inductive current transferring from the capacitor to CR108. Linear scan now begins as a result of the DC voltage on C143 being impressed across the yoke. C143 is charged to the power supply voltage of +55 VDC and prevents any DC currents from flowing through the yoke.

After the electron beam has moved a distance of about 35% across the screen, Q3 is turned on and the yoke current is transferred from CR108 to Q3. Q3 now carries the yoke current for the remainder of the linear scan time.

As discussed in the vertical section, the desired current waveform for a linear scan on the CRT is an "S" shaped ramp. C143 also provides this "S" shaping by impressing a parabolic voltage across the yoke in addition to a DC voltage.

Additional linearity correction is provided by saturable inductor L104. Without L104, the video would be stretched on the left side of the screen because of the R losses present in the yoke, Q3 etc.

L103 is an adjustable coil placed in series with the horizontal yoke winding to provide width control. The coil inductance controls the voltage magnitude available to the yoke and controls the resultant current magnitude and raster width.

4.3.2 CRT Operating Voltages

The DC operating voltages for the CRT with the exception of the filament voltage are all obtained by rectification and filtering of the horizontal flyback pulse. CR106 and C121 rectify and filter the flyback pulse across Q3 to produce a G2 voltage of 400-600 volts depending upon the horizontal operating frequency. This voltage also feeds the focus grid of the CRT through R141 and focus control R142.

The flyback pulse is transformer coupled by T2, rectified and filtered by CR109 and Cl19 to produce -90V which is used as the source voltage for Gl to control raster brightness. Ql11 aids in eliminating CRT spot burn during power turn off by rapidly discharging the CRT aquadag capacitance.

The CRT anode voltage is developed by T2, rectification diode CR1 and the aquadag filter capacitance. The voltage is typically 17kV for the 15" CRT size and 14kV for the 12" CRT size.

The filament voltage of 6.3V RMS is obtained from a 6 turn winding added to T2.

4.4 X-RAY PROTECTION ELECTRONICS

To comply with the DHEW requirements on X-ray radiation, Q113 and VR103 are


employed to shut down the high voltage in the event the DC supply voltage exceeds 60V DC. If by misadjustment or failure in the regulator electronics, the input voltage should exceed about 60V DC, zener VR108 will start to conduct and turn Q113 on. With Q113 conducting, the supply voltage for U102 and U103 is reduced to zero; this removes the horizontal drive signal and subsequently the high voltage.

## 4.5 DYNAMIC FOCUS ELECTRONICS

For optimum electron beam focusing over the complete CRT face, the focus voltage should increase as the deflection angle increases. As such, the focus voltage would be at a minimum in the CRT center and at a maximum in the corners. The optional dynamic focus electronics approximates this requirement by varying the focus voltage as a function of horizontal position from tube center.

The horizontal waveform across Q3 is processed by the C403, L401, C402 tank network to produce the parabolic waveform (See Figure 5.1). L401 provides for amplitude adjustment over the range of 200V to 400V p-p. C401 couples the waveform to the focus electrode.

# 4.6 LOW VOLTAGE REGULATED SUPPLY

The AC line voltage is applied to transformer T1 (Refer to Figure 4.6). The secondary voltage developed is rectified and filtered by CR302 and C1 to produce a DC voltage for the regulator electronics of 76V DC. The 76V DC input is dropped to +55V DC through the series pass transistor Q4. DC regulation of +55 volts is maintained by tapping down the voltage through the divider network of R311, R312 and R313. The resultant 6.3V DC at the wiper arm of R312 is applied to the base of Q302. Also a DC reference voltage from VR301 is applied to the emitter of Q302. This transistor then develops a DC error current that flows through R310 to the base of emitter follower Q305. Current source Q301 supplies current to the base of Q305 and collector of Q302. This bias current from Q301 will tend to shift the base in the negative direction. This results in an error current from the collector of Q302 that controls emitter follower Q305 and also the series pass transistor Q4. This enables the DC output voltage to be maintained at +55V DC, independent of the input voltage and output load current.

Foldback current limiting of the +55V DC supply is provided by means of transistor Q303 and resistors R306, R307, R308 bias network. When the load current exceeds about 2 amps, the voltage drop across R309 is of sufficient magnitude to turn on Q303. As Q303 turns on, the base current supplied to Q305 decreases to produce a corresponding decrease in output voltage. In the event of a direct short on the +55V DC bus, the short circuit current will be approximately 100ma. Removing the external short circuit will allow the regulator to resume normal operation.





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## Section 5

## ADJUSTMENT AND MAINTENANCE

### 5.1 GENERAL

This section describes the adjustment procedures to be used for set up of the various circuits during routine maintenance or after component replacement.

# 5.2 INITIAL TURN-ON

Connect the signal input connector to J101 of the main PWA and connect the power input connector to J1 of the power transformer T1. Turn on the Display Monitor and the test equipment; allow 15 minutes for warm-up.

All the horizontal and vertical circuits adjustments can be made using an "E" or "#" character. Adjust the raster brightness to just beyond visual extinction with the brightness control.

5.3 POWER SUPPLY ADJUSTMENT

Connect a DC volt meter to the anode of CR104, located near J102-3 on the main PWA. Adjust the B+ adjust control on the LV regulator PWA for a meter reading of  $+55V\pm0.5V$ .

#### 5.4 VERTICAL ELECTRONICS

5.4.1 Vertical Sync Adjust

The Vertical Sync Adjust control is located on the main PWA. Adjust this control clockwise (as viewed from the rear of the Display Monitor) until the picture just rolls down. Now back off the control (CCW) until the picture snaps back into sync.

5.4.2 Vertical Linearity and Height Adjust

Both of these controls are located on the main PWA. The vertical linearity control R118 is adjusted for the best vertical linearity. The vertical height control R116 is adjusted for the proper vertical height.

5.4.3 Skip Scan Adjust

This adjustment procedure will apply only to the Display Monitor that has the skip scan option.

Apply the skip scan signal and adjust control U101 until the proper skip height is obtained. In general, the proper skip height is obtained by removing the skip scan signal and establishing a given video height as described in Section 5.4.2. The skip scan signal is then applied and the height is increased to the final height requirement with control U101. As the increase from initial height





to final height becomes larger there will be interaction between the skip scan control and the vertical sync and linearity controls such that R113 and R118 may have to be readjusted.

5.5 HORIZONTAL ELECTRONICS

5.5.1 Width Adjustment

Adjust the Width Control L103 for the desired picture width.

5.5.2 Horizontal Centering Adjustment

Adjust the Horizontal Centering control R153 to center the video horizontally within the raster.

5.6 RASTER CENTERING

The centering magnets located at the back of the yoke are used to center the raster on the CRT. The magnets should never be used to center the video horizontally as this may cause display focus problems as well as the possibility of neck shadow. The rings may have to be used to center the video vertically since the vertical position of the video within the raster is strictly dependent on customer timing and vertical flyback time. Picture tilt is corrected by rotating the yoke.

5.7 FOCUS

Adjust focus control R142 for the best overall focus on the CRT.

5.8 DYNAMIC FOCUS

Place oscilloscope probe between TP112 and ground. The waveform should look like Figure 5.1.



Figure 5.1 Dynamic Focus Waveform

The voltage level at the bottom portion of the waveform  $(V_1)$  is controlled with the static Focus control R142 while the peak-to-peak amplitude  $(V_{P-P})$  is controlled with L401 on the dynamic focus PWA.

 $V_1$  corresponds to CRT center focus and is adjusted for 25V ±75V to give best center focus while the peak-to-peak amplitude is adjusted for 300V ±100V. This



will give the best focus compromise at the edges of the display pattern. The adjustments do interact in that increasing the Vp-p will lower the V1 voltage and vice versa.

### 5.9 BRIGHTNESS AND CONTRAST

The brightness and contrast controls are adjusted per Section 3 of the manual.

# 5.10 TROUBLESHOOTING GUIDE

#### Symptoms/Possible Solutions

Screen is dark - Check to see if the +55V is present. A convenient point
of measurement is at CR104 on the main PWA. If there is no +55V, disconnect
J104 which isolates the horizontal transistor. If the +55V is still not
present, disconnect J105 which isolates the remainder of the horizontal
output stage. If the +55V now returns to normal, the problem is most likely
the damper diode CR108.

If the +55V is still not present after disconnecting J104 and J105, the problem is either with the +55V power supply or the vertical amplifier. This can be determined by disconnecting J102 and measuring the +55V at the power supply itself. Actual isolation of a fault to a specific transistor stage is best accomplished by the use of an oscilloscope and reference to the typical waveforms in Figure 6.7 in Section 6.

- 2. Loss of Video Check Q101, Q102 and VR101.
- 3. Loss of Brightness Control Check CR109 and CR106.



Unnecessary delays may be avoided when parts are returned to Ball Electronic Display Division using the following procedures:

- (1) Package the unit or part in accordance with the method of shipment. Enclose a list of the material being returned and the reason for returning it.
- (2) Send the unit or part, transportation prepaid, to the address stipulated for returning parts.

All equipment and parts described in the warranty will be replaced, provided our examination discloses that the defects are within the limits of the warranty. If damages or defects are not within the limits of the warranty, the customer will be notified of the extent of repairs required and the cost. The unit will be repaired and returned upon agreement.

#### 6.4 WAVEFORMS

The waveforms on the component layout were taken with 1.5V peak-to-peak character signal (Refer to Section 5.2) applied to the Data Monitor. These waveforms can be used as a check point to localize problems to a specific circuit area. The waveform photographs indicate the actual peak amplitude for each test point.

#### 6.5 BOARD ASSEMBLY

The board assembly part number has a 6-002-XXXX prefix. The last four digits of the part number is found under the "use on 6-002" column in the parts list. The last four digits of the board assembly part number is stamped on the component side of the board.

6.6 COMPONENT REPLACEMENT PARTS AFFECTING PRODUCT SAFETY

Product safety must be considered when any components is replaced in this monitor. The critical components that affect X-radiation are denoted in the shaded areas on the schematics. Components in the shaded area are to be replaced only with Ball Electronic Display Division approved components.

The use of substitute components which do not have the same characteristics as the original components may create excessive X-radiation.

## 6.7 CHASSIS MOUNTED COMPONENT PARTS LIST

SYM	DESCRIPTION	PART NUMBER
C1 CR1	Cap, 1500-100, ER Diode, HV, D0463	1-012-2186 1-021-0463
J1	Connector Shell, 9 Pin Female	1-034-0313
L1	Deflection Coil - Refer to part number on yoke; i.e. 1-023-XXXX	

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	•	Transistor	
Q1		2N4923	78-85-4923
Q2		2N4920	1-015-1169
Q3		B0402 (15,750)	78-85-0402
-	or	B1213 (18,240)	1-015-1213
Q4		DTS410	78-85-0410

# Transformer

Tl Power

T2	High Voltage	Stamp on	Coil	Form; i.e.
	6-003-XXXX -	Refer to	last	four digits
	of part numb	er.		







6109-210-9 ۵ U 10 PWA, TTL-120 5-017-5019 0 0766 R. \* × × \*\*\*\*\*\* \*\*\*\*\* 0N 6 0674 USCD 0673 Bail RM1. JUNE RM1. J  $\begin{array}{c} 70 - 16 - 0564 \\ 70 - 16 - 0822 \\ 70 - 16 - 0822 \\ 70 - 16 - 0103 \\ 70 - 16 - 0101 \\ 70 - 16 - 0101 \\ - 011 - 2738 \\ 1 - 011 - 1935 \\ 1 - 011 - 1935 \\ 70 - 16 - 0102 \\ 1 - 011 - 1395 \\ 70 - 16 - 0102 \\ 70 - 0102 \\$ 1-022-9442 1-029-0445 1-029-0445 1-041-0105 1-045-2210 1-045-2210 1-045-6219 2-948-0110 2-948-0110 5330 1-021-9461 78-15-0753 -017-1/48 LG---ġ ê Ə 03 8 BR8 201--201--MISCELLAREOUS 4.7K 3.3h-1/2-5----470K-1/2-5 3.3h-1/2-5 5.3h-1/2-5 6.8K------HORIZ DRIVER 10K------200 4/2-1/2-5----6.8K-1-5 1.2-2-30---1.2-2-10 TRANSFORMER 29K-29--IK------339-1/2-5 3.3-2-19, 1 1.2-2-10, 1 100------IX 100------NOT USED USED USED Z0753----HOT USED Z0461---ZEJJER 4.7K 56.8 224 564 564 8.2K \*\*\*\*\*\*\*\* ΞŚ Q, В *ч*0 VR101 VR102 VR103 191 0766 × × × × USED DN E-092-0673 0674 0764 0 ××× × ~ × REFER I5-13-14/9 -1-016-0302--1-016-0304 -1-016-0317--1-016-03317-1-016-03317-1-016-0331 -1-015-0330--15-13-1829 -1-039-0148 1-039-0149 1-039-0148 1-015-1205 -1-915-1144-1-915-1144 7.9.6.9759-7.9.6.9759-7.9.16-011.2235-7.9.16-012.2235-7.9.16-012.2235-7.9.16-012.2235-7.9.16-012.7255-7.9.16-0125 7.9.16 -0160 -0110 -0110 -0110 -0110 -0110 -0110 -0110 -0110 -0100 -0100 81/10 1-011-1500 (-039-0, -221--2 PART RES, FIXED CARBON 1/4W±5% -4.70H -560H NIDTH RIDTH------RIDTH------LIREARITY I.LINEARITY 8.20H TRAHSISTORS COLRI, 3 PIN--NOT USED CORR, 3 PIN--COLR, 8 PIN COLR, 3 PIN--DESCRIPTION COARECTOR 283053A 283053A 2830904-----2013994 286027 286027 286027 286027 28604 MPS-406 MPS-400 MPS-406 MPS-406 MPS-400 16K-----2K 15-----510 3-1/2-5---10K 00169-00410-00410-00410-00410-00436-00436-DFODE COLL a, ¥0-1007 CR101 CR105 CR105 CR109 CR105 CR105 CR107 CR108 CR108 L102-L103-105 J102 J103 J105 J106 SYM RF

120 MAIN PWA COMPONENT LAYOUT AND PARTS LIST

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FIGURE

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TTL150 SCHEMATIC



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FIGURE 6.6 COMPONENT LAYOUT LOW VOLTAGE REGULATOR PWA

6-10





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Figure 6.7 Component Layout for Dynamic Focus PWA

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# PART NUMBERS FOR PWA'S WITH THE SKIP SCAN OPTION ARE:

6-002-0738	TTL 150, 15,750 W/BRT
6-002-0768	TTL 150, 18,240 W/BRT
6-002-0769	TTL 150, 15,750 W/O BRT
6-002-0770	TTL 150, 18,240 W/O DRT
6-002-0771	TTL 120, 15,750 W/BRT
6-002-0772	TTL 120, 18,240 W/BRT
6-002-0773	TTL 120, 15,750 W/O BRT
6-002-0774	TTL 120, 18,240 W/O BRT

THE BALANCE OF THE COMPONENT PARTS AND PART NUMBERS CAN BE FOUND ON THE APPROPRIATE COMPONENT LAYOUTS WITHOUT THE SKIP SCAN OPTION; FOR EXAMPLE, BALANCE OF THE PARTS USED ON 6-002-0738 TTL150, 15,750, W/BRT CAN BE LOCATED ON FIGURE 6.4.

#### SKIP SCAN COMPONENT PARTS LIST

REF SYM	DESCRIPTION	PART NUMBER
U101	RES TRIM 190K	1-011-8094
C105	CAP .01uF-500-20, CD	1-012-0740
C108	CAP 10uF-25, ER	1-012-2211
0104	TRANSISTOR MPS-A56	1-015-1171
VR102	ZENER, 20446	1-021-0446

Figure 6.8 Skip Scan Component Layout and Parts List

6-12

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#### WARRANTY

Ball Electronic Display Division certifies that each monitor will be free from defective materials and workmanship for one year from date of shipment to the original customer. The only exception will be the receiving tubes and solid state devices (transistors, diodes, etc.). Receiving tubes will carry a 90 day warranty, and the picture tube will have the standard one year warranty. With solid state devices, we will reflect the manufacturers' warranty.

Ball Electronic Display Division agrees to correct any of the above defects when the monitor is returned to the factory prepaid. Written authorization must be obtained and confirmed in writing by the Customer Service Department before returning the monitor to the factory.

Under this warranty, Ball Electronic Display Division will provide the necessary components required by the customer to correct the monitor in the field. The components will be shipped, prepaid, on a billing memo which will be cancelled upon receipt of the defective components at the factory. When ordering components for repair or replacement, the model number and serial number must be included on the customer request.

This warranty is invalid if the monitor is subject to mis-use, abuse, neglect, accident, improper installation or application, alteration or negligence in use, storage, transportation or handling and where the serial number has been removed, defaced or changed.



# INSTALLATION AND OPERATING MANUAL

MALFUNCTION REPORT

# Dear Customer:

-

We are trying to manufacture the most reliable product possible. You would do us a great courtesy by completing this form should you experience any failures.

Type Unit	Serial No
Module (if applicable)_	
Part failed (Name and Numbe	er)
	ailable)
Approximate hours/days of o	peration to failure
Failure occurred during:	
Final Inspection	Customer Installation Field Use
Personal Comment:	
······	
	Customer
	Address
	Signed
	Date
Ball Electro P.O. Box 43	onic Display Division
	innesota 55164

Telephone 612-786-8900 TWX 910-563-3552

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## LOST OR DAMAGED EQUIPMENT

The goods described on your Packing Slip have been received by the Transportation Company complete and in good condition. If any of the goods called for on this Packing Slip are short or damaged, you must file a claim WITH THE TRANSPORTATION COMPANY FOR THE AMOUNT OF THE DAMAGE AND/OR LOSS.

## IF LOSS OR DAMAGE IS EVIDENT AT TIME OF DELIVERY:

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# IF DAMAGE OR LOSS IS CONCEALED AND DISCOVERED AT A LATER DATE:

If any concealed loss or damage is discovered, notify your local Freight Agent or Express Agent **AT ONCE** and request him to make an inspection. This is absolutely necessary. Unless you do this, the Transportation Company will not consider any claim for loss or damage valid. If the agent refuses to make an inspection, you should draw up an affidavit to the effect that you notified him on a certain date and that he failed to make the necessary inspection.

After you have ascertained the extent of the loss or damage, ORDER THE REPLACEMENT PARTS OF COMPLETE NEW UNITS FROM THE FACTORY. We will ship to you and bill you for the cost. This new invoice will then be a part of your claim for reimbursement from the Transportation Company. This, together with other papers, will properly support your claim.

Remember, it is extremely important that you <u>do not give the Transportation Company a clear receipt if damage or shortages are evident upon delivery</u>. It is equally important that you call for an inspection if the loss or damage is discovered later. DO NOT, UNDER ANY CIRCUMSTANCES, ORDER THE TRANSPORTATION COMPANY TO RETURN SHIPMENT TO OUR FACTORY OR REFUSE SHIPMENT UNTIL WE HAVE AUTHORIZED SUCH RETURN.

### IMPORTANT

### EQUIPMENT RETURN TO BALL ELECTRONIC DISPLAY DIVISION

- 1. Receive return authorization from the plant unless the unit was sent to you upon evaluation or rental.
- 2. Return prepaid.
- 3. Be sure a declared value equal to the price of the unit is shown on the bill of lading, express receipt, or air freight bill, whichever is applicable. This would cover claim for shipping damage on return.

# **RETURN LETTER**

# Title: MON592 - Monitor Technical Manual

RCSL No.: 44-RT2051

A/S Regnecentralen af 1979/RC Computer A/S maintains a continual effort to improve the quality and usefulness of its publications. To do this effectively we need user feedback, your critical evaluation of this manual.

Please comment on this manual's completeness, accuracy, organization, usability, and readability:

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Do you find errors in this manual? If so, specify by page.

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How can this manual be improved?

Other comments?

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Thank you

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