

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT	TOTAL
			1		44
			2		
			3		
			4		
			5		
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			49		

```
*****
OKI TYPED 5230
MICRO LINE 82A

PROGRAM LIST ( 1 )
REV. NO.1
*****
```

***** ROM (2716) *****

CHIP NO. 005 (MEMORY BANK 0)

ROM NO. LYH - 10255

WHITE DOT Q'TY FIVE

LC - 97466 - 1
1981.10

EJECT

LOC OBJECT CODE

SEQ

SOURCE STATEMENT

1
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TOTAL 44

OKI TYPER 5230
MICRO LINE 82A

PROGRAM LIST (1)
REV. NO.1

***** ROM (2716) *****

CHIP NO. 005 (MEMORY BANK 0)

ROM NO. LYH - 10255

WHITE DOT Q'TY FIVE

LC - 97466 - 1

1981.10

EJECT

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT	PAGE
			50	ET - 5230 PROGRAM (MBO)	6
			51	TABLE OF CONTENTS	4
			52	TITLE	6
			53	1. INITIAL RESET (IRT)	7
			54	1. 1 POWER ON	8
			55	1. 2 I/O RESET	4
			56	1. 3 REGISTER CLEAR	7
			57	1. 4 CR HOMING	8
			58	2. INTERRUPT DISTRIBUTOR	4
			59	2. 1 TIMER INTERRUPT DISTRIBUTE	4
			60	2. 2 INTERRUPT ROUTINE TERMINATE	5
			61	3. INPUT SUB ROUTINES	5
			62	3. 1 8155 PC PORT WRITE	5
			63	3. 2 DISP SWITCH READ	5
			64	3. 3 SEL KEY ON MEMORY	6
			65	4. DATA INPUT CONTROL	8
			66	4. 1 DATA WAIT	8
			67	4. 2 MANUAL KEY	11
			68	4. 3 DATA RECEIVE	11
			69	4. 4 INPUT END CONTROL	11
			70	4. 5 BLOCK END CODE CONTROL	12
			71	4. 6 LF AFTER CR CODE	13
			72	4. 7 BUFFER EMPTY CONTROL	14
			73	4. 8 PRINT START CONTROL	15
			74	4. 9 INPUT TO PRINT MODE SHIFT	13
			75	4.10 PRINT CNT PRESET	16
			76	5. PRINT CONTROL SUB ROUTINES	16
			77	5. 1 DATA READ	17
			78	5. 2 165CPI SPPM PHASE CHANGE #1	17
			79	5. 3 165CPI SPPM PHASE CHANGE #2	17
			80	5. 4 TIME DELAY	18
			81	5. 5 SPACE PULSE MOTOR PHASE CHANGE	18
			82	5. 6 LF PULSE MOTOR PHASE CHANGE	19
			83	5. 7 LF PULSE MOTOR OVER DRIVE SET	19
			84	5. 8 LF PULSE MOTOR OVER DRIVE RESET	19
			85	5. 9 PB PORT OF 8155 READ	19
			86	5.10 COLUMN COUNTER SET	19
			87	5.11 LF STEP SET	20
			88		
			89		
			90		
			91		
			92		
			93		
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			98	EJECT	
			99		

ERR LOC OBJECT CODE

SEQ SOURCE STATEMENT

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100 ;
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135 ;

ET - 5230 PROGRAM ( MB 0 )

TITLE

6. PRINT CONTROL
6.1 SPACE & PRINT MOTOR RISE UP
6.1.1 SPACE PULSE MOTOR RISE UP
6.1.2 SPPM PULSE RATE TABLE
6.1.3 SPPM PUASE CONTROL & TIMER SET
6.1.4 DOT HEAD SET CONTROL
6.1.5 COLUMN ( CLM ) CNT & CHARACTER CNT SET
6.1.6 PRINT CONTROL DISTRIBUTE
6.1.7 CHARACTER GENERATOR READ ( GRAPHIC )
6.1.8 DATA READ FROM RAM
6.1.9 CHARACTER GENERATOR READ ( CHARACTER )
6.1.10 PRINT END CONTROL ( SHORT LINE SEEKING )
6.1.11 SP PULSE MOTOR STOP CONTROL
6.1.12 SOFT STOP
6.1.13 HOME DETECTION
6.1.14 CONTROL AFTER SPPM STOP
6.3 LINE FEED CONTROL
6.3.1 LF NUMBER SET ( FF.VFU CONTROL ) *SUBROUTINE
6.3.2 LF CONTROL
6.4 CR END
6.5 TIMER INTERRUPT
6.6 INTERRUPT SEQUENCE END

7. LABEL LIST
8. OBJECT LIST

EJECT

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PAGE
20
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ERR	LOC	OBJECT CODE	SEO	SOURCE STATEMENT
		(0001)	136	***** MODEL NAME ASSIGNMENT *****
			137	
			138	
			139	ET-5230(ML82A).....ML82A : EQU 1
			140	ET-8220A(ML83A).....ML82A : EQU 0
			141	
			142	ML82A: EQU 1
			143	
			144	
			145	TO ODC.....ODC : EQU 1
			146	TO OEE.....ODC : EQU 0
			147	
		(0000)	148	ODC: EQU 0
			149	
			150	
			151	***** JUMP ADDRESS FROM MBO TO MB1
			152	
		(0000)	153	SIF1: EQU ~000
		(0062)	154	SIF2: EQU ~062
		(0088)	155	SIF3: EQU ~088
		(0091)	156	SIF4: EQU ~091
		(0095)	157	SIF5: EQU ~095
		(00DE)	158	CRLF6: EQU ~0DE
		(0100)	159	CS1: EQU ~100
		(0250)	160	CON22: EQU ~250
		(02C8)	161	DTW: EQU ~2C8
		(05DA)	162	VFU15: EQU ~5DA
		(0603)	163	VFU12: EQU ~603
		(0612)	164	VFU16: EQU ~612
			165	
			166	TITLE , ET-5230(ML82A) PROGRAM (MBO) #1-2*
			167	
			168	
			169	
			170	
			171	
			172	
			173	
			174	***** POWER ON *****
			175	
			176	
			177	ORG 0
			178	
		(0000)	179	ANL P2,~FD ; 8155 CS ON
0000	9A	FD	180	MOV RO,~00
0002	B8	00	181	ENTD CLK ; CLOCK OUT FROM TO TERMINAL
0004	75		182	JMP IRT ;TO IRT
0005	04	83	183	
			184	***** INTERRUPT DISTRIBUTER *****
			185	* *

ERR	LOC	OBJECT CODE

SOURCE STATEMENT

DES

新華書店經售

TIMER INTERRUPT DISTRIBUTE *****

(0007)

ORG 7

0007	65
0008	16
000A	0A
000B	D5
000C	B8
000D	28
000E	88
000F	00
0010	18
0011	0A
0012	00
0013	8A
0014	FF
0016	F8
0018	F2
001A	24
001C	D2
001E	1E
0020	82
0022	36
0024	C6
0026	30
0028	64
002A	9C
002C	12
002E	22
0030	C4
0032	D9
0034	C4
0036	D2
0038	0A
003A	D5

0026	12 2A	
0028	E4 BE	
002A	32 2E	
002C	E4 F8	
002E	E4 6A	
0030	35	
0031	04 33	

(0033)

0033 B8 29
0035 F0
0036 3A F0
0037 BA F0
0039 C8
003A F0
003B C5

[illegible]

STOP	TCNT
JTF	\$+2
SEL	RBI
MOV	RO.#~28
MOV	@RO.A
INC	RO
IN	A.P2
MOV	@RO.A
ORL	P2.#~FF
MOV	A.R3
J87	INTR3
J86	INTR1
J85	INTR5
JZ	INTR8
JMP	PRTR
J80	INTR2
JMP	CR2
JMP	CR5T
JMP	TMG
J80	INTR6
JMP	LF
J81	INTR7
JMP	LFED
JMP	LFO
DIS	TCNT1
JMP	IREND

```
*****
***** INTERRUPT ROUTINE TERMINATE *****
*****
```

```

; IREND:
ORG      ~033
MOV      RO, #~29
MOV      A, @RO
OUTL     P2, A
ORL      P2, #~F0
DEC      RO
MOV      A, @RO
SEL      RBO

```

```

;ACC RETIRE
;PORT-2 RETIRE
;R3 INTERRUPT REG.

```

```

; TO SPACE & PRINT CONTROL
;TO CR CONTROL (AFTER CR START)
; TO CR START
; TIME DELAY

```

```

;TO LF CONTROL
;TO LF END CONTROL
;TO LF START CONTROL

```

;P2 RESTORE

;ACC RESTORE

SES

003C 93

RETR

;INTERR. END

```
*****  
***** INPUT SUBROUTINES *****  
*****
```

8155 PC PORT WRITE

(003D)

ARG -03D

003D	B9 03
003F	9A FD
0041	91
0042	8A 02
0044	83

```
PCPW:  MOV R1,#~03
        ANL P2,#~FD
        MOVX @R1,A
        ORL P2,#~02
        RET
```

DIP SWITCH READ

0045	FA	4F
0046	E7	
0047	AA	
0048	19	
0049	F9	
004A	3A	
004B	0A	
004C	F2	
004E	CA	45
004F	E8	FF
0051	8A	
0053	83	

```

; R2=DATA

```

```

DSWR:  MOV     A,R2
      RL      MOV     R2,A
      INC     R1
      MOV     A,R1
      OUTL    P2,A
      IN      A,P2
      JB7     $+3
      DEC     R2
      DJNZ    R0,D
      ORL     P2,#
      RET

```

; RO=READ #

SEL KEY ON MEM *****

0054	9A	F2
0056	0A	64
0057	F2	80
0059	B8	5B
005B	E8	64
005D	0A	FD
005E	F2	43
0060	FD	04
0061	43	

SLKYM:	ANL	P2, #F2
	IN	A P2
	JB7	SLKY1
	MOV	RO, #~80
	DJNZ	RO, \$
	IN	A P2
	JB7	SLKY1
	MOV	A R5
	ORL	A, #~04

TIME DELAY

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
	0063	AD	286	MOV R5,A
	0064	8A OD	287	SLKY1: ORL P2,#~OD
	0066	83	288	RET
			289	;
			290	;
	0067	R0 00	291	TMG6: MOV @R0,#~00 ; WAIT REG CLEAR
	0069	C4 F9	292	JMP PSTP4
			293	;
			294	;
			295	;
			296	*****
			297	* INITIAL RESET (IRT) *
			298	*****
			299	*****
			300	***** IO RESET *****
			301	;
			302	;
			303	;
			304	ORG ~083
			305	INT?
	0083	23 3B	306	MOV A,#~3B
	0085	90 03	307	MOV @R0,A
	0086	B9 03	308	MOV R1,#~03
	0088	23 1D	309	MOV A,#~1D
	008A	91	310	MOV @R1,A
	008B	23 D4	311	MOV A,#~D4
	008D	90	312	MOV @R0,A
	008E	C9	313	DEC R1
	008F	81	314	MOVX A,@R1
	0090	85	315	CLR FO
	0091	F2 94	316	JB7 S+3
	0093	95	317	CPL FO
	0094	23 D6	318	MOV A,#~D6
	0096	90 CC	319	MOV @R0,A
	0097	23 CC	320	MOV A,#~CC
	0099	91	321	MOV @R1,A
	009A	8A 02	322	ORL P2,#~02
	009C	88 08	323	MOV R0,#~08
	009E	B9 F7	324	MOV R1,#~F7
	00A0	8A FF	325	MOV R2,#~FF
	00A2	14 45	326	CALL DSWR
	00A4	FA	327	MOV A,R2
	00A5	72 AA	328	JB3 IRT1
	00A7	F5	329	SEL MB1
	00A8	04 00	330	JMP SIF1
			331	;
			332	;
			333	;
			334	;
			335	***** REGISTER CLEAR *****

; ACK, BUSY, SEL SET

; PC=ALT3 MODE SET

; R1=02

; 8155 PB7 PORT IN

; ALT3, PB OUT SET

;DIP SWITCH READ

; IF PARA. INTERFACE

; TO MB1, SIF1 OF SERIAL INTERFACE

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
	00AA	FB	336	;
	00AB	53 7F	337	;
	00AD	B6 B1	338	IRT1: MOV A,R3
	00AF	43 80	339	ANL A,#~7F
	00B1	AB	340	S+4
	00B2	27	341	ORL A,#~80
	00B3	B8 2F	342	MOV R3,A
	00B5	B9 04	343	CLR A
	00B7	A1	344	MOV R0,#~2F
	00B8	19	345	MOV R1,#~04
	00B9	E8 87	346	MOV @R1,A
	00BB	B9 30	347	INC R1
	00BD	B1 0C	348	MOV R0,S-2
	00BF	00	349	MOV R1,#~30
	00C0	F5	350	MOV @R1,#~0C
	00C1	C4 12	351	NOP
			352	SEL MB1
			353	JMP VFU16
			354	;
	00C3	37	355	IRT2: CPL A
	00C4	92 C7	356	JB4 S+3
	00C6	1D	357	INC R5
	00C7	04 D7	358	JMP IRT3
	00C9	9A F0	359	P2,#~F0
	00CB	0A	360	IN A,P2
	00CC	F2 C3	361	IRT2
	00CE	BD 02	362	MOV R5,#~02
	00D0	B8 75	363	MOV R0,#~75
	00D2	B0 00	364	MOV @R0,#~00
	00D4	18	365	INC R0
	00D5	B0 20	366	MOV @R0,#~20
	00D7	B8 26	367	MOV R0,#~26
	00D9	B0 FF	368	MOV @R0,#~FF
	00DB	8A FF	369	P2,#~FF
			370	;
			371	;
			372	***** CR HOMING *****
			373	;
			374	;
	00DD	D5	375	SEL RB1
	00DE	BB 7F	376	MOV R3,#~7F
	00E0	25	377	EN TCNT1
	00E1	55	378	STRT T
	00E2	85	379	CLR F0
			380	;
			381	*****
			382	*****
			383	* DATA INPUT CONTROL *
			384	*****
			385	*****

; 8155 PB7 IN SET

; TO MB1, VFU16 OF VFU CONTROL

; IF PAPER END, NOT SELECT MODE

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
	00E3	FB	386	;
	00E4	96 E3	387	;
	00E5	C5	388	;
	00E6	23 C7	389	;
	00E7	54 FF	390	;
	00E8	89 04	391	;
	00E9	9A F0	392	;
	00EA	0A	393	;
	00EB	37	394	;
	00EC	F2 EF	395	;
	00ED	37	396	;
	00EE	17	397	;
	00EF	43 80	398	;
	00F0	3A	399	;
	00F1	E9 EF	400	;
	00F2	8A FF	401	;
	00F3	23 C7	402	;
	00F4	54 FF	403	;
	00F5	9A FD	404	;
	00F6	89 03	405	;
	00F7	23 25	406	;
	00F8	35	407	;
	00F9	91	408	;
	00FA	23 05	409	;
	00FB	91	410	;
	00FC	D5	411	;
	00FD	F8	412	;
	00FE	C5	413	;
	00FF	C6 17	414	;
	0100	25	415	;
	0101	16 24	416	;
	0102	8A 02	417	;
	0103	FD	418	;
	0104	32 83	419	;
	0105	FA	420	;
	0106	72 30	421	;
	0107	F5	422	;
	0108	04 62	423	;
	0109		424	;
	010A		425	;
	010B		426	;
	010C		427	;
	010D		428	;
	010E		429	;
	010F		430	;
	0110		431	;
	0111		432	;
	0112		433	;
	0113		434	;
	0114		435	;
	0115		436	;
	0116		437	;
	0117		438	;
	0118		439	;
	0119		440	;
	011A		441	;
	011B		442	;
	011C		443	;
	011D		444	;
	011E		445	;
	011F		446	;
	0120		447	;
	0121		448	;
	0122		449	;
	0123		450	;
	0124		451	;
	0125		452	;
	0126		453	;
	0127		454	;
	0128		455	;
	0129		456	;
	012A		457	;
	012B		458	;
	012C		459	;
	012D		460	;
	012E		461	;
	012F		462	;
	0130		463	;
	0131		464	;
	0132		465	;
	0133		466	;
	0134		467	;
	0135		468	;
	0136		469	;
	0137		470	;
	0138		471	;
	0139		472	;
	013A		473	;
	013B		474	;
	013C		475	;
	013D		476	;
	013E		477	;
	013F		478	;
	0140		479	;
	0141		480	;
	0142		481	;
	0143		482	;
	0144		483	;
	0145		484	;
	0146		485	;
	0147		486	;
	0148		487	;
	0149		488	;
	014A		489	;
	014B		490	;
	014C		491	;
	014D		492	;
	014E		493	;
	014F		494	;
	0150		495	;
	0151		496	;
	0152			

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
	0124	B8 08	436	DWT12: MOV R0,#08
	0126	F0	437	MOV A,@R0
	0127	D3 24	438	XRL A,#~24
	0129	C6 2E	439	JZ \$+5
	0128	23 FF	440	MOV A,#~FF
	012D	62	441	MOV T,A
	012E	24 19	442	JMP DWT2
			443	;
	0130	9A FD	444	DWT11: ANL P2,#~FD
	0132	B9 00	445	MOV R1,#~00
	0134	81	446	MOVX A,@R1
	0135	8A 02	447	ORL P2,#~02
	0137	12 A0	448	JBO DREC
	0139	FE	449	MOV A,R6
	013A	96 7C	450	JNZ BLACK
	013C	FD	451	MOV A,R5
	013D	32 5B	452	PERD
	013F	B8 A0	453	MOV R0,#~A0
	0141	9A F2	454	ANL P2,#~F2
	0143	0A	455	IN A,P2
	0144	8A 0F	456	ORL P2,#~0F
	0146	37	457	CPL A
	0147	F2 8A	458	JB7 SELKY
	0149	FD	459	MOV A,R5
	014A	12 5B	460	PERD
	014C	B8 24	461	MOV R0,#~24
	014E	B9 FF	462	MOV R1,#~FF
	0150	9A F0	463	ANL P2,#~F0
	0152	0A	464	IN A,P2
	0153	8A 0F	465	ORL P2,#~0F
	0155	37	466	CPL A
	0156	F2 91	467	JB7 LFKY
	0158	F5	468	SEL MB1
	0159	A4 DA	469	JMP VFU15
			470	;
	015B	C4 EE	471	PERD: JMP PERD2
	015D	00	472	NOP
	015E	09	473	PERD6: IN A,P1
	015F	37 00	474	CPL A
	0160	FC	475	JB7 DWT3
	0162	96 78	476	MOV A,R4
	0163	99 7F	477	NPE
	0165	99 7F	478	ANL P1,#~7F
	0167	FD	479	MOV A,R5
	0168	53 FC	480	ANL A,#~FC
	016A	AD	481	MOV R5,A
	016B	23 1D	482	ANL A,#~1D
	016D	14 3D	483	MOV PCPW
	016F	FA	484	MOV A,R2
	0170	72 75	485	DWAIT JB3

; IDLE TIME START

; TO DATA RECOGNITION, IF DATA RECEIVED

; IF NOT FIRST DATA

; IF SEL KEY ON

; TO KEY ON

; TO MB1, VFU15 OF VFU CONTROL

; PAPER END SET

; PAPER OVERRIDE

; PAPER END OF I.F. SET

; SEL LOCAL RST

; 8155 PC WRITE

```

*****
***** DATA RECEIVE *****
*****

```

ET-5230(MLR2A) PROGRAM (MBO) #1-2*

OKI META CROSS ASSEMBLER
V2.0 PROCESSOR : I-8048

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
			536	; ;
			537	; ;
		(01A0)	538	ORG ^1A0
			539	; ;
			540	; ;
			541	DREC: MOV A,R5
			542	MOV R5,#~30
			543	JRO \$+4
			544	MOV R5,#~38
			545	XCH A,R5
			546	CALL PCPW
			547	MOV R1,#~01
			548	ANL P2,#~FD
			549	MOVX A,@R1
			550	ORL P2,#~02
			551	CPL A
			552	MOV R7,A
			553	DRC1: MOV
			554	SEL MB1
			555	JMP CS1
			556	; ;
			557	; ;
			558	; ;
			559	; ;
			560	; ;
			561	; ;
			562	; ;
			563	; ;
			564	; ;
			565	; ;
			566	; ;
			567	; ;
			568	ORG ^1B6
			569	MOV A,@R1
			570	INC A
			571	MOV R1,#~88
			572	CRLF
			573	; ;
			574	; ;
			575	MOV R1,#~08
			576	MOV A,@R0
			577	ANL A,#~43
			578	ORL A,R1
			579	MOV @R0,A
			580	MOV R1,#~26
			581	MOV A,@R1
			582	ADD A,#~07
			583	CPL A
			584	MOV R0,#~17
			585	MOV A,@R0
			586	ADD
			587	
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			866	

: LF BLOCK MODE SET

ERR	LOC	OBJECT CODE	SE0	SOURCE STATEMENT
	0245	C9	686	IBED4: DEC R1
	0246	F1	687	MOV A,@R1
	0247	37	688	CPL A
	0248	A1	689	MOV @R1,A
	0249	19	690	INC R1
	024A	F1	691	MOV A,@R1
	024B	12 75	692	PST
	024D	B1 00	693	IBED7: MOV @R1,#~00
	024F	B8 24	694	MOV RO,#~24
	0251	86 55	695	IBED9 JN1
	0253	44 2D	696	JMP IBED1
	0255	F0	697	IBED9: MOV A,@R0
	0256	53 1C	698	ANL A,#~1C
	0258	C6 2D	699	IBED1 JZ
	025A	0A	700	IN A,P2
	025B	37	701	CPL A
	025C	92 2D	702	JB4 IBED1
	025E	B9 26	703	IBED3: MOV R1,#~26
	0260	B1 FF	704	MOV @R1,#~FF
	0262	BE 00	705	MOV R6,#~00
	0264	FD	706	MOV A,R5
	0265	52 6C	707	IBED5 JB2
	0267	FC	708	MOV A,R4
	0268	72 71	709	IBED6 JB3
	026A	24 00	710	JMP DWT3
	026C	53 FA	711	IBED5: ANL A,#~FA
	026E	AD	712	MOV R5,A
	026F	24 6B	713	DWT5
	0271	BC 00	714	IBED6: MOV R4,#~00
	0273	24 67	715	JMP DWT4
			716	IBED5: ANL A,#~FA
			717	MOV R5,A
			718	DWT5
			719	IBED6: MOV R4,#~00
			720	JMP DWT4
			721	IBED5: ANL A,#~FA
			722	MOV R5,A
			723	DWT5
			724	IBED6: MOV R4,#~00
			725	JMP DWT4
			726	IBED5: ANL A,#~FA
			727	MOV R5,A
			728	DWT5
			729	IBED6: MOV R4,#~00
			730	JMP DWT4
			731	IBED5: ANL A,#~FA
			732	MOV R5,A
			733	DWT5
			734	IBED6: MOV R4,#~00
			735	JMP DWT4
				***** PRINT START CONTROL *****
				ORG ~275
				PST: SEL RB1
				MOV RO,#~21
				MOV @RO,#~00
				CALL NOPS
				INC R1
				MOV RO,#~2A
				MOV @RO,#~00
				MOV A,@R1
				JB7 PST3
				SEL RBO
				***** PRINT MODE SHIFT TO PRINT SIDE *****
				IBED7: MOV @R1,#~00
				IBED9 JN1
				JMP IBED1
				IBED3: MOV R1,#~26
				MOV @R1,#~FF
				MOV R6,#~00
				MOV A,R5
				IBED5 JB2
				MOV A,R4
				IBED6 JB3
				JMP DWT3
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6: MOV R4,#~00
				JMP DWT4
				IBED5: ANL A,#~FA
				MOV R5,A
				DWT5
				IBED6

OKI META CROSS ASSEMBLER
V2.0 PROCESSOR : I-8048

ET-5230(ML82A) PROGRAM (MBO) #1-2*

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
	0285	F5	736	SEL MB1
	0286	C4 03	737	JMP VFU12
	0288	D5	738	SEL RB1
	0289	54 A5	739	CALL PRSET
	028B	62	740	MOV T.A
	028C	25	741	EN TCNTI
	028D	55	742	T
	028E	C5	743	SEL RB0
	028F	B6 93	744	S+4
	0291	44 4D	745	IBED7
	0293	B8 24	746	MOV RO.#~24
	0295	F0	747	MOV A.ERO
	0296	52 91	748	PST1
	0298	44 2A	749	JMP CRLF5
	029A	B4 9D	750	
	029C	B8 3F	751	LFST
	029E	74 37	752	MOV R3.#~3F
	02A0	BD 0B	753	CALL LFOVD
	02A2	27	754	MOV R5.#~0B
	02A3	44 8B	755	CLR A
			756	JMP PST4
			757	
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			761	
			762	
			763	
	02A5	BB 01	764	PRSET: MOV R3.#~01
	02A7	74 37	765	CALL LFOVD
	02A9	B8 2C	766	MOV RO.#~2C
	02AB	F0	767	MOV A.ERO
			768	
			769	IF ML82A
			770	
	02AC	RD 07	771	MOV R5.#~07
	02AE	RE FD	772	MOV R6.#~FD
			773	
			774	ELSE
			775	
			776	MOV R5.#~01
			777	MOV R6.#~FB
			778	
			779	ENDIF
			780	
	02B0	RF 06	781	MOV R7.#~06
	02B2	12 BA	782	S+8
			783	
			784	IF ML82A
			785	

***** PRINT CNT PRESET (SUBROUTINE) *****

; TO MB1, VFU12 OF VFU CONTROL

; INTERRUPT ENABLE

; TIMER START

; IF FULL+1 CHR.

; IF AFTER BUFF. FULL PRINT

; IF AFTER FULL+1 INPUT

; LF NUMBER SET

; LF INTR. SET

; LF PM OVERDRIVE SET

; LF TRIGGERED SET

; ET-5230(ML82A)

; CLM CNT SET, 16.5 CPI

; CHR CNT, 16.5 CPI

; ET-8220A(ML83A)

; RISE PLS #, 16.5 CPI

; ET-5230(ML82A)

DATA READ

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K1 META CROSS ASSEMBLER
V2.0 PROCESSOR : I-8048

ET-5230(ML82A) PROGRAM (MBO) *1-2*

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
			886	;
			887	;
	0307	B9 30	888	SPPM: R1,#~30
	0309	F1	889	MOV A,@R1
	030A	47	890	SWAP A
	030B	41	891	ORL A,@R1
	030C	E7	892	RL A
	030D	76 11	893	JF1 SPPM1
	030F	77	894	RR A
	0310	77	895	RR A
	0311	53 OF	896	SPPM1: ANL A,#~OF
	0313	A1	897	MOV @R1,A
	0314	9A FD	898	SPPM2: ANL P2,#~FD
	0316	B8 02	899	MOV R0,#~02
	0318	80	900	MOVX A,@R0
	0319	53 F8	901	ANL A,#~F8
	031B	41	902	ORL A,@R1
	031C	53 FB	903	ANL A,#~FB
	031E	90	904	MOVX @R0,A
	031F	43 8C	905	ORL A,#~8C
	0321	90 OF	906	SPPM3: MOVX @R0,A
	0322	8A OF	907	ORL P2,#~OF
	0324	83	908	RET
			909	;
			910	;
			911	***** SPPM OVD SET & RESET *****
			912	;
	0325	B9 30	913	SPOVD: MOV R1,#~30
	0327	64 14	914	JMP SPPM2
			915	;
			916	;
			917	;
			918	;
			919	;
			920	;
			921	ORG ~329
			922	;
			923	LFPM: CALL PBRD
			924	RL A
	0329	74 41	925	S+4
	032B	E7	926	JB6 A,#~10
	032C	D2 30	927	ORL A,#~30
	032E	43 10	928	ANL R1,#~30
	0330	53 30	929	LFPM1: MOV A,@R1
	0332	B9 30	930	ORL SPPM3
	0334	41	931	JMP
	0335	64 1F	932	;
			933	;
			934	;
			935	;

***** LF PULSE MOTOR PHASE CHANGE *****

***** LF PULSE MOTOR OVERDRIVE SET *****

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
	0337 74 41		936	LFOVD: CALL PBRD
	0339 64 32		937	JMP LFPM1
			938	;
			939	;
			940	;
			941	;
			942	;
			943	;
	0338 74 41		944	LFRST: CALL PBRD
	033D 43 40		945	ORL A,#~40
	033F 64 32		946	JMP LFPM1
			947	;
			948	;
			949	;
			950	;
	0341 88 02		951	PRRD: MOV R0,#~02
	0343 9A FD		952	ANL P2,#~FD
	0345 80		953	MOVX A,@R0
	0346 53 30		954	ANL A,#~30
	0348 83		955	RET
			956	;
			957	;
			958	;
			959	;
			960	;
			961	;
			962	ORG ~349
			963	;
	0349 FD		964	CLMS: MOV A,R5
	034A 76 53		965	JF1 CLM2
	034C CD		966	DEC R5
	034D 96 52		967	JNZ CLM3
	034F RD OR		968	MOV R5,#~0B
	0351 CE		969	DEC R6
	0352 83		970	RET
			971	;
			972	;
	0353 1D		973	CLM2: INC R5
	0354 D3 0B		974	XRL A,#~0B
	0356 96 5A		975	JNZ CLM1
	0358 AD		976	MOV R5,A
	0359 1E		977	INC R6
	035A 83		978	RET
			979	;
			980	;
			981	;
			982	;
			983	;
			984	ORG ~35B
			985	;

***** LF PULSE MOTER OVERDRIVE RESET *****

***** PB PORT OF 8155 READ *****

***** COLUMN COUNTER SET (SUB ROUTINE) *****

***** LF STEP SET (SUBROUTINE) *****

;IF CHR CNT IS NOT 8N WHEN 16.5CPI
; R5=00
;CHR CNT +1

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
	038C	64 78	1036	JMP PREND6
			1037	
			1038	
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			1098	
			1099	
			1100	
			1101	
			1102	
			1103	
			1104	
			1105	
			1106	
			1107	
			1108	
			1109	
			1110	
			1111	
			1112	
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			1120	
			1121	
			1122	
			1123	
			1124	
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			1177	
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			1200	
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			1362	

ERR	LDC	OBJECT CODE	SEQ	SOURCE STATEMENT
03C4	74 38		1086	CALL LFRST
03C6	BC 01		1087	MOV R4,#01
03C8	86 D0		1088	JFO PRT4
03CA	BF 04		1089	MOV R7,#04
03CC	74 49		1090	CALL CLMS
03CE	EF CC		1091	DJNZ R7,PRT3
03D0	04 33		1092	JMP IREND
03D2	23 F3		1093	MOV A,#F3
03D4	76 D8		1094	JF1 \$+4
03D6	23 E7		1095	MOV A,#E7
03D8	64 B8		1096	JMP PRT1
03DA	84 04		1097	JMP PRT6
			1098	;
			1099	;
			1100	;
			1101	;
			1102	;
			1103	;
			1104	ORG ^3DC
			1105	IF ML82A ; ET-5230(ML82A)
			+1 1106	
			+1 1107	
			+1 1108	
			+1 1109	
			+1 1110	
			+1 1111	
			+1 1112	
			+1 1113	
03DC	E9 E8 E6 E3			DB ^E9,^E8,^E6,^E3,^DF,^DA ; 16.5 CPI REV
03E0	DF DA			
03E2	E9 E8 E6 E3			DB ^E9,^E8,^E6,^E3,^DF,^DA ; 16.5 CPI FOM
03E6	DF DA			
03E8	F5 F4 F3 F2			DB ^F5,^F4,^F3,^F2,^F1,^F0,^EF,^EE,^ED,^EC,^EB,^EA ; 10 CPI REV
03EC	F1 F0 EF EE			
03F0	ED EC EB EA			DB ^F6,^F5,^F4,^F3,^F2,^F1,^F0,^EF,^EE,^ED,^EC,^EB
03F4	F6 F5 F4 F3			
03F8	F2 F1 F0 EF			
03FC	EE ED EC EB			
			+1 1117	
			+1 1118	
			+1 1119	
			+1 1120	
			+1 1121	
			+1 1122	
			+1 1123	
			+1 1124	
			+1 1125	
			+1 1126	
			+1 1127	
			+1 1128	
			+1 1129	
				ELSE ; ET-8220A(ML83A)
				*** FOR ET-8220A(ML83A) ***
				DB ^E9,^E8,^E6,^E3,^DF,^DA ; 16.5 CPI REV
				DB ^E9,^E8,^E6,^E3,^DF,^DA ; 16.5 CPI FOM
				DB ^F5,^F4,^F3,^F2,^F1,^F0,^EF,^EE,^ED,^EC,^EB,^EA ; 10 CPI REV
				DB ^F5,^F4,^F3,^F2,^F1,^F0,^EF,^EE,^ED,^EC,^EB,^EA ; 10 CPI FOM

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
		+	+1	ENDIF
			+1	***** SPPM PHASE CONTROL & TIMER SET *****
		(0400)		ORG ~400
				PRPM2: JFO PRPM1
				JMP DOT
	0400	B6 06		
	0402	B4 0A		; IF 10 CPI
	0404	EC 00		
	0406	BC 05		; PHASE CNT SET
	0408	74 07		; SPACE PULSE MOTER PHASE CHANGE
				***** DOT HEAD SET CONTROL *****
				DOT: MOV A,R3
				XRL A,#~07
				JNZ CLM
				MOV RO,#~2A
				MOV A,@RO
				J87 CLM
				J82 DOT3
				J84 CLM
				J81 DOT3
				MOV A,#~F7
				ADD A,R5
				JC CLM
				MOV RO,#~23
				MOV R1,#~77
				MOV A,@RO
				OUTL P1,A
				MOV A,#~FD
				ANL A,@R1
				MOV @R1,A
				OUTL P2,A
				MOV RO,#~02
				MOV A,@RO
				ANL A,#~F7
				MOVX @RO,A
				ORL A,#~08
				MOVX @RO,A
				ORL P2,#~02
				***** COLUMN(CLM) CNT & CHARACTER CNT SET *****
				DOT1: ADD
				JC
				MOV RO,#~23
				MOV R1,#~77
				MOV A,@RO
				OUTL P1,A
				MOV A,#~FD
				ANL A,@R1
				MOV @R1,A
				OUTL P2,A
				MOV RO,#~02
				MOV A,@RO
				ANL A,#~F7
				MOVX @RO,A
				ORL A,#~08
				MOVX @RO,A
				ORL P2,#~02
				***** COLUMN(CLM) CNT & CHARACTER CNT SET *****
	040A	D3 07		
	040B	96 35		; IF NOT PRINT CYCLE
	040D	88 2A		
	040F	F0 35		
	0411	F2 35		
	0412	52 53		
	0414	92 35		
	0416	32 53		
	0418	23 F7		
	041A	6D 35		
	041C	F8 35		
	041D	B8 23		
	041F	89 77		
	0421	F0 35		
	0423	39 FD		
	0424	23 FD		
	0425	51		
	0427	51		
	0428	A1		
	0429	3A 02		
	042A	88 02		
	042C	80		
	042D	53 F7		
	042F	90		
	0430	43 08		
	0432	90		
	0433	8A 02		

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
	046E	B9 2F	1230	PDBT1: MOV R1,#2F
	0470	76 9C	1231	JF1 PDBT4
	0472	C9	1232	DEC R1
	0473	FE	1233	MOV A,R6
	0474	17	1234	INC A
	0475	D1	1235	XRL A,R1
	0476	C6 A0	1236	JZ PDBT5
	0478	19	1237	INC R1
	0479	FE	1238	MOV A,R6
	047A	84 82	1239	JMP PDBT3
			1240	;
			1241	;
	047C	CF	1242	PDBT8: DEC R7
	047D	C4 34	1243	JMP TRL
			1244	;
			1245	;
	047F	C9	1246	PDBT2: DEC R1
	0480	FE	1247	MOV A,R6
	0481	17	1248	INC A
	0482	D1	1249	XRL A,R1
	0483	96 87	1250	JNZ \$+4
	0485	8B 07	1251	MOV R3,#07
			1252	;
	0487	D1	1253	XRL A,R1
	0488	A9	1254	MOV R1,A
			1255	;
	0489	F0	1256	MOV A,R0
	048A	D3 01	1257	XRL A,#01
	048C	A0	1258	MOV R0,A
	048D	37	1259	CPL A
	048E	32 94	1260	JB1 PDBT10
	0490	12 98	1261	JB0 PDBT9
	0492	76 9A	1262	JF1 PDBT9+2
	0494	F9	1263	MOV A,R1
	0495	AA	1264	MOV R2,A
	0496	84 F8	1265	JMP PDR
			1266	;
	0498	76 94	1267	PDBT9: JF1 PDBT10
	049A	84 B0	1268	JMP PDBT6
			1269	;
	049C	FE	1270	PDBT4: MOV A,R6
	049D	D1	1271	XRL A,R1
	049E	96 7F	1272	JNZ PDBT2
	04A0	C4 00	1273	JMP PEND
			1274	;
	04A2	C4 BC	1275	PDBT13: JMP PSTP3
			1276	;
	04A4	B1 FF	1277	PDBT7: MOV R1,#FF
	04A6	A4 2F	1278	JMP CGC
			1279	;

;IF FOW
;R1 2E

;CHARACTER CNT IS START ADDRESS REG. ?

;R1 2F

;TO TRAIL CONTROL

;R1 2E

;TO DATA READ
;PRINT INTRR. SET TO DATA READ

;R0=2A

;IF NOT ELONG.
;IF LEFT
;IF LOW

; TO RAM READ

;TO PRINT END CONTROL

; TO STOP INTR.

;R1 22
;TO CG READ

ADDR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
04E4	FA		1330	MOV A,R2
04E5	03 FA		1331	A,#~FA
04E7	F6 FB		1332	CGG1
04E9	9A FE		1333	P2,#~FE
04EB	FF		1334	A,R7
04EC	37		1335	A
04ED	F6 F0		1336	\$+3
04EF	E7		1337	A
04F0	72 F6		1338	\$+6
04F2	F8		1339	A,R0
04F3	43 80		1340	A,#~80
04F5	A8		1341	ORL RO,A
04F6	A4 4D		1342	JMP CGC3
			1343	
			1344	
			1345	
			1346	
			1347	
			1348	
			1349	
			1350	ORG ~4F8
04F8	F0		1351	MOV A,@RO
04F9	53 F8		1352	ANL A,#~F8
04FB	A0		1353	MOV @RO,A
04FC	FR		1354	A,R3
04FD	37 A2		1355	CPL A
04FE	52 C1		1356	JB2 PDBT13
0500	54 C1		1357	DTR
0502	AF		1358	MOV R7,A
0503	76 23		1359	PDR2
0505	B9 2E		1360	JF1 R1,#~2E
0507	FA		1361	MOV A,R2
0508	D1		1362	XRL A,@R1
0509	C6 16		1363	PDR1
050B	CA		1364	R2
050C	54 C1		1365	DTR
050E	D3 80		1366	A,#~80
0510	96 16		1367	PDR1
0512	23 03		1368	A,#~03
0514	40		1369	MOV A,@RO
0515	A0		1370	@RO,A
0516	FF		1371	A,R7
0517	53 60		1372	A,#~60
0519	96 21		1373	PDR4
051B	F0		1374	MOV A,@RO
051C	F2 20		1375	\$+4
051E	43 0C		1376	A,#~0C
0520	A0		1377	@RO,A
0521	A4 8B		1378	JMP CGC5
			1379	

ET-5230(ML800) PROGRAM (MBO) *1-2*

K1 META CROSS ASSEMBLER
2.0 PROCESSOR : 1-8048

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
	055F	FO	1430	;
	0560	D2 64	1431	ENDIF
	0562	B9 BF	1432	;
	0564	E7	1433	MOV A,000
	0565	43 81	1434	S+4
	0567	A0	1435	R1,#~BF
	0568	F9	1436	A
	0569	8A 0F	1437	A,#~81
	056B	R8 77	1438	000,A
	056D	20	1439	A,R1
	056E	32 87	1440	P2,#~OF
	0570	54 F1	1441	RO,#~77
	0572	42	1442	A,000
	0573	03 02	1443	CGC10
	0575	E6 72	1444	SPPMS2
	0577	89 7F	1445	A,T
	0579	8A FF	1446	A,#~02
	057B	04 33	1447	CGC8
	057D	FO	1448	P1,#~7F
	057E	12 82	1449	P2,#~FF
	0580	89 DF	1450	IEND
	0582	77	1451	;
	0583	43 C0	1452	CGC9:
	0585	A4 67	1453	MOV A,000
	0587	54 F1	1454	S+4
	0589	04 33	1455	R1,#~DF
	058B	23 FF	1456	A
	058D	A4 69	1457	A,#~C0
	058F	17	1458	CGC4
	0590	52 88	1459	SPPMS2
	0592	FF	1460	IEND
	0593	47	1461	A,#~FF
	0594	A4 32	1462	CGC7
	0596	FF	1463	;
	0597	53 1F	1464	CGC1:
	0599	43 10	1465	INC A
	059B	A4 48	1466	CGC5
			1467	JB2
			1468	MOV A,R7
			1469	A
			1470	SWAP
			1471	JMP
			1472	;
			1473	CGC2:
			1474	MOV A,R7
			1475	ANL A,#~1F
			1476	ORL A,#~10
			1477	JMP CGC3-2
			1478	;
			1479	;

***** LF NUMBER SET (FF, VFU CONTROL) SUBROUTINE *****

OKI META CROSS ASSEMBLER
V2.0 PROCESSOR : 1-8048

ET-5230(ML82A) PROGRAM (MBO) *1-2*

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
	0612	64 65	1580	PREFND
	0614	R9 24	1581	R1, #~24
	0616	F0	1582	A, @RO
	0617	72 A0	1583	PEND11
	0619	F1	1584	A, @R1
	061A	F2 88	1585	PEND9
	061C	R9 2E	1586	R1, #~2E
	061E	R8 26	1587	RO, #~26
	0620	76 5F	1588	PEND8
	0622	18	1589	RO
	0623	F0	1590	A, @RO
			1591	;
		(0001)	1592	IF
	0624	03 03	1593	ML82A
	0626	R6 2A	1594	ADD A, #~03
	0628	03 02	1595	JFO \$+4
			1596	ADD A, #~02
			1597	ELSE
			1598	;
			1599	ADD A, #~06
			1600	JFO \$+4
			1601	ADD A, #~04
			1602	;
			1603	ENDIF
			1604	;
	062A	37	1605	CPL A
	062B	61	1606	A, @R1
	062C	E6 4B	1607	ADD
	062E	R8 2A	1608	JNC
	0630	F0	1609	PEND4: MOV RO, #~2A
	0631	43 20	1610	MOV A, @RO
	0633	A0	1611	A, #~20
			1612	@RO, A
	0634	FF	1613	;
	0635	R8 OF	1614	TRL: MOV A, R7
	0637	96 44	1615	MOV R3, #~OF
	0639	BB 1E	1616	JNZ TRL4
			1617	MOV R3, #~1E
			1618	;
		(0001)	1619	IF ML82A
	063B	BF 11	1620	;
	063D	B6 46	1621	TRL1: MOV R7, #~11
	063F	RF 06	1622	JFO TRL3
			1623	MOV R7, #~06
			1624	;
	0641	00	1625	TRL2: NOP
	0642	00	1626	NOP
	0643	00	1627	NOP
			1628	;
			1629	ELSE
			1630	;

; RO 2A
 ; IF UPPER LINE OF GRAPHIC
 ; IF NEXT BLOCK IS FUNCTION ONLY
 ; IF FOW
 ; ET-5230(ML82A)
 ; IDLE SPACE SET
 ; IF 16.5 CPI
 ; ET-8220A(ML83A)
 ; IF L <= (NEXT R + 6)
 ; NEXT LINE IS REVERSE
 ; TRAIL INTR. SET
 ; PRINT STOP INTR. SET
 ; ET-5230(ML82A)
 ; 10 CPI, RISE UP NO.
 ; 16.5 CPI, RISE UP NO.
 ; ET-8220A(ML83A)

LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
0644	A4 8B	+1 1630	TRL1: MOV R7,#~23
0646	76 41	+1 1631	JFO TRL3
0648	CF	+1 1632	MOV R7,#~0C
0649	C4 41	+1 1633	;
		+1 1634	TRL2: NOP
		+1 1635	NOP
		+1 1636	NOP
		+1 1637	;
		+1 1638	ENDIF
		+1 1639	;
0644	A4 8B	1640	TRL4: JMP CGC5
0646	76 41	1641	TRL3: JF1 TRL2
0648	CF	1642	DEC R7
0649	C4 41	1643	JMP TRL2
		1644	;
		1645	;
064B	C8	1646	PEND5: RO
064C	F0	1647	MOV A,@RO
064D	37	1648	CPL A
064E	61	1649	ADD A,@R1
064F	17	1650	INC A
0650	F6 59	1651	JC PEND7
0652	B8 2A	1652	MOV RO,#~2A
0654	F0	1653	MOV A,@RO
0655	53 DF	1654	ANL A,#~DF
0657	C4 33	1655	JMP PEND2+2
		1656	;
0659	B8 2F	1657	PEND7: MOV RO,#~2F
065B	B9 27	1658	MOV R1,#~27
065D	C4 72	1659	JMP PEND12
		1660	;
065F	19	1661	PEND8: INC R1
0660	F1	1662	MOV A,@R1
	(0001)	1663	;
		1664	;
		1665	IF ML82A
0661	03 03	+1 1666	ADD A,#~03
0663	B6 67	+1 1667	JFO \$+4
0665	03 02	+1 1668	ADD A,#~02
		+1 1669	;
		+1 1670	ELSE
		+1 1671	;
		+1 1672	ADD A,#~06
		+1 1673	JFO \$+4
		+1 1674	ADD A,#~04
		+1 1675	;
		+1 1676	ENDIF
		+1 1677	;
0667	37	1678	CPL A
0668	60	1679	ADD A,@RO

LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
0644	A4 8B	+1 1630	TRL1: MOV R7,#~23
0646	76 41	+1 1631	JFO TRL3
0648	CF	+1 1632	MOV R7,#~0C
0649	C4 41	+1 1633	;
		+1 1634	TRL2: NOP
		+1 1635	NOP
		+1 1636	NOP
		+1 1637	;
		+1 1638	ENDIF
		+1 1639	;
0644	A4 8B	1640	TRL4: JMP CGC5
0646	76 41	1641	TRL3: JF1 TRL2
0648	CF	1642	DEC R7
0649	C4 41	1643	JMP TRL2
		1644	;
		1645	;
064B	C8	1646	PEND5: RO
064C	F0	1647	MOV A,@RO
064D	37	1648	CPL A
064E	61	1649	ADD A,@R1
064F	17	1650	INC A
0650	F6 59	1651	JC PEND7
0652	B8 2A	1652	MOV RO,#~2A
0654	F0	1653	MOV A,@RO
0655	53 DF	1654	ANL A,#~DF
0657	C4 33	1655	JMP PEND2+2
		1656	;
0659	B8 2F	1657	PEND7: MOV RO,#~2F
065B	B9 27	1658	MOV R1,#~27
065D	C4 72	1659	JMP PEND12
		1660	;
065F	19	1661	PEND8: INC R1
0660	F1	1662	MOV A,@R1
	(0001)	1663	;
		1664	;
		1665	IF ML82A
0661	03 03	+1 1666	ADD A,#~03
0663	B6 67	+1 1667	JFO \$+4
0665	03 02	+1 1668	ADD A,#~02
		+1 1669	;
		+1 1670	ELSE
		+1 1671	;
		+1 1672	ADD A,#~06
		+1 1673	JFO \$+4
		+1 1674	ADD A,#~04
		+1 1675	;
		+1 1676	ENDIF
		+1 1677	;
0667	37	1678	CPL A
0668	60	1679	ADD A,@RO

LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
0644	A4 8B	+1 1630	TRL1: MOV R7,#~23
0646	76 41	+1 1631	JFO TRL3
0648	CF	+1 1632	MOV R7,#~0C
0649	C4 41	+1 1633	;
		+1 1634	TRL2: NOP
		+1 1635	NOP
		+1 1636	NOP
		+1 1637	;
		+1 1638	ENDIF
		+1 1639	;
0644	A4 8B	1640	TRL4: JMP CGC5
0646	76 41	1641	TRL3: JF1 TRL2
0648	CF	1642	DEC R7
0649	C4 41	1643	JMP TRL2
		1644	;
		1645	;
064B	C8	1646	PEND5: RO
064C	F0	1647	MOV A,@RO
064D	37	1648	CPL A
064E	61	1649	ADD A,@R1
064F	17	1650	INC A
0650	F6 59	1651	JC PEND7
0652	B8 2A	1652	MOV RO,#~2A
0654	F0	1653	MOV A,@RO
0655	53 DF	1654	ANL A,#~DF
0657	C4 33	1655	JMP PEND2+2
		1656	;
0659	B8 2F	1657	PEND7: MOV RO,#~2F
065B	B9 27	1658	MOV R1,#~27
065D	C4 72	1659	JMP PEND12
		1660	;
065F	19	1661	PEND8: INC R1
0660	F1	1662	MOV A,@R1
	(0001)	1663	;
		1664	;
		1665	IF ML82A
0661	03 03	+1 1666	ADD A,#~03
0663	B6 67	+1 1667	JFO \$+4
0665	03 02	+1 1668	ADD A,#~02
		+1 1669	;
		+1 1670	ELSE
		+1 1671	;
		+1 1672	ADD A,#~06
		+1 1673	JFO \$+4
		+1 1674	ADD A,#~04
		+1 1675	;
		+1 1676	ENDIF
		+1 1677	;
0667	37	1678	CPL A
0668	60	1679	ADD A,@RO

ET-5230(ML82A) PROGRAM (MBO) #1-2*

JK1 META CROSS ASSEMBLER
V2.0
PROCESSOR : I-8048

ERR	LOC	OBJECT CODE	SEO	SOURCE STATEMENT
	0669	F6 52	1680	JC PEND6
	0668	18	1681	INC RO
	066C	F1	1682	MOV A,@R1
	066D	37	1683	CPL A
	066E	17	1684	INC A
	066F	60	1685	ADD A,@R0
	0670	E6 99	1686	PEND3
	0672	AF	1687	R7,A
	0673	C8	1688	RO
	0674	F0	1689	A,@R0
	0675	37	1690	A
	0676	03 10	1691	A,#~10
	0678	61	1692	A,@R1
	0679	37	1693	A
	067A	6F	1694	A,R7
	067B	E6 99	1695	PEND3
	067D	B8 2A	1696	RO,#~2A
	067F	F0	1697	A,@R0
	0680	92 99	1698	PEND3
			1699	ML82A
		(0001)	1700	IF
	0682	BF 06	1701	MOV R7,#~06
			1702	ELSE
			1703	MOV R7,#~0B
			1704	ENDIF
			1705	INC RO
			1706	@R0
			1707	PEND3
			1708	\$+6
	0684	18	1709	R1,#~2E
	0685	10	1710	A,@R1
	0686	C4 99	1711	MOV R7,A
			1712	MOV RO,#~2A
	0688	76 8E	1713	A,@R0
	068A	B9 2E	1714	A,#~40
	068C	F1	1715	PEND2
	068D	AF	1716	PEND3
	068E	B8 2A	1717	PEND13
	0690	F0	1718	PEND4
	0691	43 40	1719	PEND6
	0693	C4 31	1720	@R1
	0695	F2 99	1721	PEND3
	0697	92 0E	1722	PEND3
	0699	76 2E	1723	PEND3
	069B	C4 52	1724	PEND3
			1725	INC @R1
	069D	11	1726	PEND3
	069E	C4 99	1727	JMP
			1728	;
			1729	;

;IF (NEXT L) > R + 6
 ;RO 27
 ;IF R >= NEXT R
 ; RO=26
 ; IF NOT TURN-BACK
 ; IF GRAPH 2ND LINE
 ; ET-5230(ML82A)
 ; ET-8220A(ML83A)
 ; RO=2B
 ; TURN=BACK SET
 ; STSRT @
 ; IF NO PR LINE
 ; IF 2ND LINE OF GRAPHIC
 ; IF FOW
 ; TURN BACK 2 SET

ET-5230(MLR2A) PROGRAM (MBO) *1-2*

OKI META CROSS ASSEMBLER
V2.0 PROCESSOR : I-8048

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
	06A0	F1	1730	PEND11: MOV A,@R1
	06A1	D2 99	1731	PEND3
	06A3	23 F7	1732	MOV A,#~F7
	06A5	50	1733	ANL A,@R0
	06A6	A0	1734	MOV @R0,A
	06A7	C4 19	1735	JMP PEND14
			1736	
			1737	
			1738	
			1739	
			1740	ORG ^6A9
			1741	
	06A9	B6 C0	1742	PSTP15
	06AB	FC	1743	MOV A,R4
	06AC	D3 01	1744	A,#~01
	06AE	96 BC	1745	PSTP3
	06B0	EF BC	1746	R7,PSTP3
	06B2	BF 02	1747	R7,#~02
	06B4	FB	1748	MOV A,R3
	06B5	43 80	1749	A,#~80
	06B7	AB	1750	R3,A
	06B8	23 80	1751	MOV A,#~80
	06BA	E4 A4	1752	LFED11
			1753	
	06BC	54 F1	1754	PSTP3: CALL SPPMS2
	06BE	04 33	1755	JMP IREND
			1756	
			1757	
			1758	
			1759	
			1760	
	06C0	FF	1761	PSTP15: MOV A,R7
	06C1	03 F3	1762	A,#~F3
	06C3	F6 B0	1763	PSTP1
	06C5	23 FF	1764	MOV A,#~FF
	06C7	76 CB	1765	S+4
	06C9	23 0B	1766	JF1
	06CB	6F	1767	MOV A,#~0B
	06CC	37	1768	ADD A,R7
	06CD	E3	1769	CPL A
	06CE	62	1770	MOV A,@A
	06CF	55	1771	T,A
	06D0	C4 B0	1772	STRT T
			1773	JMP PSTP1
			1774	
			1775	
			1776	
			1777	
			1778	
			1779	

; R1=24
 ; IF NEXT IS GRAPHIC LINE
 ; R0=2A
 ; GRAPH SET
 ***** SPACE PULSE MOTOR STOP CONTROL *****
 ; IF 10 CPI
 ; IF R4 IS NOT 5
 ; TMG INTR. SET
 ; 7.0 MS SET
 ; 16.5 CPI SP PM PHASE CHANGE
 ***** SOFT STOP *****
 ; IF REV
 ; TABLE READ
 ***** HOME DETECTION *****

ET-5230(ML82A) PROGRAM (MB0) *1-2*

GRI META CROSS ASSEMBLER
V2.0 PROCESSOR : I-8048

ERR	LOC	OBJECT CODE	SEO	SOURCE STATEMENT
			1780	;
			1781	;
		(06D2)	1782	ORG ^6D2
			1783	;
		A5	1784	CRST: CLR F1
			1785	;
		(0001)	1786	IF ML82A
		BA 26	1787	MOV R2, #~26
			1788	;
			1789	ELSE
			1790	;
			1791	MOV R2, #~20
			1792	;
			1793	ENDIF
			1794	;
		46 D8	1795	CR4: JNT1
		85	1796	CPL F1
		CB	1797	DEC R3
			1798	;
		76 E1	1799	CR2: JF1
		46 E3	1800	CR3: JNT1
		EA E3	1801	DJNZ R2, CR3
		EA A8	1802	JMP LFED6
		46 E9	1803	CR7: JNT1
		74 07	1804	CALL SPPM
		23 E4	1805	CR3: MOV A, #~E4
		EA A4	1806	JMP LFED11
		EA E3	1807	;
		1B	1808	CR5: DJNZ R2, CR3
		EA A3	1809	INC R3
			1810	JMP LFED9
			1811	;
			1812	***** PAPER END ADDITION *****
			1813	;
		8A 10	1814	PERD2: ORL P2, #~10
		0A	1815	IN A, P2
		92 F5	1816	JB4 PERD4
		24 5D	1817	JMP PERD6
			1818	;
			1819	;
		24 78	1820	PERD4: JMP NPE
			1821	;
			1822	;
			1823	;
			1824	;
			1825	***** CONTROL AFTER SP PM STOP *****
			1826	;
		(06F7)	1827	ORG ^6F7
			1828	;
		E4 C6	1829	PSTP11: JMP PED

ET-5230(ML82A) PROGRAM (MBO) *1-2*

KI META CROSS ASSEMBLER
2.0 PROCESSOR : I-8048

ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
0743	43 90		1880	PSTP13: ORL A,#~90
0745	53 F7		1881	ANL A,#~F7
0747	E4 3C		1882	JMP PSTP10
0749	53 FC		1883	;
074B	E4 3C		1884	PSTP14: ANL A,#~FC
074D	A5		1885	JMP PSTP10
074E	F1		1886	;
074F	B2 52		1887	PSTP8: CLR F1
0751	B5		1888	MOV A,@R1
0752	74 49		1889	S+3
			1890	CPL F1
			1891	CALL CLMS
			1892	;
			1893	IF ML82A
			1894	;
			1895	NOP
			1896	NOP
			1897	;
			1898	ELSE
			1899	;
			1900	NOP
			1901	NOP
			1902	;
			1903	ENDIF
			1904	;
0756	B8 2C		1905	MOV RO,#~2C
0758	F0		1906	MOV A,@RO
0759	37		1907	A
075A	12 5E		1908	S+4
075C	74 49		1909	CALL CLMS
075E	B9 2A		1910	MOV R1,#~2A
0760	F1		1911	MOV A,@R1
0761	D2 68		1912	S+7
0763	19		1913	R1
0764	F1		1914	A,@R1
0765	D3 20		1915	A,#~20
0767	A1		1916	@R1,A
0768	E4 2F		1917	PSTP18
			1918	;
			1919	;
			1920	;
			1921	;
			1922	;
			1923	;
			1924	;
			1925	;
			1926	;
			1927	;
			1928	;
076A	C8		1929	LF0: DEC R3
				ORG ~76A
				;LF INTR. SET

* LINE FEED CONTROL *

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ERR	LOC	OBJECT CODE	SEQ	SOURCE STATEMENT
	07AE	B2 C6	1980	PED
	07B0	43 20	1981	A,*~20
	07B2	A1	1982	@R1,A
	07B3	27	1983	A
	07B4	B8 2E	1984	RO,*~2E
	07B6	A0	1985	@RO,A
	07B7	37	1986	A
	07B8	18	1987	RO
	07B9	A0	1988	@RO,A
	07BA	B8 2C	1989	RO,*~2C
	07BC	E4 98	1990	LFED3
			1991	JMP
			1992	
	07BE	23 RF	1993	MOV A,*~BF
	07C0	E4 6F	1994	JMP LF1
			1995	
	07C2	BC 00	1996	MOV R4,*~00
	07C4	E4 84	1997	JMP LF2
			1998	
			1999	
			2000	
			2001	
			2002	
			2003	***** CR END *****
			2004	
		(07C6)	2005	ORG ~7C6
	07C6	86 E7	2006	
	07C8	B8 24	2007	JNI TMG3
	07CA	F0	2008	MOV RO,*~24
	07CB	RF 64	2009	MOV A,@RO
	07CD	D2 D1	2010	MOV R7,*~64
	07CF	RF 32	2011	PED1
	07D1	RB C0	2012	JB6 R7,*~32
	07D3	E4 A3	2013	MOV R3,*~CO
			2014	LFED9
			2015	JMP
			2016	
			2017	
			2018	***** TIMER INTERRUPT *****
			2019	
	07D5	B8 31	2020	TMG:
	07D7	F0	2021	MOV RO,*~31
	07D8	96 E9	2022	MOV A,@RO
	07DA	F8	2023	JNZ TMG4
	07DB	D2 E5	2024	MOV A,R3
	07DD	74 25	2025	JB6 TMG2
	07DF	EF E3	2026	CALL SPOVD
	07E1	C4 F9	2027	DJNZ R7,TMG1
			2028	JMP PSTP4
			2029	
	07F3	C4 B4	2030	TMG1: JMP PSTP2
				; IF WAIT REG SET
				; AFTER CR END
				; SPPM OVER DRIVE SET
				; TO CTL AFTER STOP
				; TO SPPM STOP CTL
				; IF HEAD TEMPERATURE IS HIGH
				; 1.4 SEC SET
				; GRAPH ?
				; 0.7 SEC SET
				; TMG INTR. SET
				; REV SET
				; START ADDRESS =00 SET
				; STOP ADDRESS =FF SET

ET-5230(ML82A) PROGRAM (MBO) *1-2*

OKI META CROSS ASSEMBLER
V2.0 PROCESSOR : I-8048

ERR	LDC	OBJECT CODE	SEQ	SOURCE STATEMENT
	07E5	EF D1	2030	
	07E7	E4 A8	2031	TMG2: DJNZ R7,PED1
	07E9	B9 21	2032	TMG3: JMP LFED6
	07EB	F1	2033	TMG4: MOV R1,#~21
	07EC	12 F6	2034	MOV A,@R1
	07EE	EA A3	2035	TMG5 JBO R2,LFED9
	07F0	B9 2A	2036	MOV R1,#~2A
	07F2	F1	2037	MOV A,@R1
	07F3	43 40	2038	MOV A,#~40
	07F5	A1	2039	ORL @R1,A
	07F6	04 67	2040	MOV TMG6
			2041	TMG5: JMP
			2042	;
			2043	;
			2044	***** INTERRUPT SEQUENCE END *****
	07F8	74 38	2045	LFED: CALL LFRST
	07FA	88 00	2046	MOV R3,#~00
	07FC	35 13A	2047	DIS TCNTI
	07FD	04 33	2048	JMP IREND
			2049	;
			2050	;
			2051	;
			2052	;
			2053	END

; IF INPUT END
; IF WAIT NB CNT NOT ZERO

; NEXT LINE CR SET

***** INTERRUPT SEQUENCE END *****

LFED: CALL LFRST
MOV R3,#~00
DIS TCNTI
JMP IREND

NO.	SYMBOL	VALUE	STATEMENT NUMBERS		
1	BFIN	(01BE)	527	576 *	
2	BLCK	(017C)	450	495 *	
3	CGC	(052F)	1278	1300	
4	CGC1	(058F)	1396	1464 *	1395 *
5	CGC10	(0587)	1443	1459 *	
6	CGC12	(05F8)	1426	1550 *	
7	CGC2	(0596)	1408	1470 *	
8	CGC3	(054D)	1342	1413 *	
9	CGC4	(0567)	1438 *	1457	1473
10	CGC5	(058B)	1011	1301	1553
11	CGC6	(0532)	1397 *	1468	1378
12	CGC7	(0569)	1440 *	1462	
13	CGC8	(0572)	1445 *	1467	
14	CGC9	(057D)	1420	1429	1452 *
15	CGG	(04C7)	1291	1311 *	
16	CGG1	(04EB)	1332	1334 *	
17	CLM	(0435)	1152	1155	1157
18	CLM1	(035A)	975	978 *	
19	CLM2	(0353)	965	973 *	
20	CLM3	(0352)	967	970 *	
21	CLM4	(0448)	1194	1196 *	
22	CLM6	(044F)	1186	1202 *	
23	CLM9	(044D)	1192	1201 *	
24	CLM5	(0349)	964 *	1083	1090
25	CON22	(0250)	160 *	663	
26	CR1	(06D8)	1795	1797 *	
27	CR2	(06D9)	210	1799 *	
28	CR3	(06E3)	1800	1801	
29	CR4	(06D5)	1795 *		1804 *
30	CR5	(06E9)	1803	1808 *	
31	CR7	(06E1)	1799	1803 *	
32	CRLF	(01FB)	573	632	
33	CRLF1	(0206)	639 *		
34	CRLF2	(0228)	642	645	661 *
35	CRLF3	(021F)	653	656 *	
36	CRLF5	(022A)	659	662 *	749
37	CRLF6	(00DE)	158 *	650	
38	CRLF7	(0217)	638	651 *	
39	CRST	(06D2)	211	1784 *	
40	CS1	(0100)	159 *	555	
41	DOT	(040A)	1141	1150 *	
42	DOT1	(041C)	1160 *	1214	
43	DOT2	(041F)	1162 *	1209	
44	DOT3	(0453)	1156	1158	1206 *
45	DRC1	(01B2)	552 *		
46	DRC2	(01B3)	431	554 *	
47	DREC	(01A0)	448	541 *	
48	DSWR	(0045)	260 *	326	
49	DTR	(02C1)	819 *	1357	1365
50	DTR1	(02CD)	821	826 *	1384

NO.	SYMBOL	VALUE	STATEMENT NUMBERS						
51	DTR2	(02D0)	825 *	828 *					
52	DTR3	(02D7)	823	833 *					
53	DTR4	(02D9)	827	834 *					
54	DTW	(02C8)	161 *	592					
55	DWAIT	(0175)	485	488 *					
56	DWT1	(0139)	413	449 *	506				
57	DWT10	(00E3)	394 *	489					
58	DWT11	(0130)	433	444 *					
59	DWT12	(0124)	428	436 *					
60	DWT2	(0119)	429 *	442	498				
61	DWT3	(0100)	412 *	475	493				
62	DWT4	(0167)	479 *	503	713				
63	DWT5	(0168)	482 *	716	718				
64	DWT6	(00EF)	401 *	403	408				
65	DWT7	(014C)	415	461 *					
66	DWT8	(0141)	454 *	512					
67	DWT9	(0150)	463 *	529					
68	FFKY	(0196)	524 *						
69	IBED1	(022D)	609	670 *	680	697	701	704	
70	IBED2	(0238)	676	678 *					
71	IBED3	(025E)	660	685	706 *				
72	IBED4	(0245)	677	684	687 *				
73	IBED5	(026C)	710	714 *					
74	IBED6	(0271)	712	717 *					
75	IBED7	(024D)	694 *	745					
76	IBED8	(0240)	673	683 *					
77	IBED9	(0255)	696	699 *					
78	IED1	(01D1)	590 *	597					
79	IED2	(01DD)	586	598 *					
80	IED3	(01E2)	602 *	661					
81	IED4	(01EC)	605	609 *					
82	INTR1	(001E)	205	209 *					
83	INTR2	(0022)	209	211 *					
84	INTR3	(0024)	204	212 *					
85	INTR5	(0026)	206	214 *					
86	INTR6	(002A)	214	216 *					
87	INTR7	(002E)	216	218 *					
88	INTR8	(0030)	207	220 *					
89	IREND	(0033)	221	229 *					
90	IRT	(0083)	182	306 *	1092	1450	1460	1755	2048
91	IRT1	(00AA)	328	338 *					
92	IRT2	(00C3)	355 *	361					
93	IRT3	(00D7)	358	367 *					
94	IRT4	(00C9)	359 *						
95	LF	(07BE)	215	1993 *					
96	LFO	(076A)	218	1929 *					
97	LF1	(076F)	1932 *	1994					
98	LF2	(0784)	1946 *	1997					
99	LF3	(07C2)	1942	1944	1996 *				
100	LFED	(07F8)	217	2045 *					

NO.	SYMBOL	VALUE	STATEMENT NUMBERS													
101	LFED1	(078A)	1867	1955 *												
102	LFED11	(07A)	1752	1806	1971 *											
103	LFED3	(0798)	1964 *	1990												
104	LFED6	(07A8)	1802	1960	1975 *	2031										
105	LFED7	(07A6)	1937	1949	1973 *											
106	LFED8	(07AC)	1957	1978 *												
107	LFED9	(07A3)	1810	1868	1970 *	1976	2014	2035								
108	LF1N	(0186)	570 *													
109	LFKY	(0191)	467	521 *												
110	LFKY2	(019C)	522	528 *												
111	LFQVD	(0337)	754	765	936 *	1845	1936									
112	LFPM	(0329)	923 *	1935												
113	LFPM1	(0332)	928 *	937	946											
114	LFRT	(0338)	944 *	1086	1934	2045										
115	LFST	(059D)	752	1482 *	1844											
116	LFST1	(05A8)	1487	1492 *	1520	1542	1546									
117	LFST10	(05F0)	1521	1541 *												
118	LFST12	(05F4)	1524	1544 *												
119	LFST14	(05DF)	1529 *													
120	LFST2	(05C4)	1509 *	1531	1536	1539										
121	LFST3	(05CD)	1505	1508	1516 *											
122	LFST4	(05D6)	1522 *	1541												
123	LFST5	(05DC)	1514	1527 *												
124	LFST6	(058A)	1495	1502 *												
125	LFST7	(05E5)	1528	1533 *												
126	LFST8	(05DB)	1490	1525 *												
127	LFSTP	(0358)	986 *	1930	1938											
128	ML82A	(0001)	142 *	769	784	1016	1027	1106	1592	1618	1664	1700				
			1786	1893												
129	MODS	(01EE)	617 *	729	1857											
130	MS1	(01F4)	620 *	624												
131	NPE	(0178)	477	492 *	1820											
132	OC	(0000)	148 *	1425	1550											
133	PBRD	(0341)	923	936	944	951 *										
134	PCPW	(003D)	250 *	483	546											
135	PDBT	(0462)	1201	1223 *												
136	PDBT1	(046E)	1230 *													
137	PDBT10	(0494)	1260	1263 *	1267											
138	PDBT11	(0465)	1225 *													
139	PDBT12	(04A8)	1225	1281 *												
140	PDBT13	(04A2)	1275 *	1285												
141	PDBT15	(04C5)	1297	1301 *	1356											
142	PDBT16	(04C3)	1298	1300 *												
143	PDBT2	(047F)	1246 *	1272												
144	PDBT3	(0482)	1239	1249 *												
145	PDBT4	(049C)	1231	1270 *												
146	PDBT5	(04A0)	1236	1273 *												
147	PDBT6	(04B0)	1268	1288 *												
148	PDBT7	(04A4)	1277 *	1293	1295											
149	PDBT8	(047C)	1229	1242 *												

ET-5230(NL82A) PROGRAM (MBO) *1-2*

JKI META CROSS ASSEMBLER
V2.0 PROCESSOR : 1-8048

NO.	SYMBOL	VALUE	STATEMENT NUMBERS									
180	PDBT9	(0498)	1261	1262	1267 *							
181	PDR	(04F8)	1265	1351 *								
182	PDR1	(0516)	1363	1367	1371 *							
183	PDR2	(0523)	1359	1381 *	1382							
184	PDR4	(0521)	1373	1378 *								
185	PDR5	(0514)	1369 *	1387								
186	PED	(07C6)	1829	1980	2007 *							
187	PED1	(07D1)	2011	2013 *	2030							
188	PEND	(0600)	1273	1569 *								
189	PEND1	(069D)	1576	1727 *								
190	PEND10	(0695)	1573	1722 *								
191	PEND11	(06A0)	1583	1730 *								
192	PEND12	(0672)	1659	1687 *								
193	PEND13	(060E)	1577 *	1723								
194	PEND14	(0619)	1584 *	1735								
195	PEND15	(0614)	996	1581 *								
196	PEND2	(0631)	1610 *	1655	1721							
197	PEND3	(0699)	1686	1695	1698							
198	PEND4	(062E)	1624	1608 *	1724							
199	PEND5	(064B)	1607	1646 *								
170	PEND6	(0652)	1652 *	1680	1725							
171	PEND7	(0659)	1651	1657 *								
172	PEND8	(065F)	1588	1661 *	1712							
173	PEND9	(0688)	998	1585								
174	PERD	(0158)	452	460	1714 *							
175	PERD2	(06EE)	471	1814 *	471 *							
176	PERD4	(06F5)	1816	1820 *								
177	PERD6	(015D)	472 *	1817								
178	PREEND	(0365)	995 *	1580								
179	PREND1	(0369)	995	997 *								
180	PREND2	(036D)	997	1000 *								
181	PREND3	(0368)	998 *	1000	1003							
182	PREND4	(038A)	1006	1027 *								
183	PREND5	(0382)	1008	1013 *								
184	PREND6	(0378)	1008 *	1036								
185	PRPM1	(0406)	1140	1144 *								
186	PRPM2	(0400)	1140 *	1143								
187	PRSET	(02A5)	739	764 *								
188	PRT	(0404)	1097	1143 *								
189	PRT1	(0388)	1078 *	1096								
190	PRT2	(03D2)	1074	1093 *								
191	PRT3	(03CC)	1090 *	1091								
192	PRT4	(03D0)	1084	1088								
193	PRT6	(03DA)	1073	1097 *	1092 *							
194	PRT8	(039C)	208	1059 *								
195	PST	(0275)	693	726 *								
196	PST1	(0291)	745 *	748								
197	PST2	(0293)	746 *									
198	PST3	(029A)	734									
199	PST4	(028B)	740 *	752 *								
				757								

OKI META CROSS ASSEMBLER
V2.0 PROCESSOR : 1-8048

EL5230(MLR2) PROGRAM (MBO) *1-2*

NO.	SYMBOL	VALUE	STATEMENT NUMBERS
200	PST5	(0288)	738 *
201	PSTP	(06A9)	1203
202	PSTP1	(06B0)	1746 *
203	PSTP10	(073C)	1874 *
204	PSTP11	(06F7)	1829 *
205	PSTP12	(073F)	1871
206	PSTP13	(0743)	1872
207	PSTP14	(0749)	1855
208	PSTP15	(06C0)	1742
209	PSTP18	(072F)	1865 *
210	PSTP2	(06B4)	1748 *
211	PSTP3	(068C)	1275
212	PSTP4	(06F9)	292
213	PSTP5	(0728)	1849
214	PSTP6	(070E)	1836
215	PSTP7	(0714)	1835
216	PSTP8	(074D)	1864
217	PSTP9	(0736)	1850
218	SELKY	(018A)	458
219	SELKY1	(0183)	502 *
220	SIF1	(0000)	153 *
221	SIF2	(0062)	154 *
222	SIF3	(0088)	155 *
223	SIF4	(0091)	156 *
224	SIF5	(0095)	157 *
225	SLKY1	(0064)	279
226	SLKYM	(0054)	277 *
227	SPQVD	(0325)	799
228	SPPM	(0307)	854
229	SPPM1	(0311)	893
230	SPPM2	(0314)	898 *
231	SPPM3	(031F)	905 *
232	SPPM51	(02DE)	845 *
233	SPPM52	(02F1)	860 *
234	SPPM53	(02EE)	845
235	TDLY	(02FF)	398
236	TMG	(07D5)	212
237	TMG1	(07E3)	2026
238	TMG2	(07E5)	2024
239	TMG3	(07E7)	2007
240	TMG4	(07E9)	2022
241	TMG5	(07F6)	2034
242	TMG6	(0067)	291 *
243	TRL	(0634)	1243
244	TRL1	(063B)	1572
245	TRL2	(0641)	1624 *
246	TRL3	(0646)	1621
247	TRL4	(0644)	1615
248	VFU12	(0603)	163 *
249	VFU15	(05DA)	162 *
			737
			469
			1643
			1641 *
			1631
			1630 *
			1613 *
			2040
			2040 *
			2032
			2031 *
			2030 *
			2029 *
			2020 *
			411
			877 *
			854 *
			1459
			1226
			1281
			863
			867
			860
			1754
			1754 *
			1847 *
			1843
			1844 *
			1875
			2027
			1746
			1754 *
			1917
			1761 *
			1884 *
			1880 *
			1877 *
			1832
			1878
			1763
			1742 *
			1885
			1772
			1804
			2025
			1145
			1082
			1969
			678
			287 *
			283
			607
			487
			330
			510
			509 *
			1871 *
			1887 *
			1840
			1837
			1860 *
			1831 *
			1745
			2029

OKI META CROSS ASSEMBLER
V2.0 PROCESSOR : I-8048

E1-5230(ML87A) PROGRAM (MBO) *1-2*

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NO.	SYMBOL	VALUE	STATEMENT NUMBERS
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250	VFU16	(0612)	164 * 353
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ASSEMBLY COMPLETE, NO ERRORS

SAP-8080 OBJECT LIST

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0000	9A	FD	B8	00	75	04	83	65	16	0A	D5	B8	28	A0	18	0A
0010	A0	8A	FF	FB	F2	24	D2	1E	B2	26	C6	30	64	9C	12	22
0020	C4	D9	C4	D2	E4	D5	12	2A	E4	BE	32	2E	E4	F8	E4	6A
0030	35	04	33	B8	29	F0	3A	8A	F0	C8	F0	C5	93	B9	03	9A
0040	FD	91	8A	02	83	FA	E7	AA	19	F9	3A	0A	F2	4F	CA	E8
0050	45	8A	FF	83	9A	F2	0A	F2	64	B8	80	E8	5B	0A	F2	64
0060	FD	43	04	AD	8A	0D	83	B0	00	C4	F9	00	00	00	00	00
0070	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
0080	00	00	00	23	3B	90	R9	03	23	1D	91	23	D4	90	C9	81
0090	85	F2	94	95	23	D6	90	23	CC	91	8A	02	B8	08	B9	F7
00A0	BA	FF	14	45	FA	72	AA	F5	04	00	FB	53	7F	B6	B1	43
00B0	80	AB	27	B8	2F	B9	04	A1	19	E8	B7	B9	30	B1	0C	00
00C0	F5	C4	12	37	92	C7	1D	04	D7	9A	F0	0A	F2	C3	BD	02
00D0	B8	75	80	00	18	B0	20	B8	26	B0	FF	8A	FF	D5	B8	7F
00E0	25	55	85	FB	96	E3	C5	23	C7	54	FF	B9	04	9A	F0	0A
00F0	37	F2	EF	37	17	43	R0	3A	E9	EF	8A	FF	23	C7	54	FF
0100	FD	32	39	37	12	4C	9A	FD	B9	03	23	25	35	91	23	05
0110	91	D5	FB	C5	C6	17	25	16	24	8A	02	FD	32	B3	FA	72
0120	30	F5	04	62	B8	08	F0	D3	24	C6	2E	23	FF	62	24	19
0130	9A	FD	B9	00	81	8A	02	12	A0	FE	96	7C	FD	32	58	B8
0140	A0	9A	F2	0A	8A	0F	37	F2	8A	FD	12	5B	B8	24	B9	FF
0150	9A	F0	0A	8A	0F	37	F2	91	F5	A4	DA	C4	EE	00	09	37
0160	F2	00	FC	96	78	99	7F	FD	53	FC	AD	23	1D	14	3D	FA
0170	72	75	F5	04	8B	D5	04	E3	89	80	24	19	FD	52	81	14
0180	54	24	19	FD	12	67	17	AD	24	75	F8	C6	83	E8	8D	24
0190	41	F9	96	9C	B9	88	FD	43	04	AD	24	BE	E9	9C	24	50
01A0	FD	BD	30	12	A7	BD	38	2D	14	3D	B9	01	9A	FD	81	8A
01B0	02	37	AF	F5	24	00	F1	17	B9	88	C6	F8	B9	08	F0	53
01C0	43	49	A0	B9	26	F1	03	07	37	B8	17	60	F6	DD	37	17
01D0	AF	B9	20	F5	54	C8	E5	1E	B8	17	10	EF	D1	B9	27	F0
01E0	07	A1	B9	21	B1	FF	FA	72	EC	F5	04	91	44	2D	B8	27
01F0	B9	2F	BF	04	F0	A1	C8	C9	EF	F4	83	F0	53	03	49	20
0200	55	16	05	64	01	65	83	B9	30	F1	47	41	E7	76	11	77
0210	77	53	0F	A1	9A	FD	B8	02	80	53	F8	41	53	FB	90	43
0220	8C	90	8A	0F	83	B9	30	64	14	74	41	E7	D2	30	43	10
0230	53	30	89	30	41	64	1F	74	41	64	32	74	41	43	40	64
0240	32	B8	02	9A	FD	80	53	30	83	FD	76	53	CD	96	52	BD
0250	0B	CE	83	1D	D3	08	96	5A	AD	1E	83	B8	2C	F0	BF	09
0260	32	64	BF	0C	83	12	69	C4	14	76	6D	C4	88	32	68	B9
0270	2C	F1	D2	68	B8	2F	F0	B6	8A	03	7D	F6	82	10	BB	03
0280	A4	8B	B8	31	B0	01	BA	2A	C4	2E	03	B1	64	78	00	00
0290	00	00	00	00	00	00	00	00	00	00	00	00	00	23	F4	62
02A0	B8	2B	F0	A5	B2	A7	B5	18	F0	85	12	AD	95	FB	32	DA
02B0	B6	D2	E1	76	B8	23	DB	6F	E3	62	55	74	07	74	49	CC
02C0	EF	D0	BB	03	74	38	BC	01	B6	D0	BF	04	74	49	EF	CC
02D0	04	33	23	F3	76	D8	23	E7	64	B8	84	04	E9	E8	E6	E3
02E0	DF	DA	E9	ER	E6	E3	DF	DA	F5	F4	F3	F2	F1	F0	EF	EE
02F0	ED	EC	EB	EA	F6	F5	F4	F3	F2	F1	F0	EF	EE	ED	EC	EB
0300	55	16	05	64	01	65	83	B9	30	F1	47	41	E7	76	11	77
0310	77	53	0F	A1	9A	FD	B8	02	80	53	F8	41	53	FB	90	43
0320	8C	90	8A	0F	83	B9	30	64	14	74	41	E7	D2	30	43	10
0330	53	30	89	30	41	64	1F	74	41	64	32	74	41	43	40	64
0340	32	B8	02	9A	FD	80	53	30	83	FD	76	53	CD	96	52	BD
0350	0B	CE	83	1D	D3	08	96	5A	AD	1E	83	B8	2C	F0	BF	09
0360	32	64	BF	0C	83	12	69	C4	14	76	6D	C4	88	32	68	B9
0370	2C	F1	D2	68	B8	2F	F0	B6	8A	03	7D	F6	82	10	BB	03
0380	A4	8B	B8	31	B0	01	BA	2A	C4	2E	03	B1	64	78	00	00
0390	00	00	00	00	00	00	00	00	00	00	00	00	00	23	F4	62
03A0	B8	2B	F0	A5	B2	A7	B5	18	F0	85	12	AD	95	FB	32	DA
03B0	B6	D2	E1	76	B8	23	DB	6F	E3	62	55	74	07	74	49	CC
03C0	EF	D0	BB	03	74	38	BC	01	B6	D0	BF	04	74	49	EF	CC
03D0	04	33	23	F3	76	D8	23	E7	64	B8	84	04	E9	E8	E6	E3
03E0	DF	DA	E9	ER	E6	E3	DF	DA	F5	F4	F3	F2	F1	F0	EF	EE
03F0	ED	EC	EB	EA	F6	F5	F4	F3	F2	F1	F0	EF	EE	ED	EC	EB

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0400	B6	06	84	0A	EC	00	BC	05	74	07	FR	D3	07	96	35	B8
0410	2A	F0	F2	35	52	53	92	35	32	53	23	F7	6D	F6	35	B8
0420	23	B9	77	F0	39	23	FD	51	A1	3A	B8	02	80	53	F7	90
0430	43	08	90	8A	02	74	49	FB	92	4F	FD	AA	B8	2A	F0	37
0440	32	4D	B9	00	12	48	B9	06	FD	97	67	69	AA	84	62	54
0450	DE	C4	A9	FD	12	35	F0	52	1F	B0	F8	12	5F	B0	F4	20
0460	84	1C	FD	D3	0B	96	A8	54	DE	B8	2A	FB	72	7C	B9	2F
0470	76	9C	C9	FE	17	D1	C6	A0	19	FE	84	82	CF	C4	34	C9
0480	FE	17	D1	96	87	BB	07	D1	A9	F0	D3	01	A0	37	32	94
0490	12	98	76	9A	F9	AA	84	F8	76	94	84	R0	FE	D1	96	7F
04A0	C4	00	C4	BC	B1	FF	A4	2F	54	DE	B8	2A	FR	37	52	A2
04B0	B9	22	9A	F7	F0	52	C7	37	32	A4	FA	C6	A4	FD	12	C5
04C0	76	C3	CA	A4	2F	A4	BB	B1	FF	B9	2C	F1	32	D0	9A	FB
04D0	F0	92	D5	9A	FD	FE	53	03	A8	FF	F2	DE	43	20	77	77
04E0	53	OC	48	A8	FA	03	FA	F6	EB	9A	FE	FF	37	F6	F0	E7
04F0	72	F6	F8	43	80	A8	A4	4D	F0	53	F8	A0	FB	37	52	A2
0500	54	C1	AF	76	23	B9	2E	FA	D1	C6	16	CA	54	C1	D3	80
0510	96	16	23	03	40	A0	FF	53	60	96	21	F0	F2	20	43	OC
0520	A0	A4	8B	D3	80	96	16	1A	54	C1	AF	23	02	A4	14	FA
0530	72	8F	32	36	9A	FE	52	3A	9A	FD	B8	80	12	40	B8	00
0540	FF	F2	45	9A	FB	FA	72	96	FF	53	7F	48	A8	80	B8	22
0550	20	50	18	A0	B9	FF	F2	7D	B8	2C	F0	B8	23	32	7D	F0
0560	D2	64	B9	BF	E7	43	81	A0	F9	8A	0F	B8	77	20	32	87
0570	54	F1	42	03	02	E6	72	89	7F	8A	FF	04	33	F0	12	82
0580	B9	DF	77	43	C0	A4	67	54	F1	04	33	23	FF	A4	69	17
0590	52	8B	FF	47	A4	32	FF	53	1F	43	10	A4	4B	8A	00	B9
05A0	2A	A5	F1	CA	92	AB	1A	1A	72	DB	CA	B8	33	F0	07	96
05B0	8A	B9	2C	F1	53	DF	A1	B9	34	F1	A0	B8	37	F0	C6	CD
05C0	07	A0	96	CD	B9	36	11	F1	A9	F1	F2	DC	A0	1A	1A	B8
05D0	2C	F0	B2	AB	92	F0	B8	32	F0	96	F4	83	17	96	E5	B9
05E0	36	B1	3F	A4	C7	07	B9	2D	D1	96	C4	76	EE	B5	A4	C4
05F0	76	D6	A4	AB	07	A0	A4	AB	00	00	00	00	00	00	00	00
0600	14	72	0E	92	0E	19	F1	32	14	B8	2C	F0	52	14	B4	9D
0610	74	37	BB	3F	B9	2A	F1	D2	28	72	36	53	6F	A1	19	F1
0620	32	49	34	EE	B8	21	B0	F0	B9	2A	F1	19	D1	B2	4D	FB
0630	B2	34	E4	8A	E4	A3	F2	3F	92	43	43	90	A1	E4	28	53
0640	7F	E4	3C	43	90	53	F7	E4	3C	53	FC	E4	3C	A5	F1	B2
0650	52	B5	74	49	00	00	B8	2C	F0	37	12	5E	74	49	B9	2A
0660	F1	D2	68	19	F1	D3	20	A1	E4	2F	CB	74	58	23	8F	62
0670	55	74	3B	74	29	74	37	EF	A6	74	5B	C5	FD	37	12	C2
0680	0A	92	C2	1C	FC	D5	72	8A	EA	A6	B9	2A	F1	D2	AC	B8
0690	2C	F0	F2	A8	F1	53	98	A1	BB	01	F0	BF	06	12	A1	BF
06A0	0C	74	25	27	62	55	04	33	BB	3D	E4	A3	19	F1	B2	C6
06B0	43	20	A1	27	B8	2E	A0	37	18	A0	B8	2C	E4	98	23	BF
06C0	E4	6F	BC	00	E4	84	86	E7	B8	24	F0	BF	64	D2	D1	BF
06D0	32	BB	C0	E4	A3	B8	31	F0	96	E9	F8	D2	E5	74	25	EF
06E0	E3	C4	F9	C4	B4	EF	D1	E4	A8	B9	21	F1	12	F6	EA	A3
06F0	B9	2A	F1	43	40	A1	04	67	74	3B	BB	00	35	04	33	00
0700	0A	92	C2	1C	FC	D5	72	8A	EA	A6	B9	2A	F1	D2	AC	B8
0710	2C	F0	F2	A8	F1	53	98	A1	BB	01	F0	BF	06	12	A1	BF
0720	0C	74	25	27	62	55	04	33	BB	3D	E4	A3	19	F1	B2	C6
0730	43	20	A1	27	B8	2E	A0	37	18	A0	B8	2C	E4	98	23	BF
0740	E4	6F	BC	00	E4	84	86	E7	B8	24	F0	BF	64	D2	D1	BF
0750	32	BB	C0	E4	A3	B8	31	F0	96	E9	F8	D2	E5	74	25	EF
0760	E3	C4	F9	C4	B4	EF	D1	E4	A8	B9	21	F1	12	F6	EA	A3
0770	B9	2A	F1	43	40	A1	04	67	74	3B	BB	00	35	04	33	00